

# Temporal Deductive Verification of Parametrized Systems

**César Sánchez**

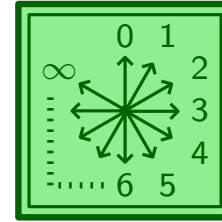
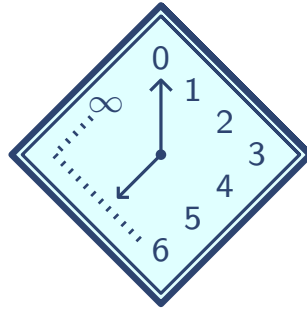
IMDEA Software Institute

Joint work with Alejandro Sanchez

# The Problem

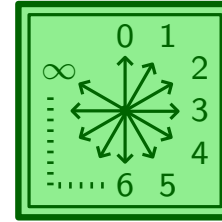
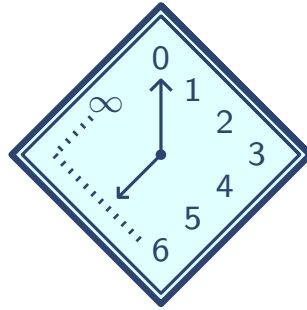
# The Problem

## Temporal Properties

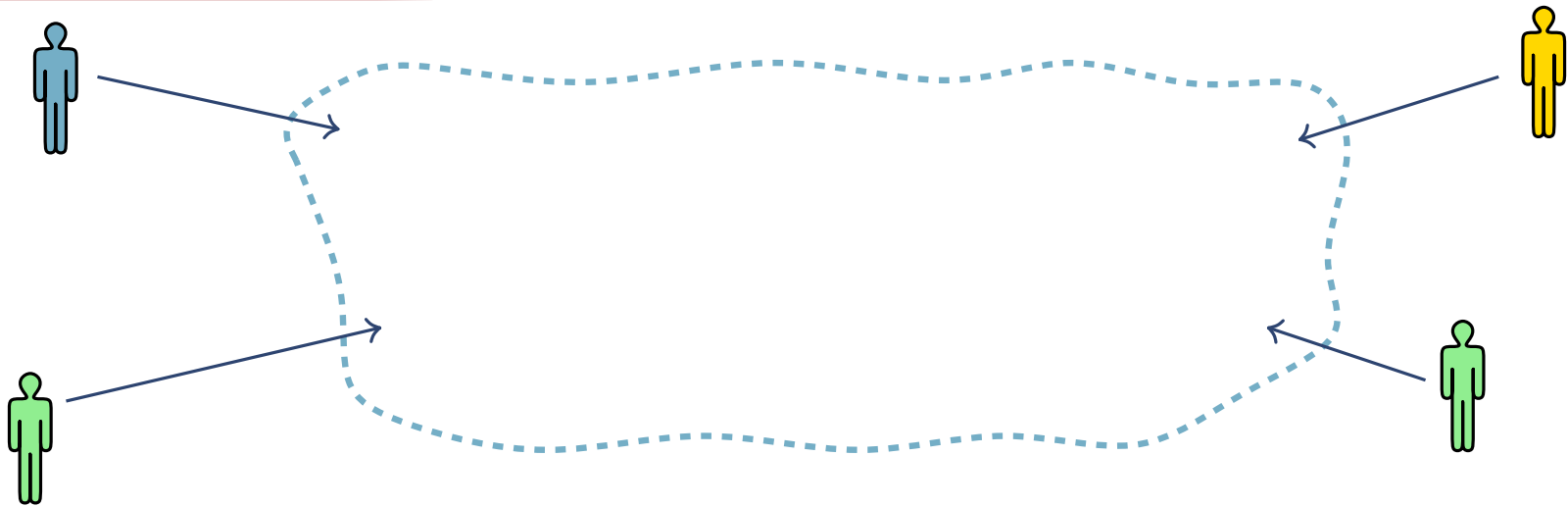


# The Problem

## Temporal Properties

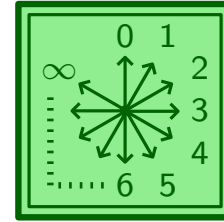
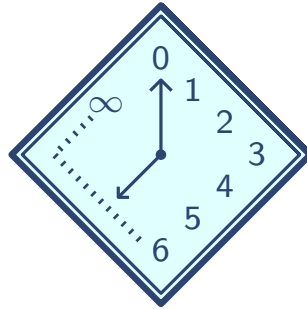


## Concurrent Datatypes

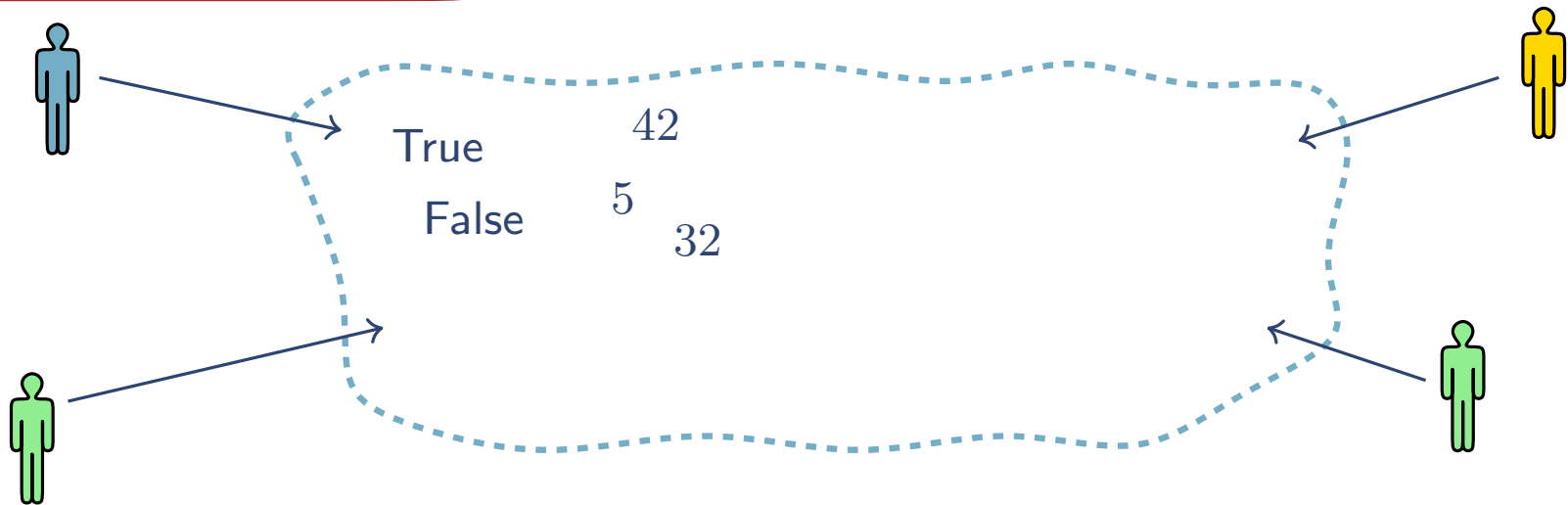


# The Problem

## Temporal Properties

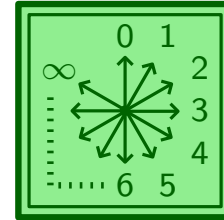
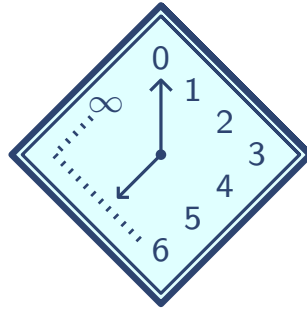


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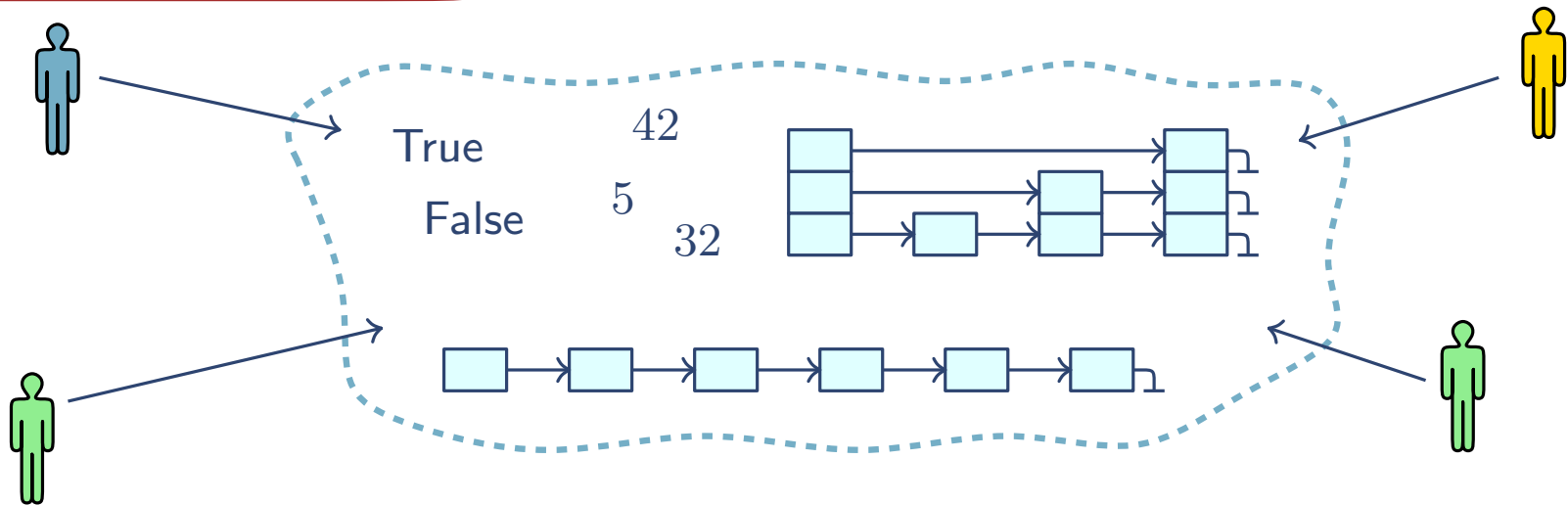


# The Problem

## Temporal Properties

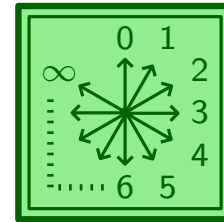
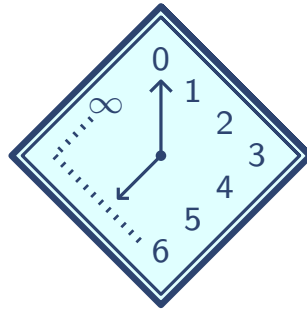


## Concurrent Datatypes

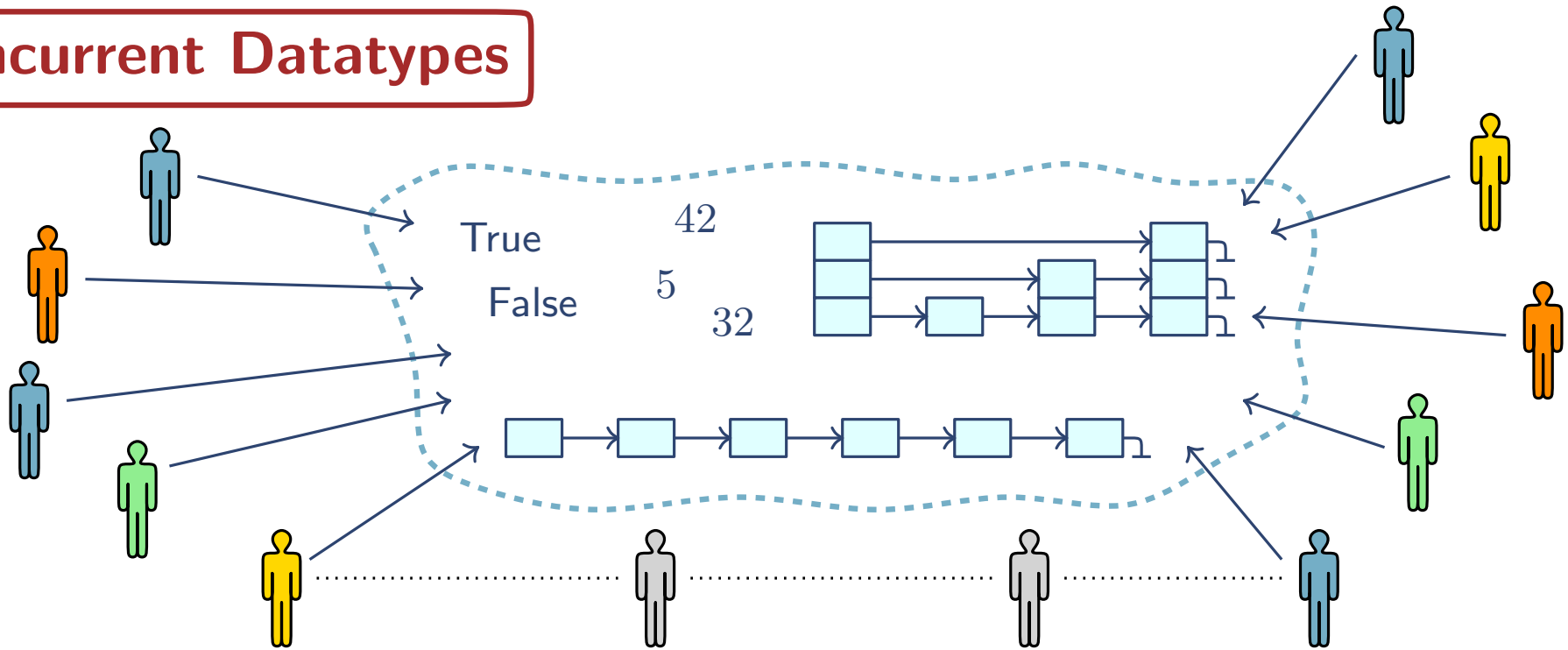


# The Problem

## Temporal Properties



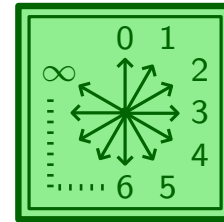
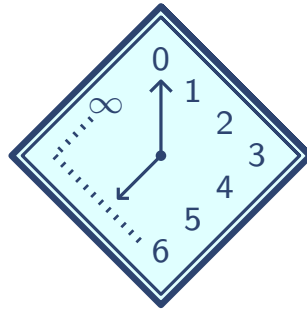
## Concurrent Datatypes



## Parametrized Verification

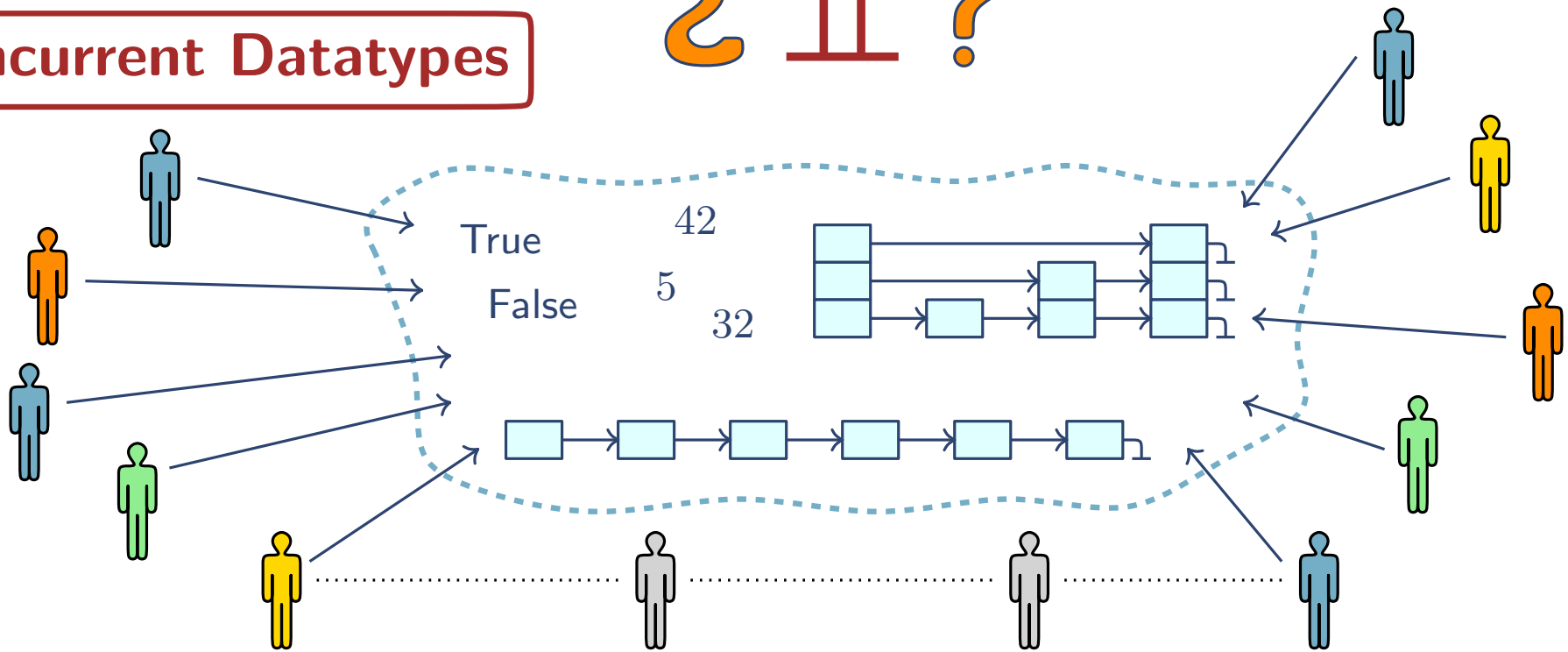
# The Problem

Temporal Properties



$\exists \perp ?$

Concurrent Datatypes



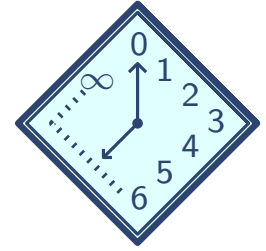
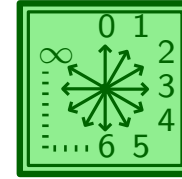
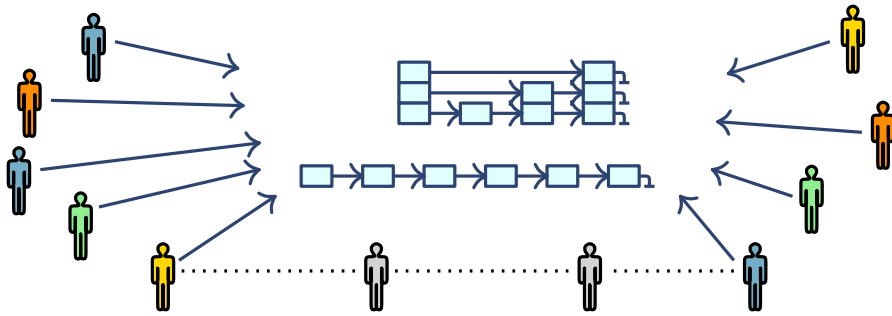
Parametrized Verification



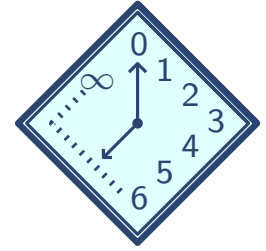
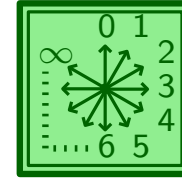
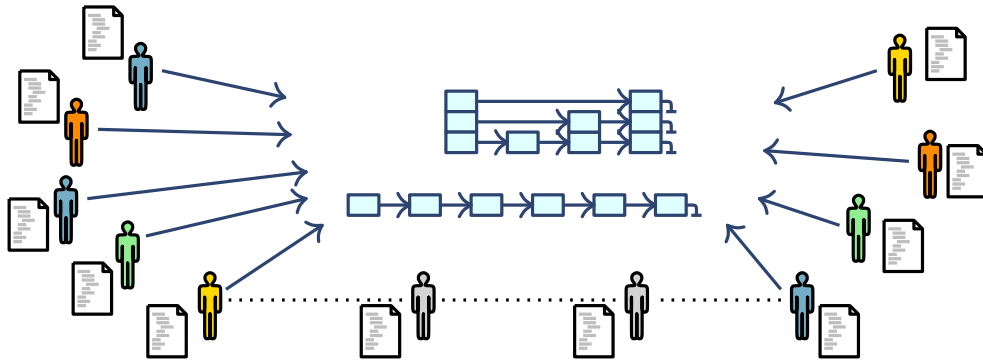
# Motivation

- ▶ Goal: **Formal Verification** (proving system correct)
- ▶ First target: concurrent data structures
- ▶ **Complex data types**: lists, trees, skiplists, hash-maps, etc
- ▶ **Unstructured fine-grained** synchronization methods
- ▶ **Automation** preferable but not mandatory
- ▶ Need to tackle **lock-based** and **lock-free** synchronization
- ▶ **Liveness** is as interesting as **safety** properties
- ▶ Parametrization to enable verification for **all system instances**

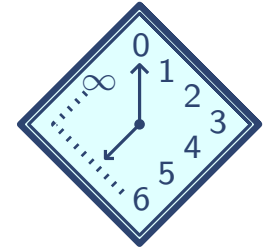
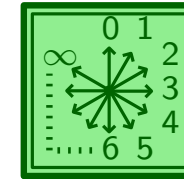
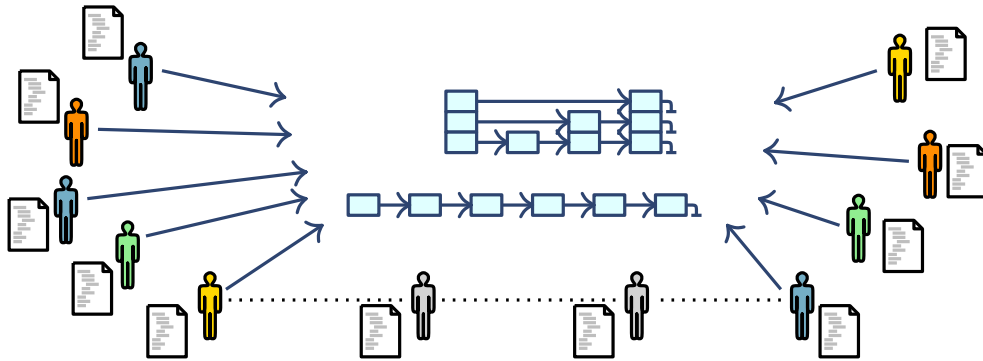
# Our Approach



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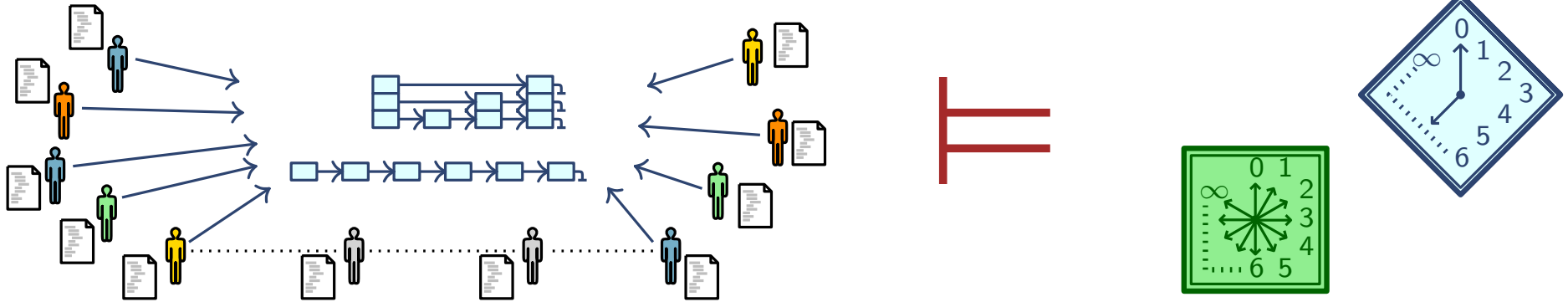


**Parametrized  
Deductive  
Methods**

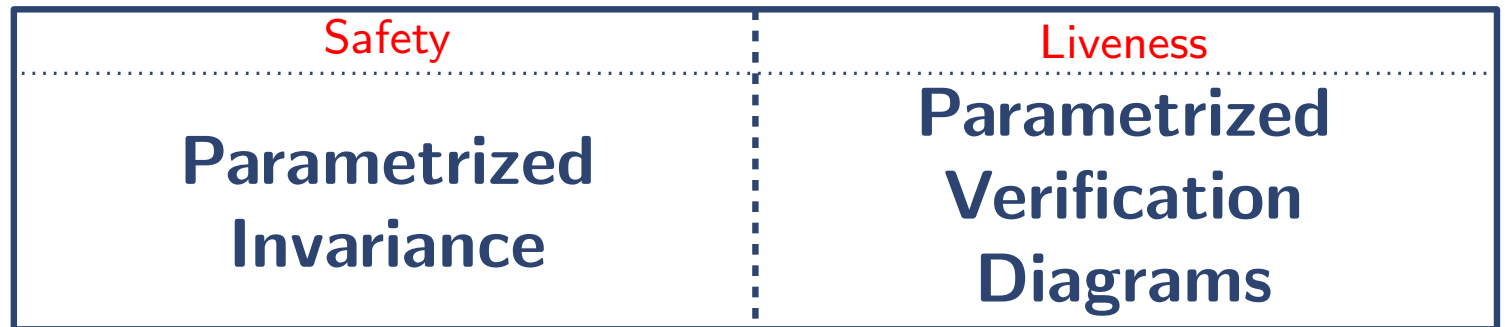
Safety

Liveness

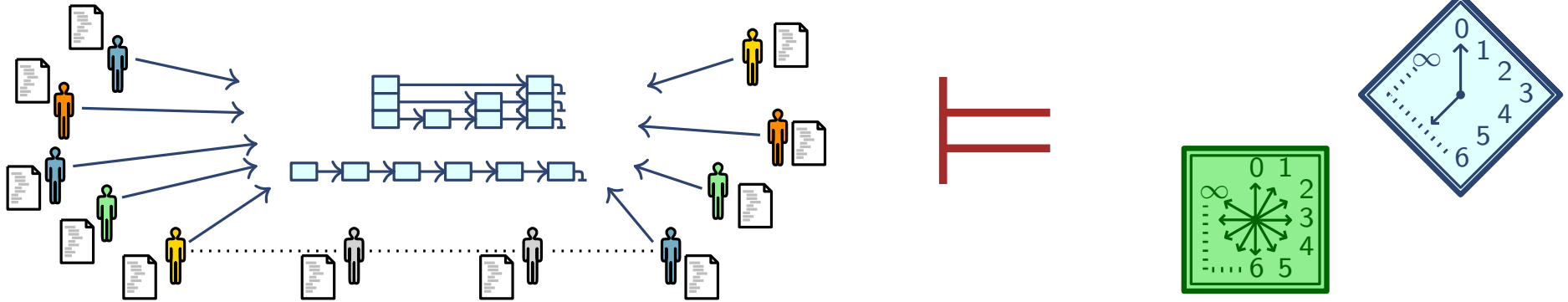
# Our Approach



**Parametrized  
Deductive  
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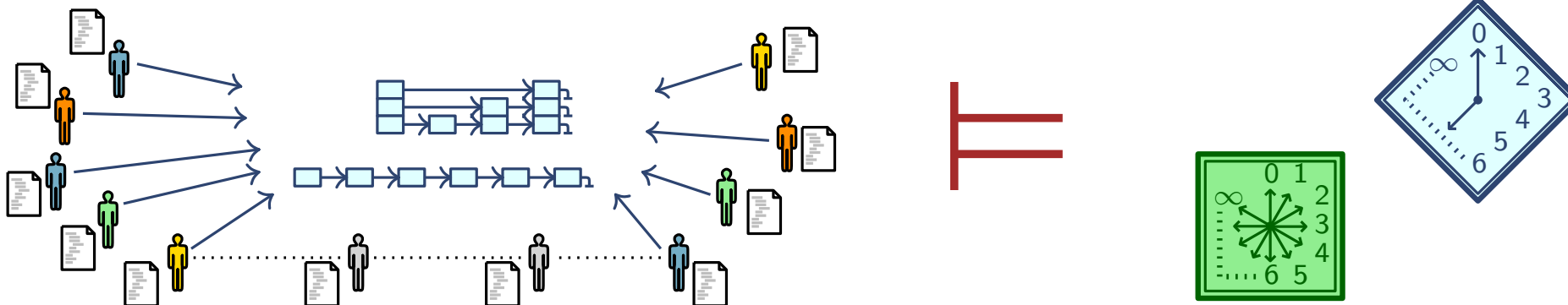
# Our Approach



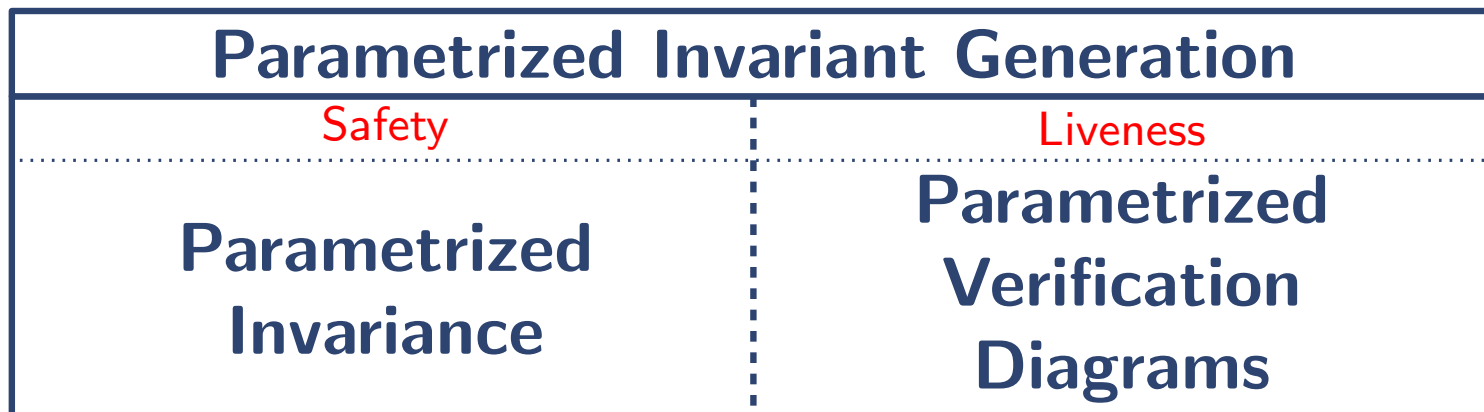
**Parametrized  
Deductive  
Methods**

Parametrized Invariant Generation	
<i>Safety</i>	<i>Liveness</i>
Parametrized Invariance	Parametrized Verification Diagrams

# Our Approach

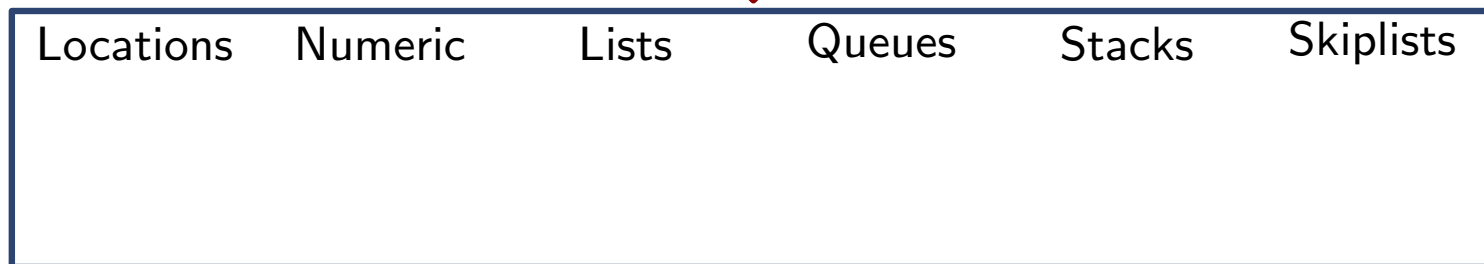


**Parametrized  
Deductive  
Methods**

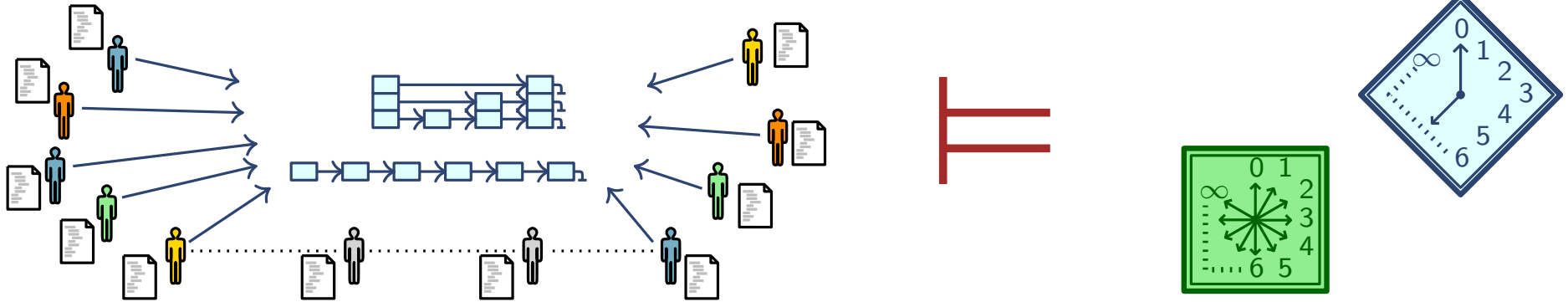


**Finite collection of VC**

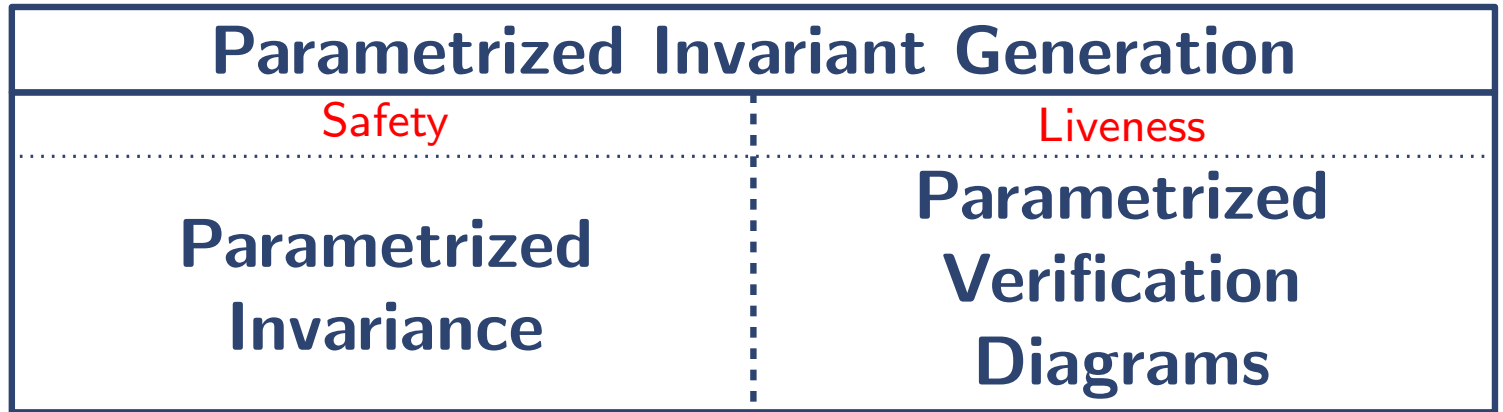
**Decision  
Procedures**



# Our Approach



**Parametrized  
Deductive  
Methods**



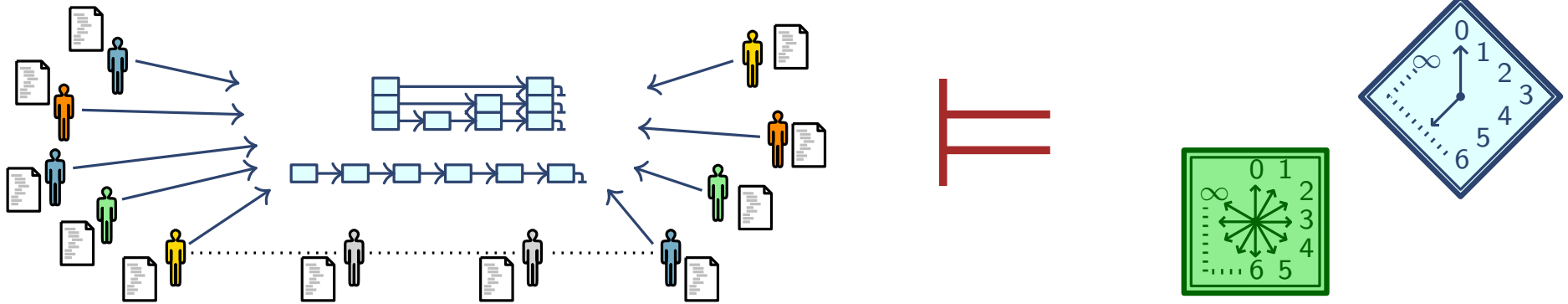
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**Decision  
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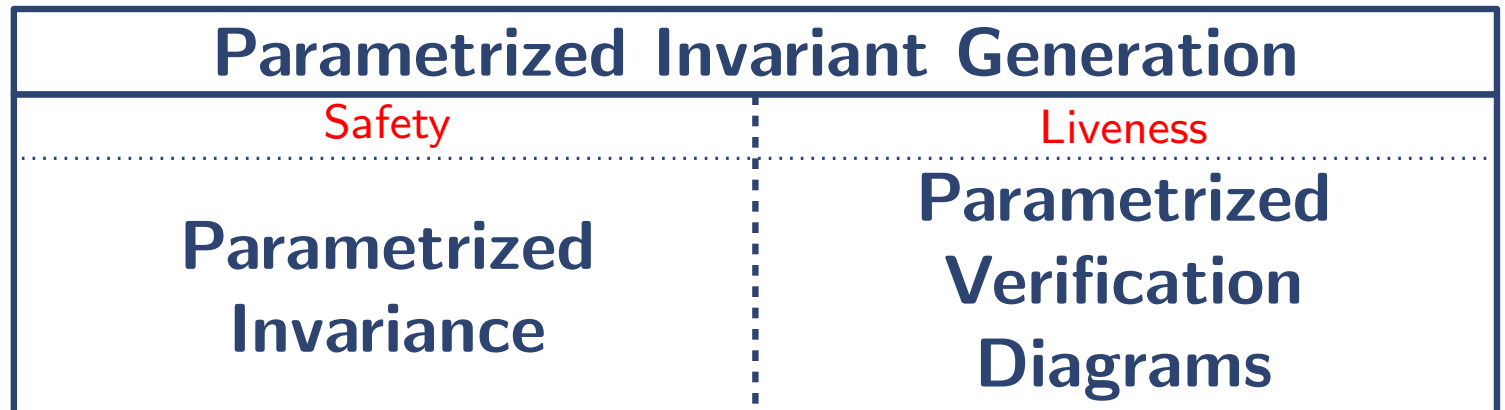
Locations	Numeric	Lists	Queues	Stacks	Skiplists
		<b>TL3</b>			<b>TSL<sub>K</sub></b> <b>TSL</b>



# Our Approach



**Parametrized  
Deductive  
Methods**

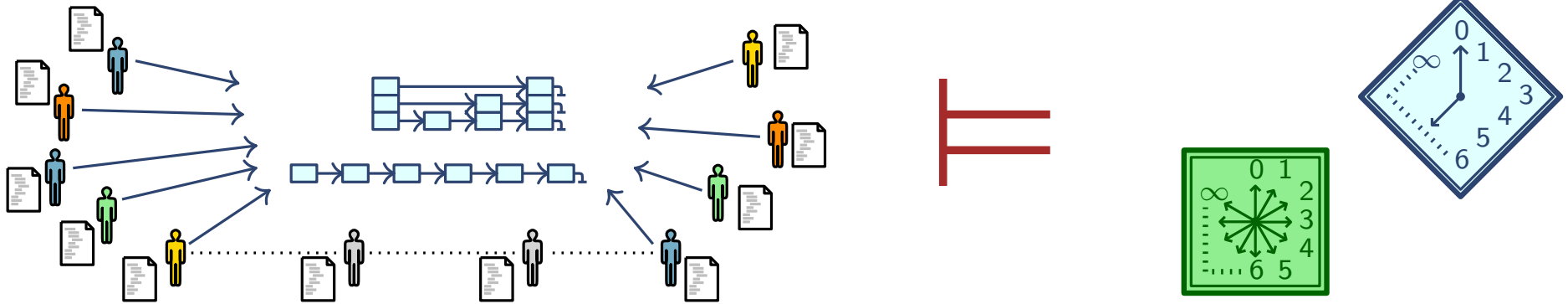


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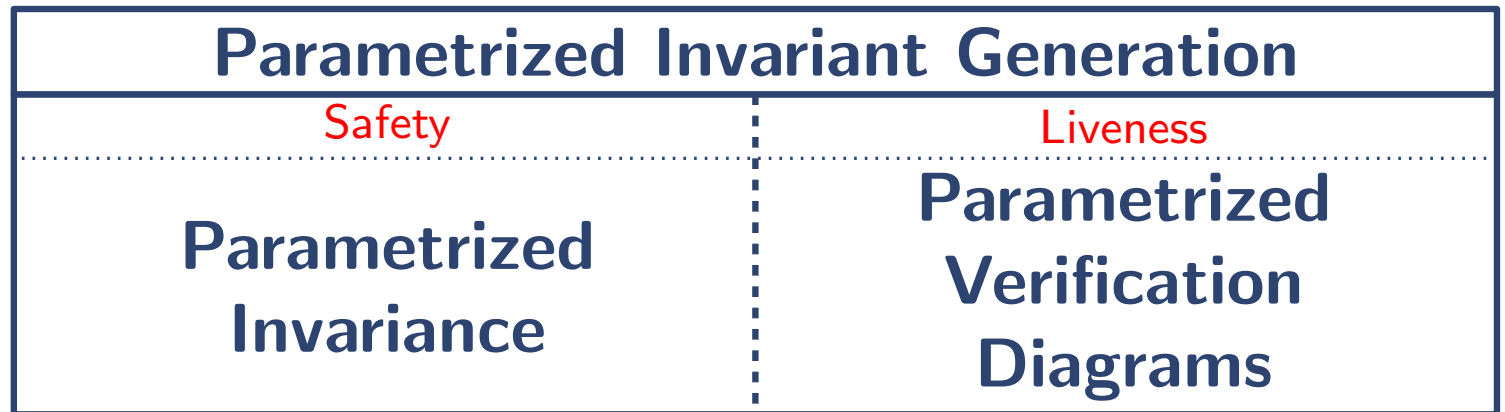
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Locations	Numeric	Lists	Queues	Stacks	Skiplists
		TL3			TSL <sub>K</sub> TSL
SMT Solvers (Z3, Yices, CVC4,...)					

# Our Approach



**Parametrized  
Deductive  
Methods**



**Finite collection of VC**

**Decision  
Procedures**

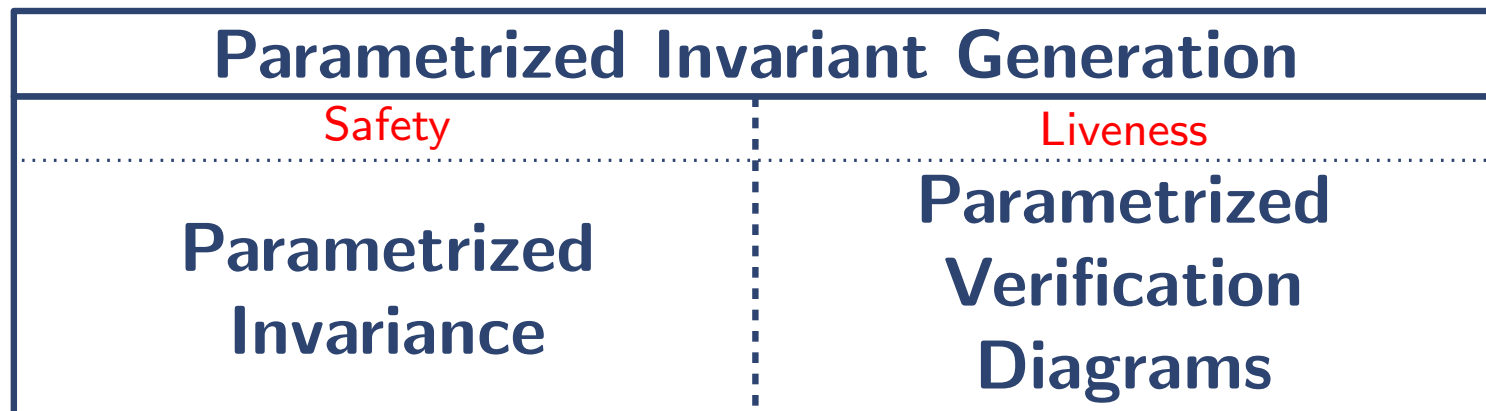
Locations	Numeric	Lists	Queues	Stacks	Skiplists
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**Implemented in LEAP**

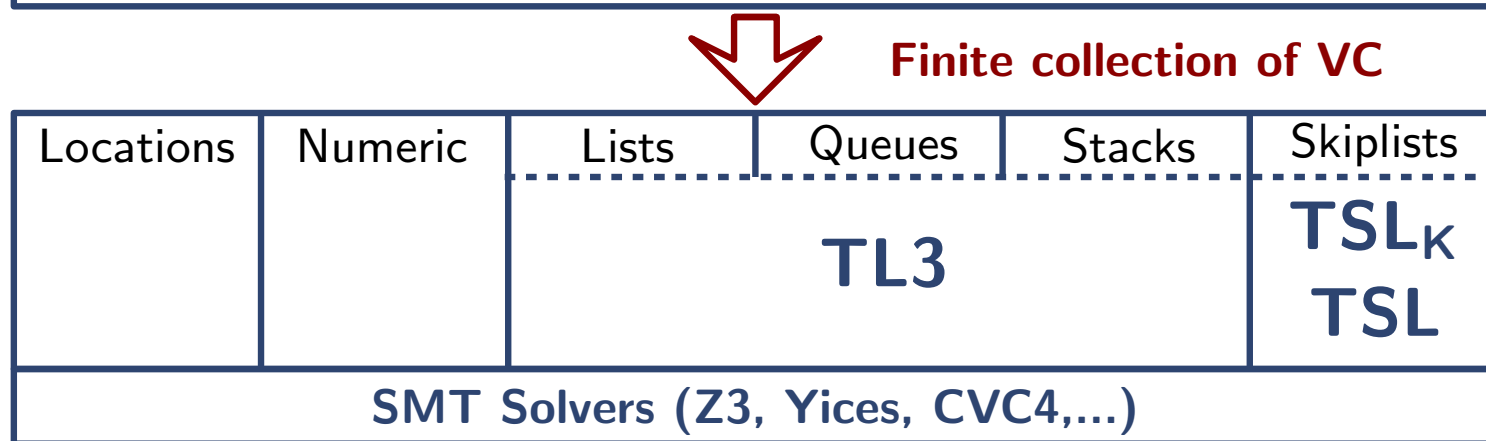
# Our Contributions

A deductive verification framework for parametrized concurrent systems

Parametrized  
Deductive  
Methods



Decision  
Procedures



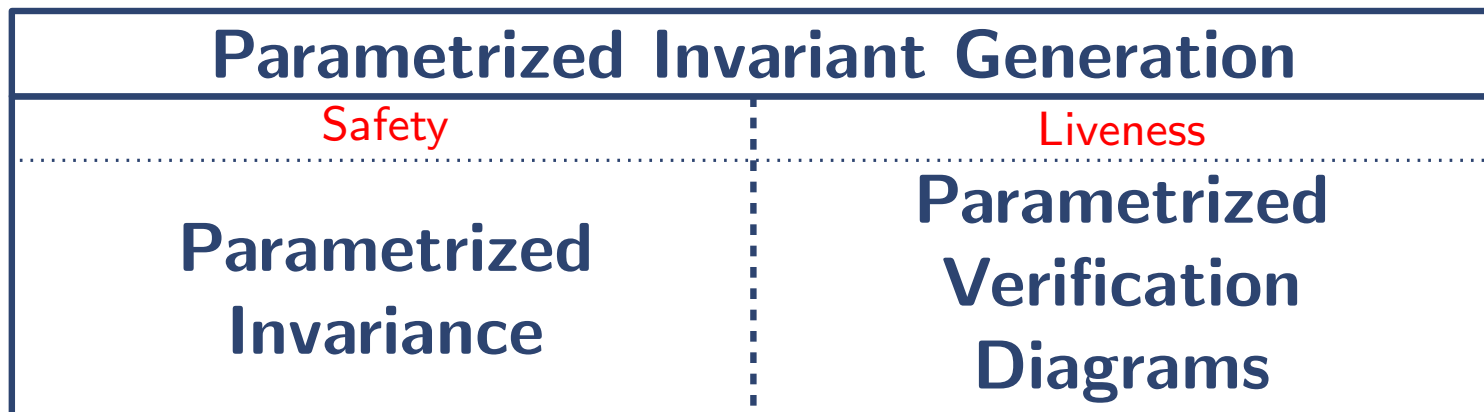
Implemented in LEAP

# Our Contributions

## A Deductive Verification Techniques for Parametrized Systems

A

Parametrized  
Deductive  
Methods



Finite collection of VC

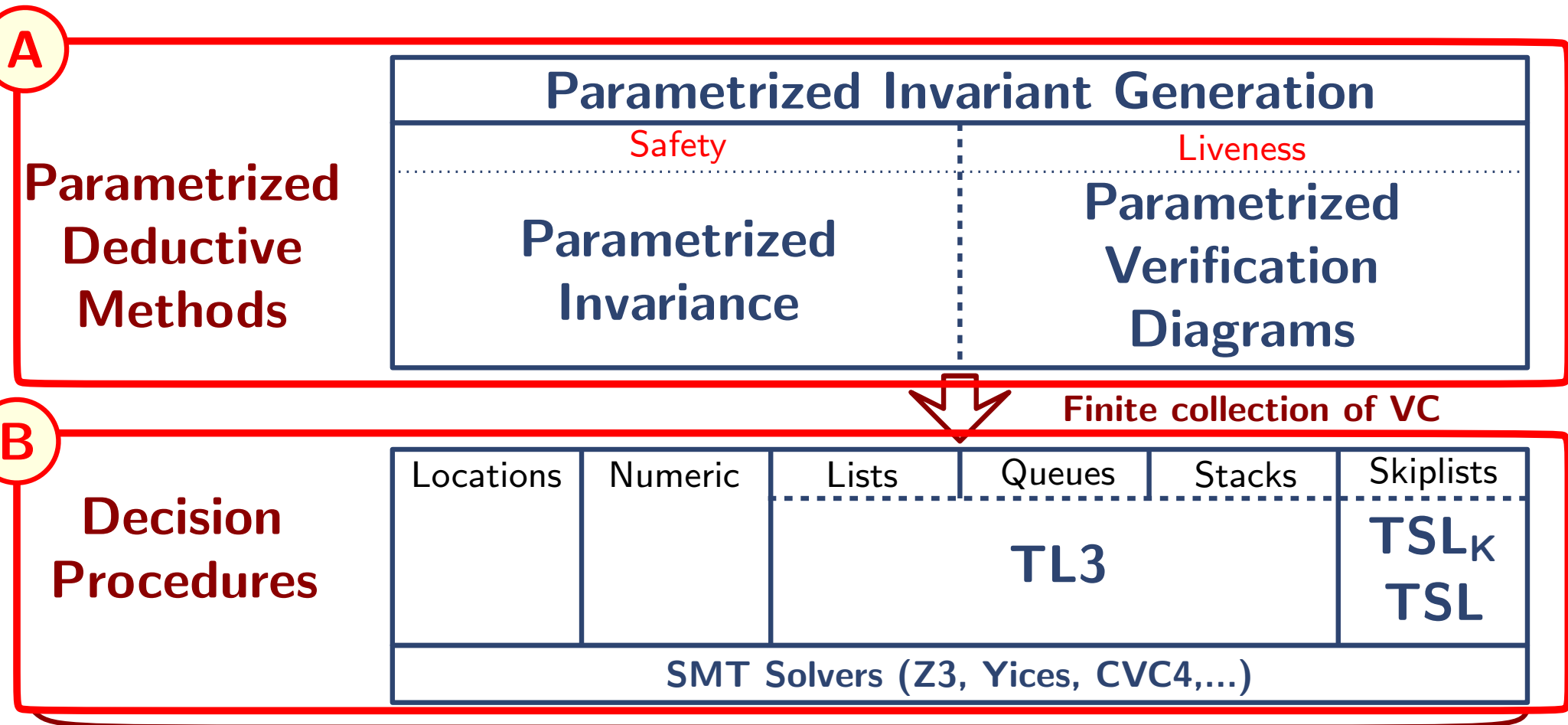
Decision  
Procedures



Implemented in LEAP

# Our Contributions

- A** Deductive Verification Techniques for Parametrized Systems
- B** Decision Procedures for Complex Data Structures



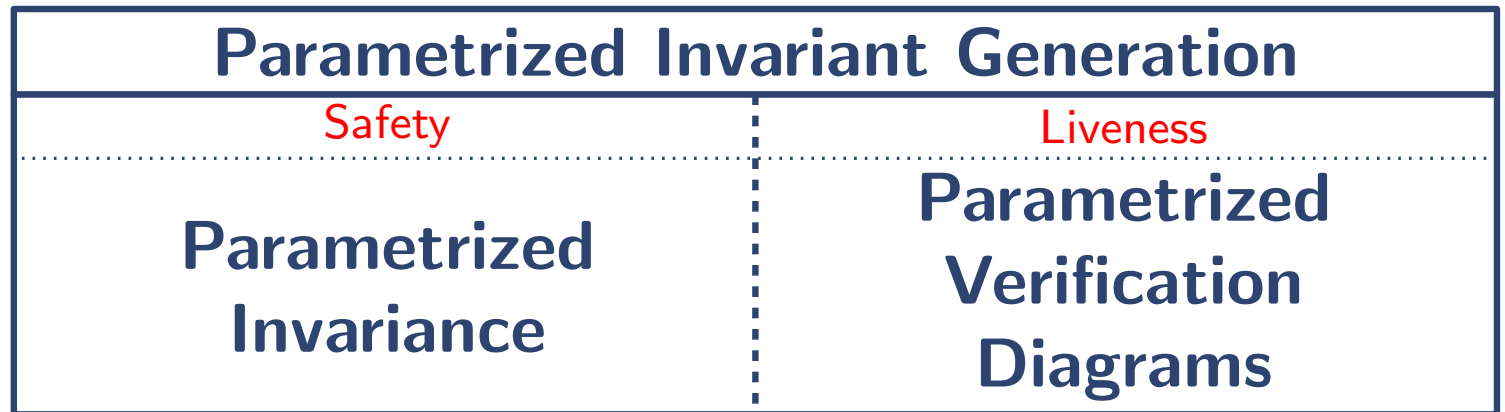
Implemented in LEAP

# Our Contributions

- A** Deductive Verification Techniques for Parametrized Systems
- B** Decision Procedures for Complex Data Structures
- C** Implementation and Evaluation of our Framework

**A**

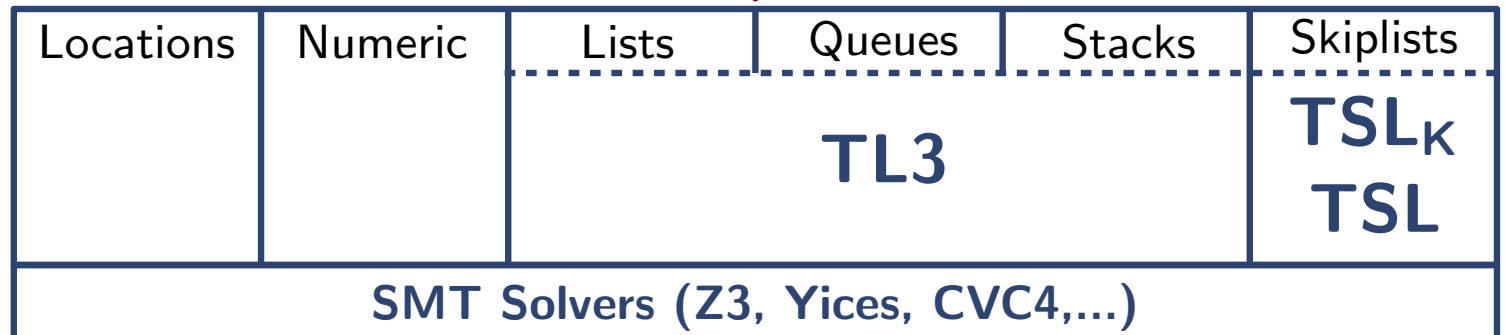
**Parametrized  
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Methods**



Finite collection of VC

**B**

**Decision  
Procedures**



**C**

**Implemented in LEAP**

# Our Contributions

**A** Deductive Verification Techniques for Parametrized Systems

**B** Decision Procedures for Complex Data Structures

**C** Implementation and Evaluation of our Framework

# Our Contributions

## **A** Deductive Verification Techniques for Parametrized Systems

- 1** **Parametrized Invariance**  
Deductive proof rules for concurrent parametrized invariants

## **B** Decision Procedures for Complex Data Structures

## **C** Implementation and Evaluation of our Framework



# Our Contributions

## **A** Deductive Verification Techniques for Parametrized Systems

### **1** Parametrized Invariance

Deductive proof rules for concurrent parametrized invariants

### **2** Parametrized Verification Diagrams

Diagram based verification for concurrent parametrized liveness properties

## **B** Decision Procedures for Complex Data Structures

## **C** Implementation and Evaluation of our Framework

# Our Contributions

## **A** Deductive Verification Techniques for Parametrized Systems

### **1** Parametrized Invariance

Deductive proof rules for concurrent parametrized invariants

### **2** Parametrized Verification Diagrams

Diagram based verification for concurrent parametrized liveness properties

### **3** Invariant Generation using Abstract Interpretation

Automatic parametrized invariant generation using off-the-shelf sequential absint

## **B** Decision Procedures for Complex Data Structures

## **C** Implementation and Evaluation of our Framework

# Our Contributions

## **A** Deductive Verification Techniques for Parametrized Systems



**1** **Parametrized Invariance**  
Deductive proof rules for concurrent parametrized invariants

**2** **Parametrized Verification Diagrams**  
Diagram based verification for concurrent parametrized liveness properties

**3** **Invariant Generation using Self-reflection**  
Automatic parametrized invariant generation using off-the-shelf sequential absint

## **B** Decision Procedures for Complex Data Structures

**4** **TL3: A Decidable Theory for Concurrent Lists**  
A Theory and decision procedure for concurrent data structures of the shape of a list

**5** **TSL<sub>K</sub>: A Decidable Family for Concurrent Bounded Skiplists**  
Theories and decision procedures for concurrent skiplists of at most K levels

**6** **TSL: A Decidable Theory for Skiplists with Arbitrary Levels**  
Theory and decision procedure for skiplists with unbounded many levels

## **C** Implementation and Evaluation of our Framework

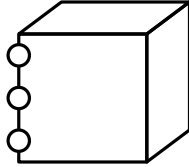
# Verification of Concurrent Data-structures

Our verification approach

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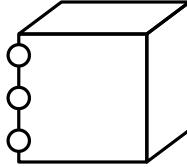
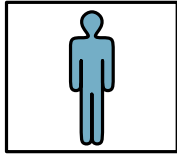
Concurrent DataStructure



# Verification of Concurrent Data-structures

## Our verification approach

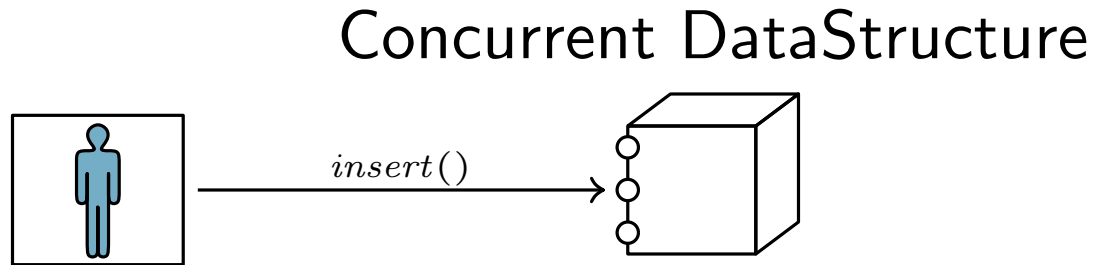
Concurrent DataStructure



Most General Client

# Verification of Concurrent Data-structures

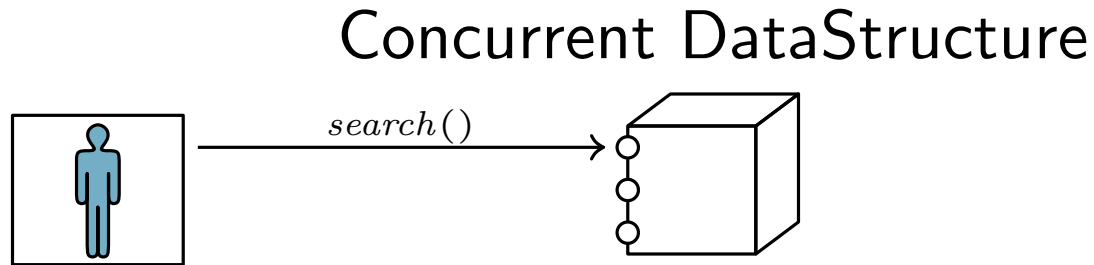
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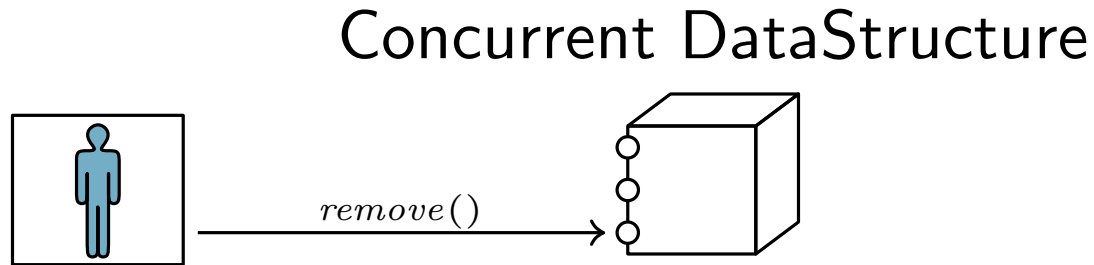


Most General Client



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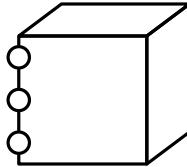
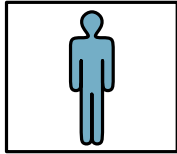


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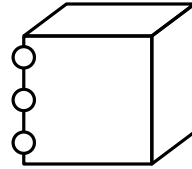


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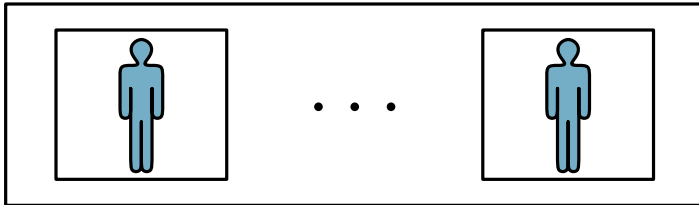
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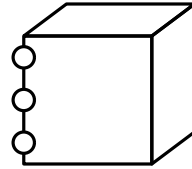
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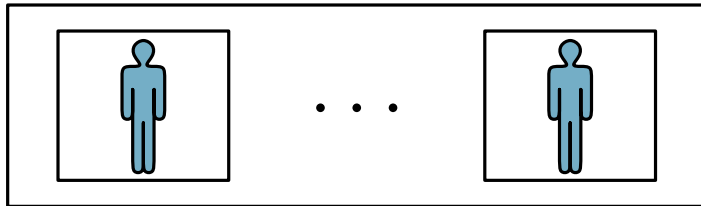
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## Our verification approach

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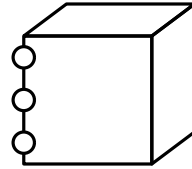


$$P[N] : P(1) || \dots || P(N)$$

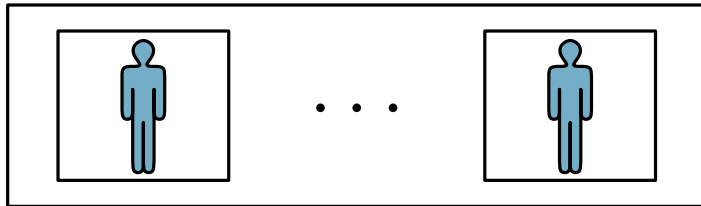
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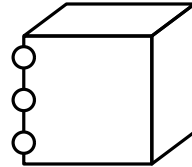
Property

$\varphi$

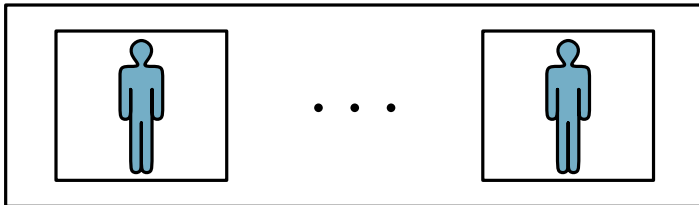
# Verification of Concurrent Data-structures

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Concurrent DataStructure



Most General Client



$P[N] : P(1) || \dots || P(N)$

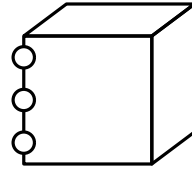
Property

$\varphi$   
LTL ( $\square, \diamond, \mathcal{U}, \dots$ )

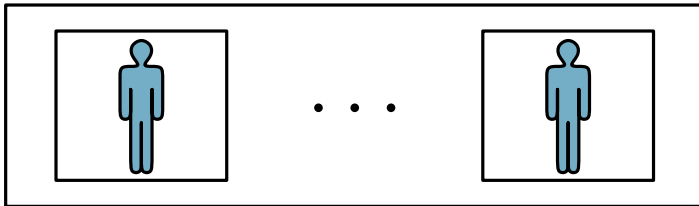
# Verification of Concurrent Data-structures

## Our verification approach

Concurrent DataStructure



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$$P[N] : P(1) || \dots || P(N)$$

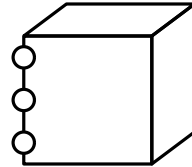
Safety  
Property

LTL ( $\square, \diamond, \mathcal{U}, \dots$ )  $\nearrow \varphi$

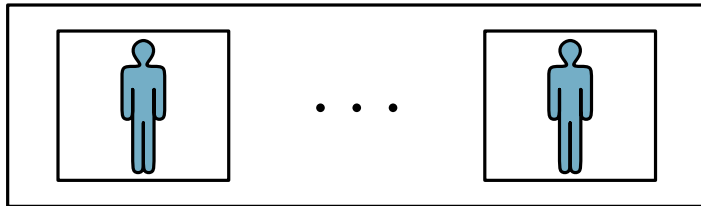
# Verification of Concurrent Data-structures

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Concurrent DataStructure



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$$P[N] : P(1) || \dots || P(N)$$

Safety  
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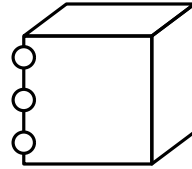
$\square p$



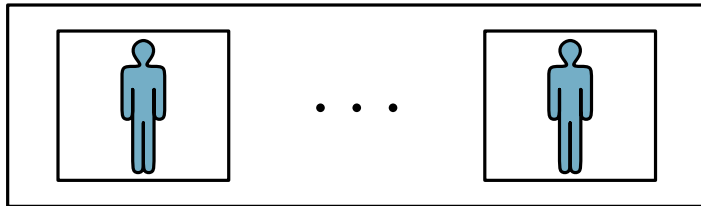
# Verification of Concurrent Data-structures

## Our verification approach

Concurrent DataStructure



Most General Client



$P[N] : P(1) || \dots || P(N)$



Safety  
Property

$\square p$

# Verification of Concurrent Data-structures

## Safety Properties

$P[N] : P(1) \parallel \dots \parallel P(N)$

?



$\square p$

# Verification of Concurrent Data-structures

## Safety Properties

$$P[N] : P(1) \parallel \dots \parallel P(N) \quad \stackrel{?}{\models} \quad \square p$$

means:

$$\text{forall } N \\ P[N] \models \square p$$

# Verification of Concurrent Data-structures

## Safety Properties

$$P[N] : P(1) \parallel \dots \parallel P(N) \quad \stackrel{?}{\models} \quad \square p$$

means:

forall  $N$

$$P[N] \models \square p(k_1, k_2)$$

# Verification of Concurrent Data-structures

## Safety Properties

$$P[N] : P(1) \parallel \dots \parallel P(N) \quad \stackrel{?}{\models} \quad \square p$$

means:

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**Parametrized Property**

# Verification of Concurrent Data-structures

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**Uniform Verification Problem**

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To show that  $P$  satisfies  $\Box p$ , find  $q$ :

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I2.  $q \wedge \tau \rightarrow q'$  for all  $\tau$

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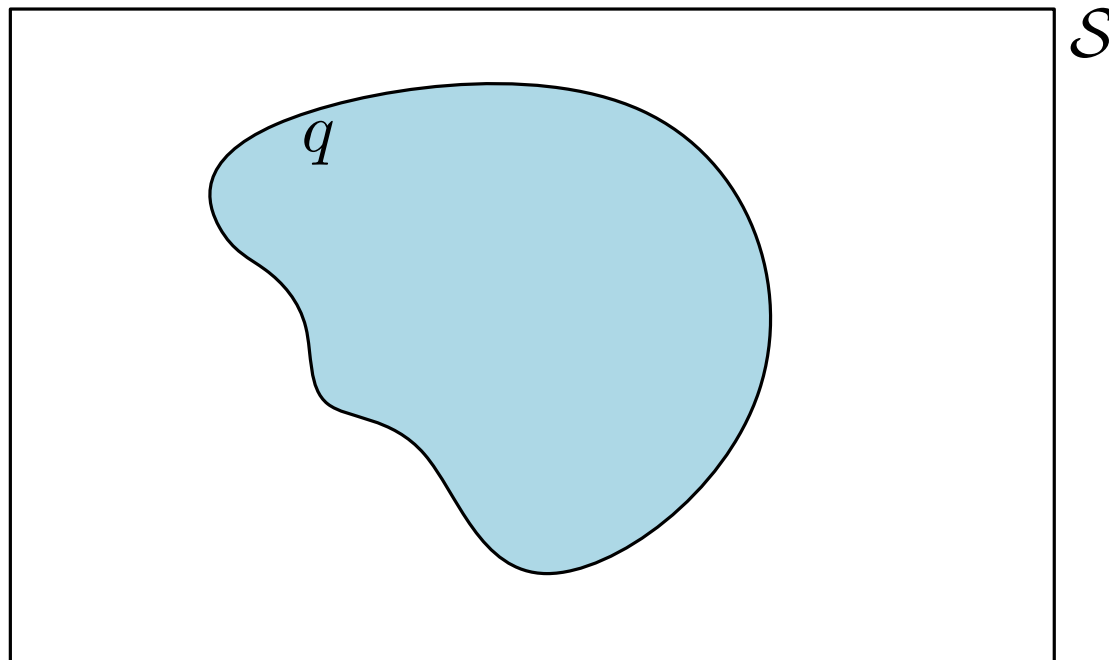
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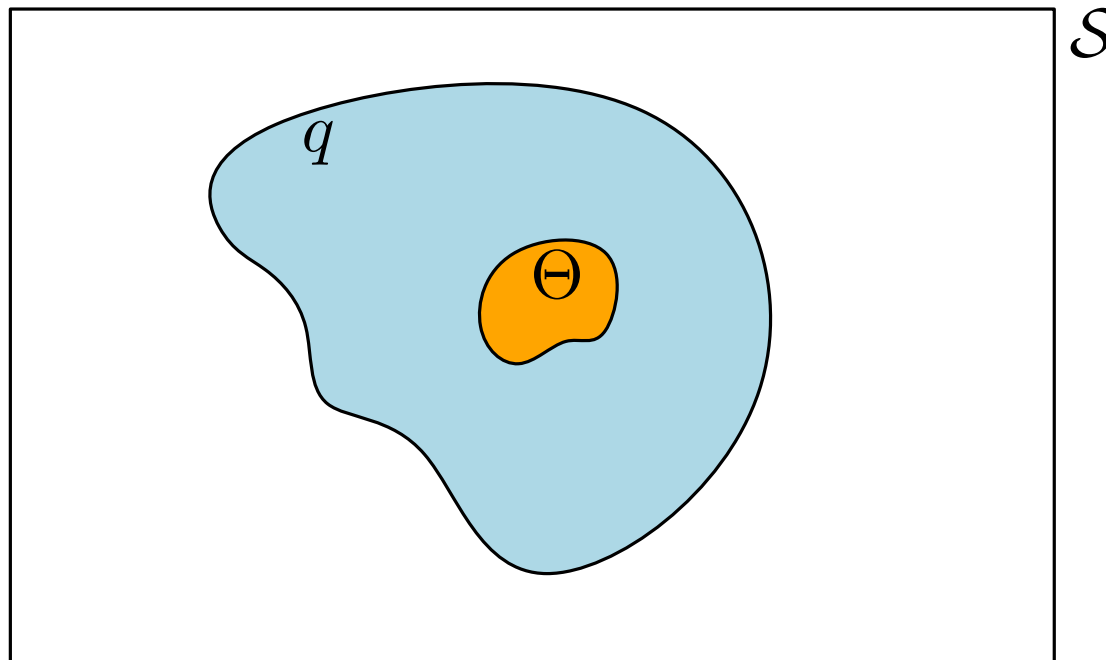
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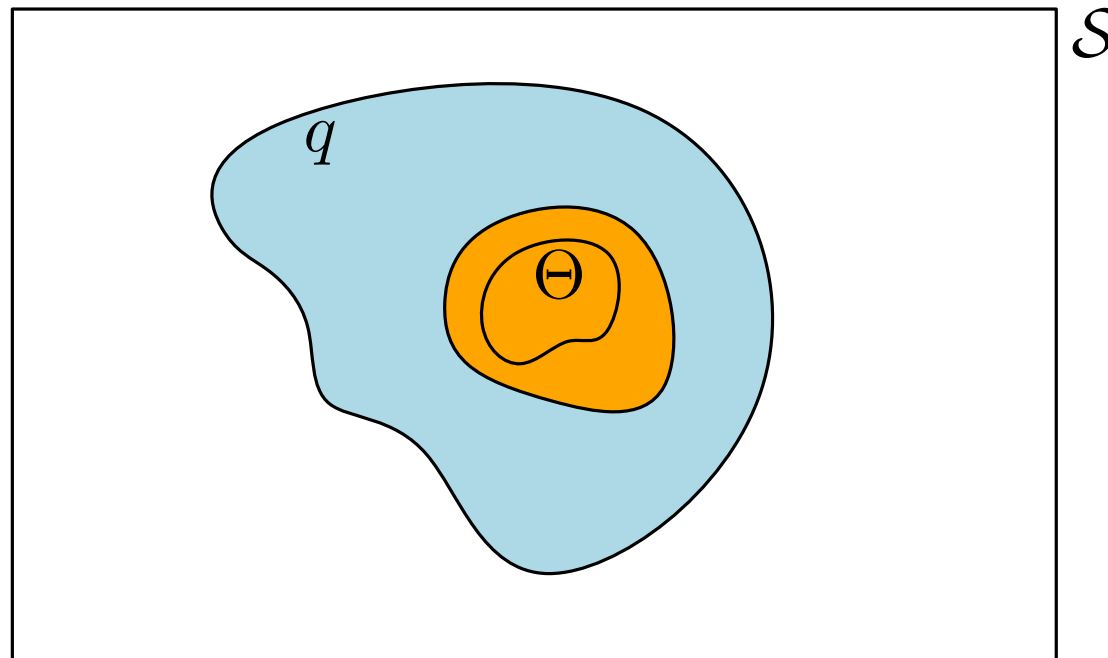
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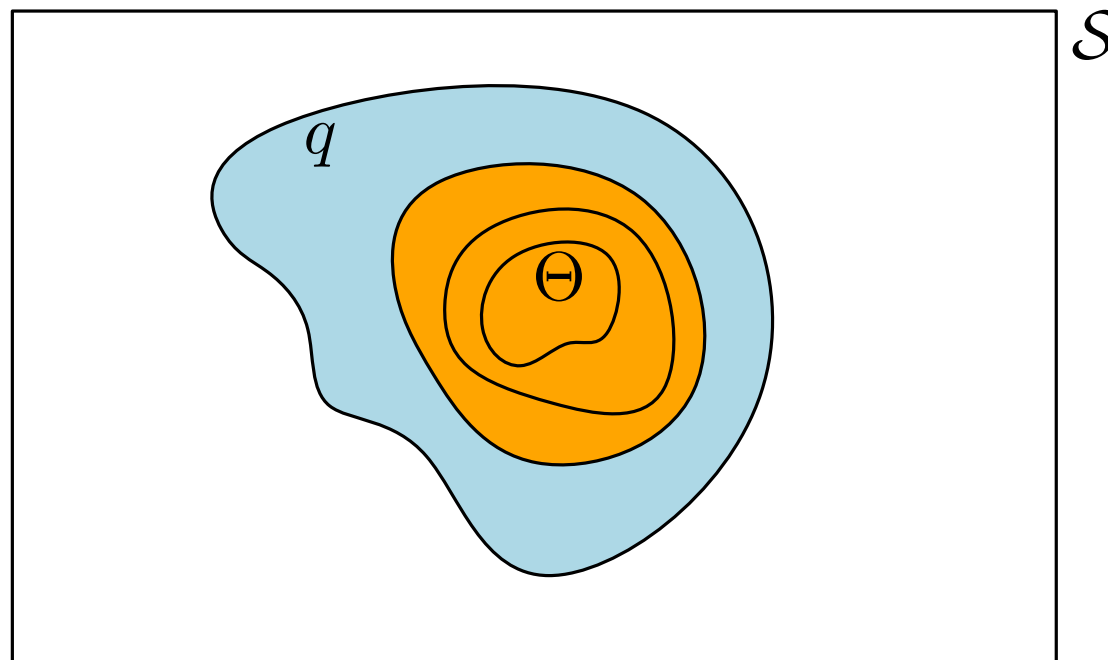
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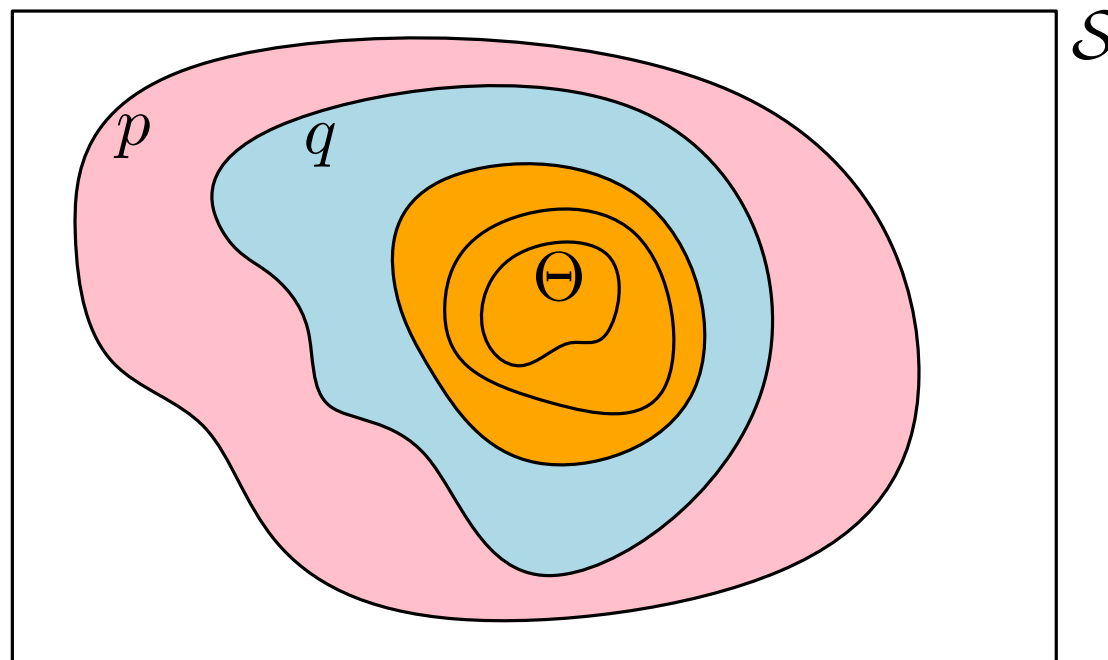
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**Why this rule does not work for parametrized systems?**

# Motivating Example: Mutual Exclusion Protocol

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**global**

*Int tick := 0*

*Set<Int> bag := ∅*

**procedure** SETMUTEX

*Int ticket := 0*

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```
1: while true do  
2:   noncritical  
3:    $\left\langle \begin{array}{l} \textit{ticket} := \textit{tick} ++ \\ \textit{bag.add}(\textit{ticket}) \end{array} \right\rangle$   
4:   await (bag.min == ticket)  
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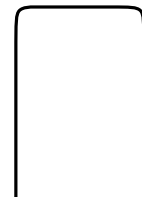
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Critical Section

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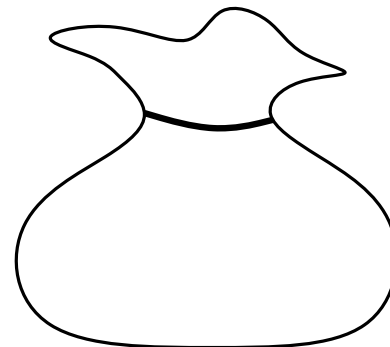
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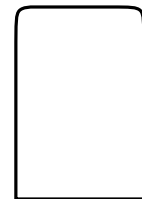
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*tick*



bag



Critical Section

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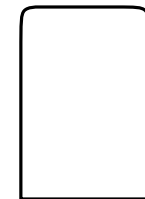
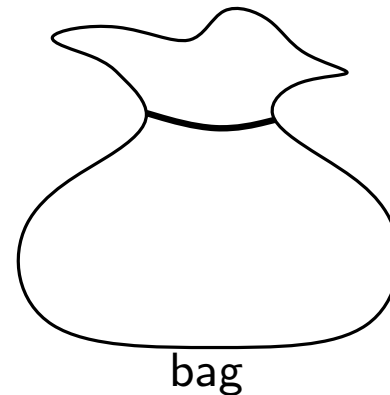
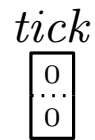
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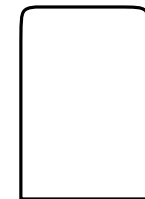
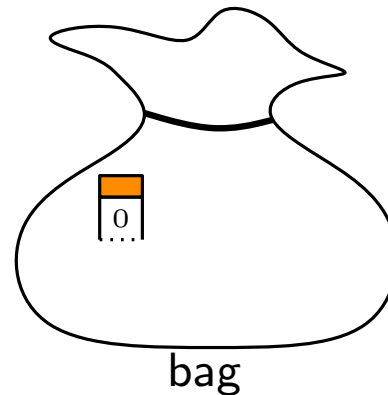
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*tick*  
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Critical Section

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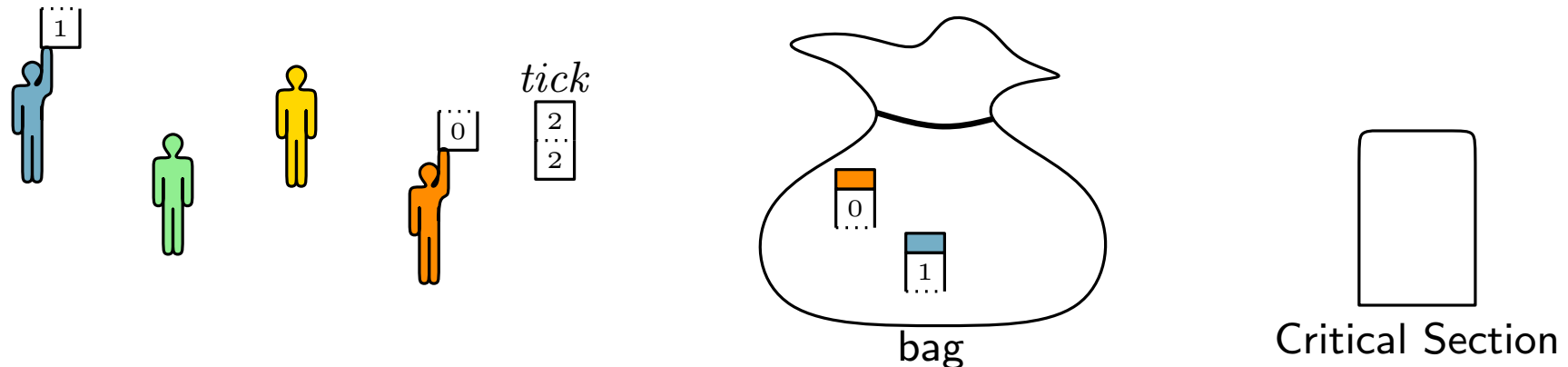
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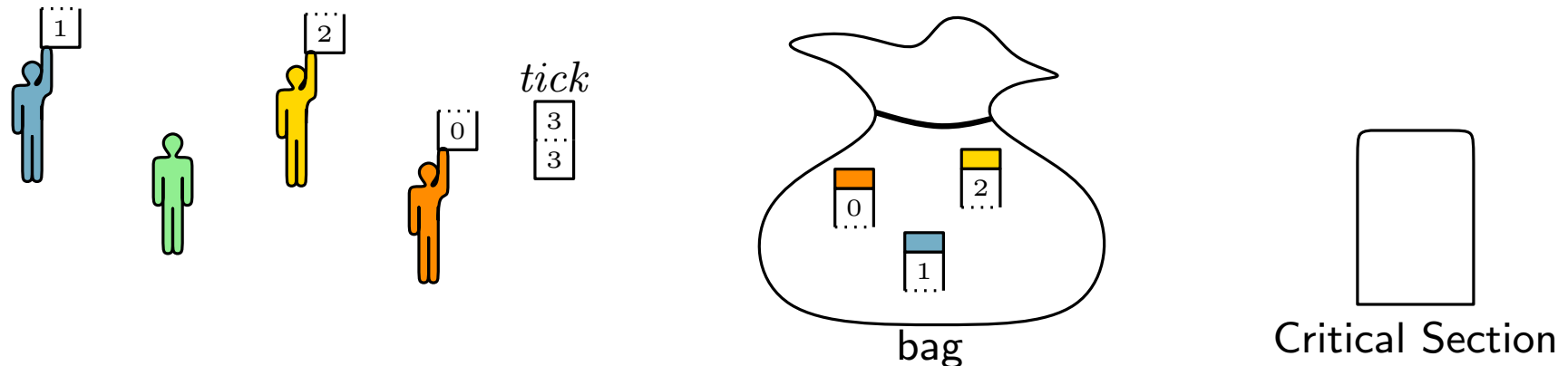
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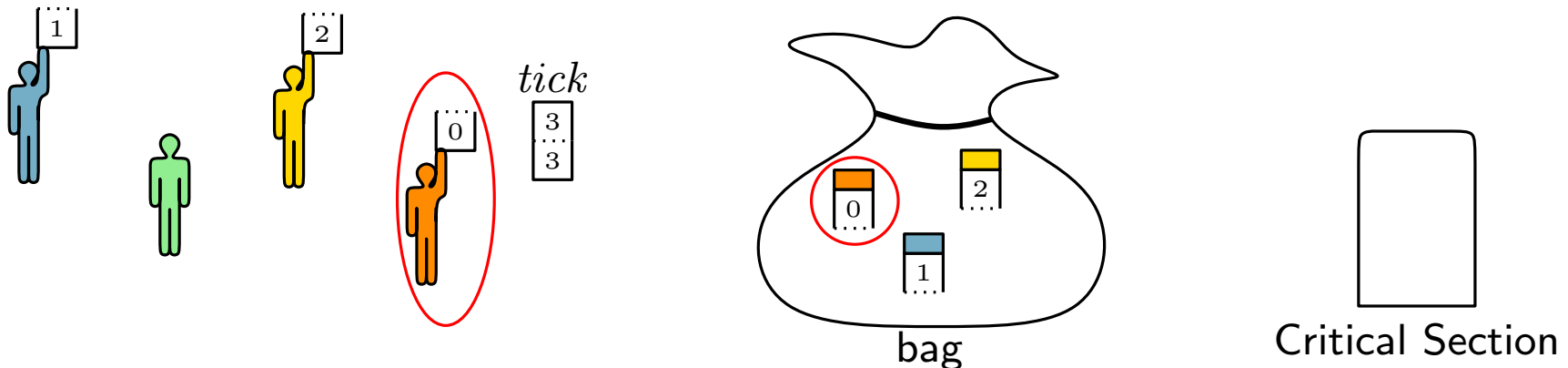
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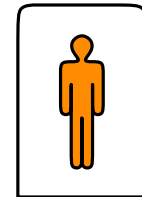
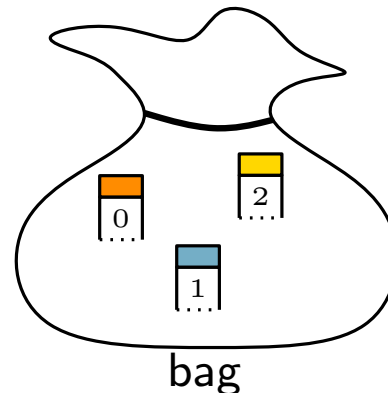
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3  
...  
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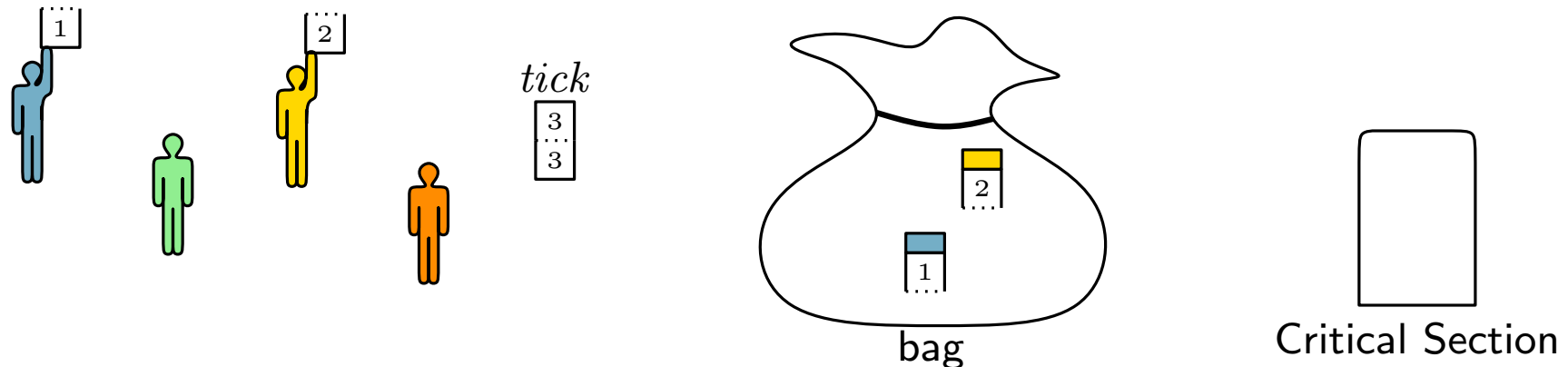
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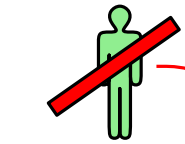
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$$\text{mutex}(i, j) \hat{=} \square [i \neq j \rightarrow \neg(\text{critical}(i) \wedge \text{critical}(j))]$$

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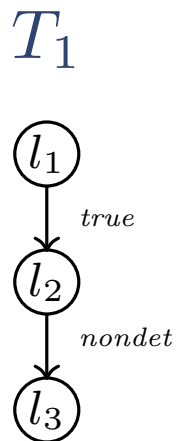
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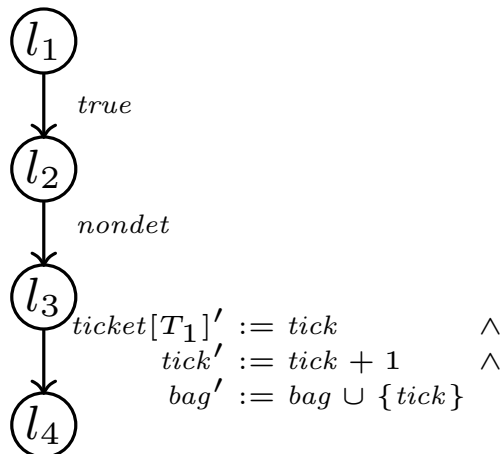
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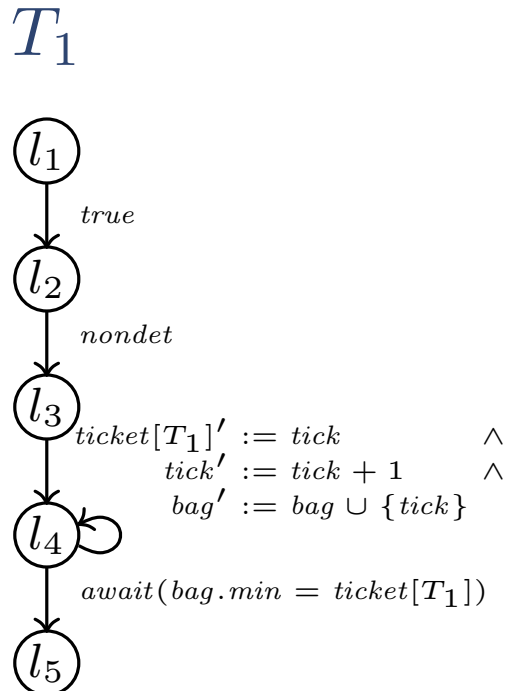
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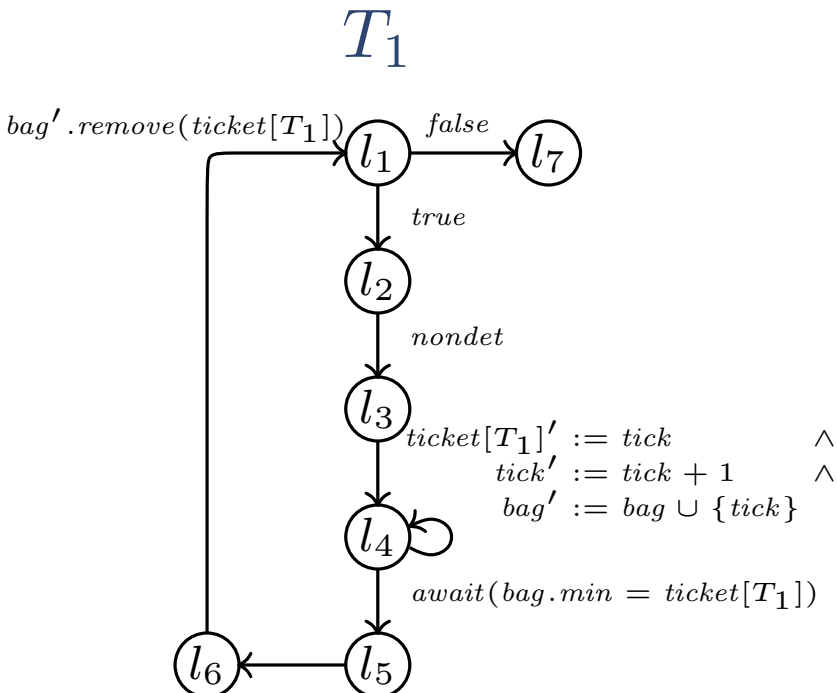
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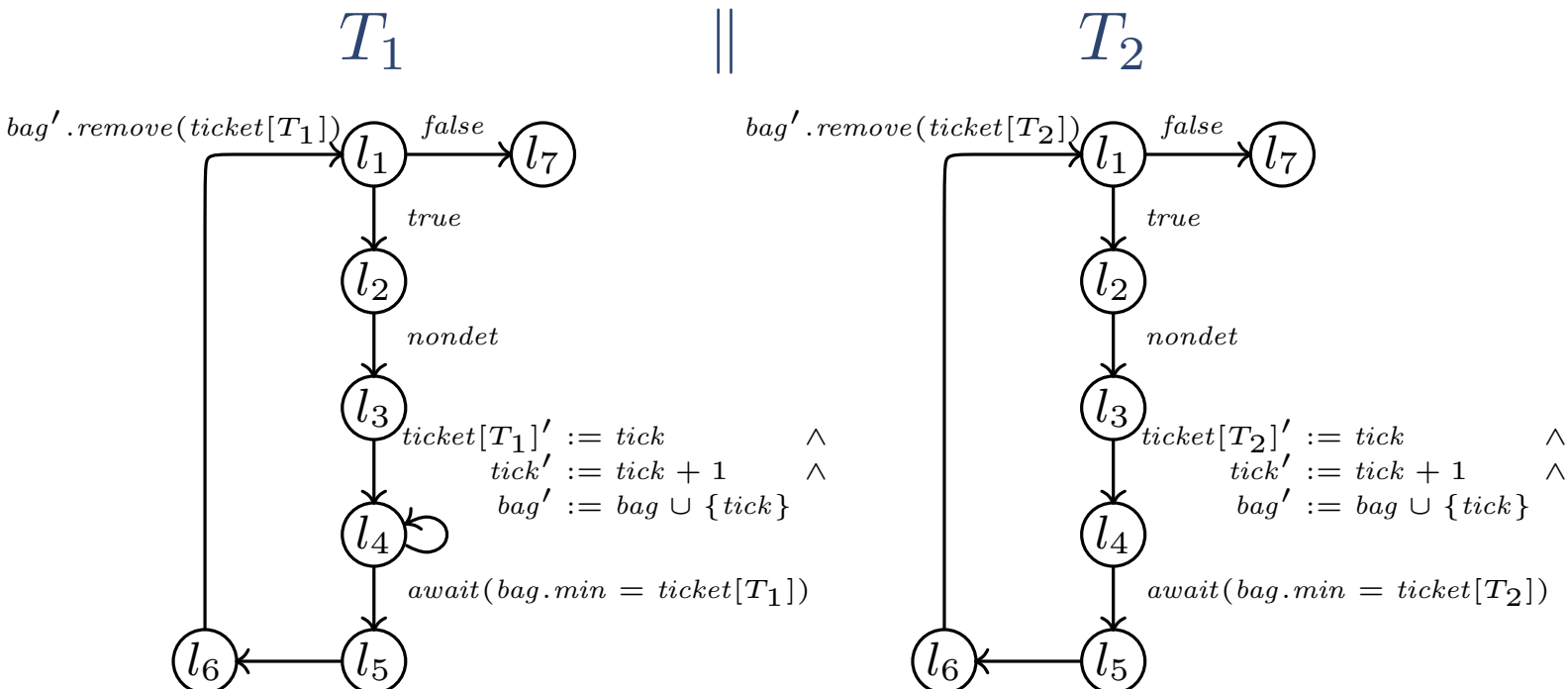
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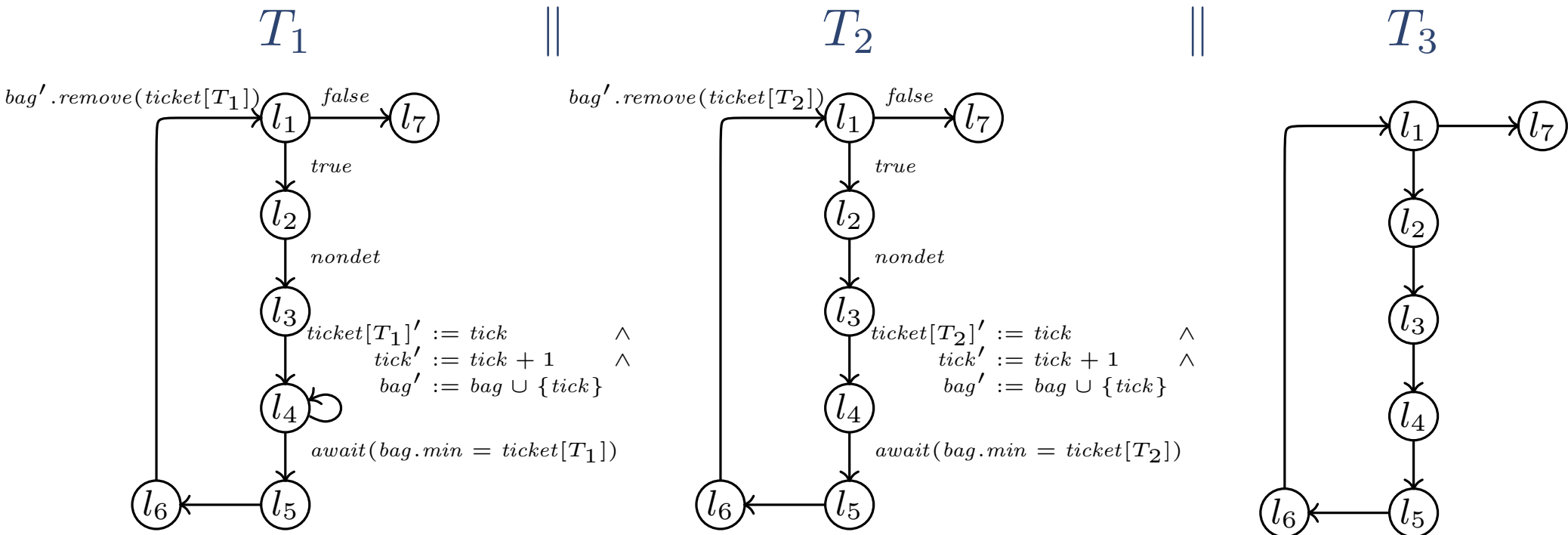
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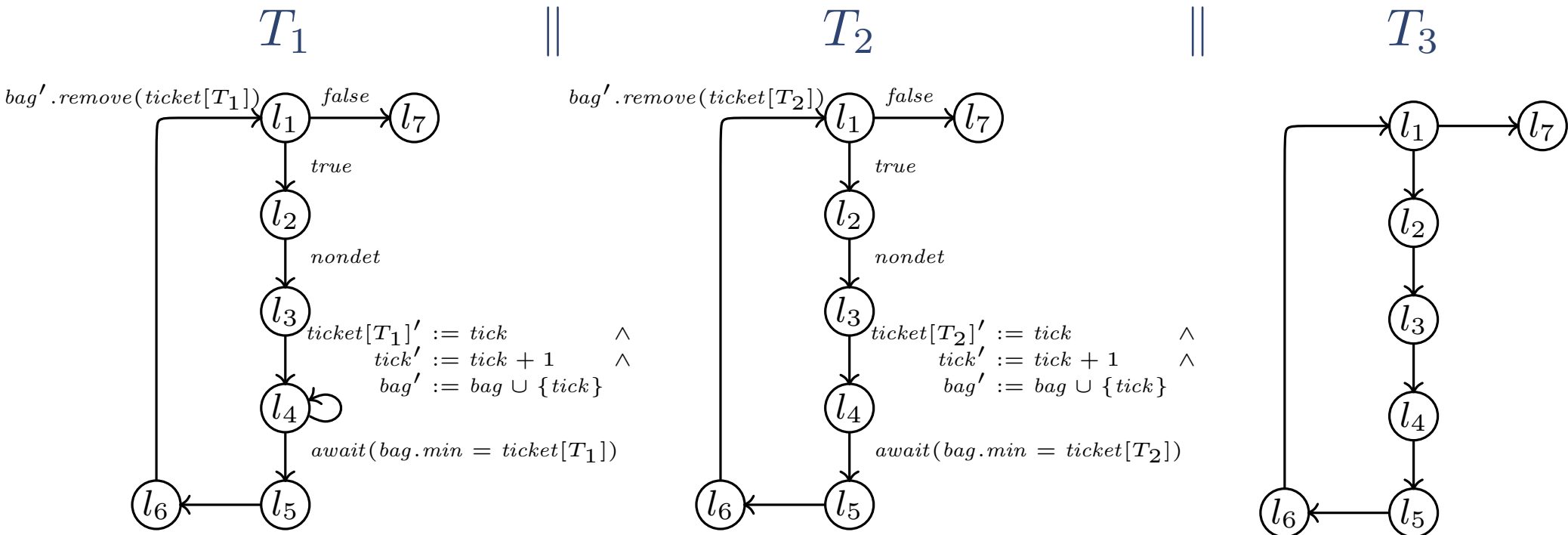
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► Consecution: ~~16 VC~~ **24 VC**



# Parametrized Invariance

## Problem

**Unbounded** number of **verification conditions**

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Unbounded number of **verification conditions**

## Our solution

- ▶ **Specialized** invariance proof rules
- ▶ **Finite and bounded** number of verification conditions

Parametrized Invariance exploits the **similarities**  
of **fully symmetric systems**

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To show that  $\mathcal{S}$  satisfies  $\varphi(i)$ :

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$\varphi \wedge k \neq i \wedge \tau^{(k)} \rightarrow \varphi'$  for all  $\tau$ , fresh  $k$

$\square \varphi$

**Self-consecution**

# Parametrized Invariance (p-inv)

- ▶ **Bounded** number of VC, based on **program** and **specification**

To show that  $\mathcal{S}$  satisfies  $\varphi(i)$ :

$$(I) \quad \Theta \rightarrow \varphi$$

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---

$\square \varphi$

→ **Other-consecution**

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- ▶ For our example:  $\text{mutex}(i, j)$

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---

$$\square \varphi$$

- ▶ For our example:  $\text{mutex}(i, j)$       **#VC : 1**

$$(I) \quad \Theta(i, j) \rightarrow \text{mutex}$$

# Parametrized Invariance (p-inv)

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- ▶ For our example:  $\text{mutex}(i, j)$       **#VC : 1 + 16**

$$\begin{array}{l} \text{( I )} \quad \Theta(i, j) \rightarrow \text{mutex} \\ \text{( SC )} \quad \text{mutex} \wedge \tau^{(i)} \rightarrow \text{mutex}' \quad \text{forall } \tau \\ \text{mutex} \wedge \tau^{(j)} \rightarrow \text{mutex}' \quad \text{forall } \tau \end{array}$$

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- ▶ For our example:  $\text{mutex}(i, j)$       **#VC : 1 + 16 + 8 = 25**

$$\begin{array}{l} \text{( I )} \quad \Theta(i, j) \rightarrow \text{mutex} \\ \text{(SC)} \quad \text{mutex} \wedge \tau^{(i)} \rightarrow \text{mutex}' \quad \text{forall } \tau \\ \quad \quad \text{mutex} \wedge \tau^{(j)} \rightarrow \text{mutex}' \quad \text{forall } \tau \\ \text{(OC)} \quad \text{mutex} \wedge k \neq i \wedge k \neq j \wedge \tau^{(k)} \rightarrow \text{mutex}' \quad \text{forall } \tau, \text{ fresh } k \end{array}$$



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**Independently on #threads** in the system

- ▶ For our example: mutex( $i, j$ )      **#VC : 1 + 16 + 8 = 25**

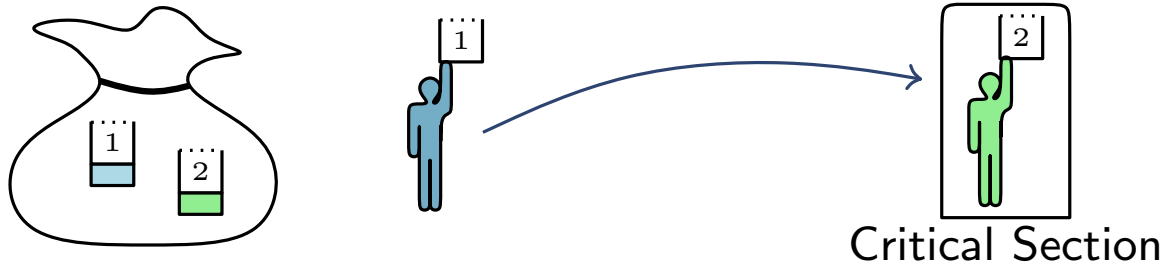
$$\begin{array}{l} \text{( I )} \quad \Theta(i, j) \rightarrow \text{mutex} \\ \text{(SC)} \quad \text{mutex} \wedge \tau^{(i)} \rightarrow \text{mutex}' \quad \text{forall } \tau \\ \quad \quad \text{mutex} \wedge \tau^{(j)} \rightarrow \text{mutex}' \quad \text{forall } \tau \\ \text{(OC)} \quad \text{mutex} \wedge k \neq i \wedge k \neq j \wedge \tau^{(k)} \rightarrow \text{mutex}' \quad \text{forall } \tau, \text{ fresh } k \end{array}$$

# Parametrized Invariance (p-inv) **is not enough**

- ▶ Lets try to prove mutex using P-INV...

# Parametrized Invariance (p-inv) is not enough

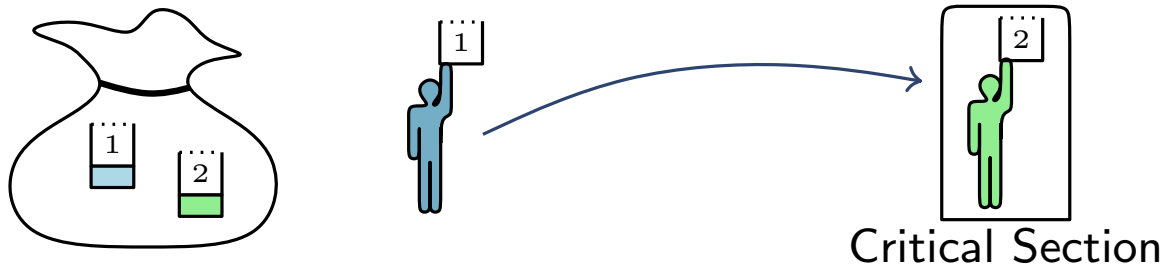
- ▶ Lets try to prove mutex using P-INV...
- ▶ ... transition 4 (i.e., **await** ( $bag.min == ticket$ )) **fails**



Because mutex does not encode that the  
**thread in the critical section owns the minimum ticket**

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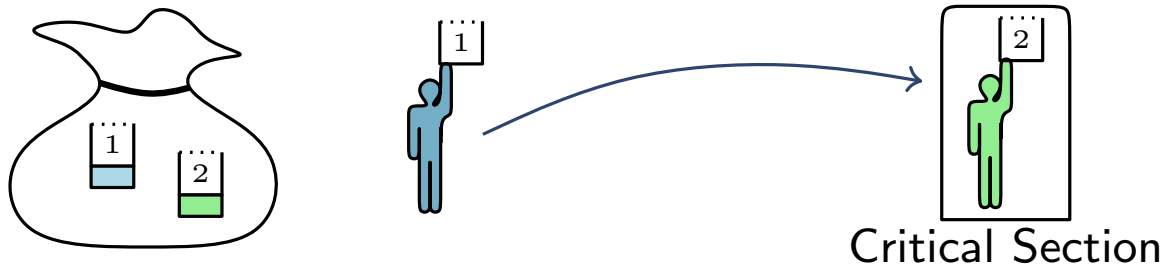
- ▶ Extra support is required

$$\text{minticket}(i) \hat{=} \square [critical(i) \rightarrow \min(bag) = ticket(i)]$$

$$\text{notsame}(i, j) \hat{=} \square [i \neq j \wedge active(i) \wedge active(j) \rightarrow ticket(i) \neq ticket(j)]$$

# Parametrized Invariance (p-inv) is not enough

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We now require a **new rule** for **invariant support**

# Parametrized Invariance with Support (sp-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi(i)$ . Find  $\psi(\bar{w})$  with:

$$\begin{array}{c}
 (S) \quad \Box\psi \\
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 (SC) \quad \psi, \varphi \triangleright \tau^{(i)} \rightarrow \varphi' \quad \text{forall } \tau \\
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 \hline
 \Box\varphi
 \end{array}$$

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---

$$\Box\varphi$$

$\Box\psi$  — **strengthening**

# Parametrized Invariance with Support (sp-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi(i)$ . Find  $\psi(\bar{w})$  with:

( S )	$\Box\psi$	$\rightarrow$	$\varphi$	→ <b>strengthening</b>
( I )	$\Theta$	$\rightarrow$	$\varphi$	→ <b>initiation</b>
( SC )	$\psi, \varphi \triangleright$	$\tau^{(i)}$	$\rightarrow \varphi'$	→ <b>self-consecution</b>
				forall $\tau$
( OC )	$\psi, \varphi \triangleright$	$k \neq i \wedge \tau^{(k)}$	$\rightarrow \varphi'$	→ <b>other-consecution</b>
				forall $\tau$ , fresh $k$
$\Box\varphi$				



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 \hline
 \Box\varphi
 \end{array}$$

Instantiate the assumptions for **self** and **others**

$$\psi \triangleright (A \rightarrow B) \quad \text{whether} \quad [(\bigwedge_{\sigma \in \mathcal{S}} \psi_{\sigma} \wedge A) \rightarrow B]$$

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► **Example:** minticket and notsame to support  $\text{mutex}(i, j)$

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Instantiate the assumptions for **self** and **others**

## Problem

Sometimes we have **invariant circular dependency**



# Parametrized Invariance with Graph Support (g-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi_i(\bar{v}) \wedge \Box\varphi_j(\bar{w})$ :

( I )		$\Theta$	$\rightarrow$	$\varphi_i \wedge \varphi_j$	
( SC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow$	$\varphi'_i$	forall $\tau$ , forall $t \in \bar{v}$
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$\Box\varphi_i \wedge \Box\varphi_j$					

# Parametrized Invariance with Graph Support (g-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi_i(\bar{v}) \wedge \Box\varphi_j(\bar{w})$ :

→ **initiation**

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( SC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright \tau^{(t)} \rightarrow \varphi'_i$	forall $\tau$ , forall $t \in \bar{v}$
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$\Box\varphi_i \wedge \Box\varphi_j$					

**initiation**

**self-consecution**

# Parametrized Invariance with Graph Support (g-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi_i(\bar{v}) \wedge \Box\varphi_j(\bar{w})$ :

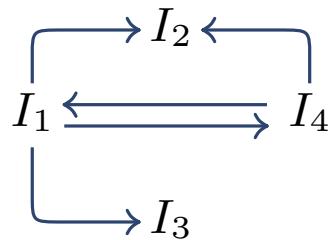
( I )	$\Theta$	$\rightarrow$	$\varphi_i \wedge \varphi_j$		<b>initiation</b>
( SC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow$	$\varphi'_i$	forall $\tau$ , forall $t \in \bar{v}$
( SC <sub>j</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow$	$\varphi'_j$	forall $\tau$ , forall $t \in \bar{w}$
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( OC <sub>j</sub> )	$\varphi_i, \varphi_j \triangleright$	$\bigwedge_{x \in \bar{w}} k \neq x \wedge \tau^{(k)}$	$\rightarrow$	$\varphi'_j$	forall $\tau$ , fresh $k \notin \bar{w}$
$\Box\varphi_i \wedge \Box\varphi_j$					<b>others-consecution</b>

# Parametrized Invariance with Graph Support (g-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi_i(\bar{v}) \wedge \Box\varphi_j(\bar{w})$ :

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( SC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow \varphi'_i$	<b>self-consecution</b> forall $\tau$ , forall $t \in \bar{v}$
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			$\Box\varphi_i \wedge \Box\varphi_j$	<b>others-consecution</b>

► A **generalization** of G-INV is a **proof graph**

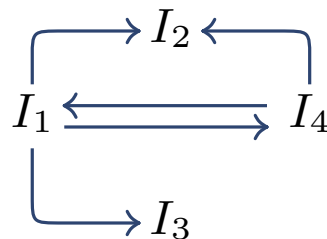


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( I )	$\Theta$	$\rightarrow$	$\varphi_i \wedge \varphi_j$	<b>initiation</b>
( SC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow \varphi'_i$	<b>self-consecution</b>
( SC <sub>j</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow \varphi'_j$	forall $\tau$ , forall $t \in \bar{v}$
( OC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright$	$\bigwedge_{x \in \bar{v}} k \neq x \wedge \tau^{(k)}$	$\rightarrow \varphi'_i$	forall $\tau$ , forall $t \in \bar{w}$
( OC <sub>j</sub> )	$\varphi_i, \varphi_j \triangleright$	$\bigwedge_{x \in \bar{w}} k \neq x \wedge \tau^{(k)}$	$\rightarrow \varphi'_j$	forall $\tau$ , fresh $k \notin \bar{v}$
			$\Box\varphi_i \wedge \Box\varphi_j$	<b>others-consecution</b>

- ▶ A **generalization** of G-INV is a **proof graph**



## Theorem

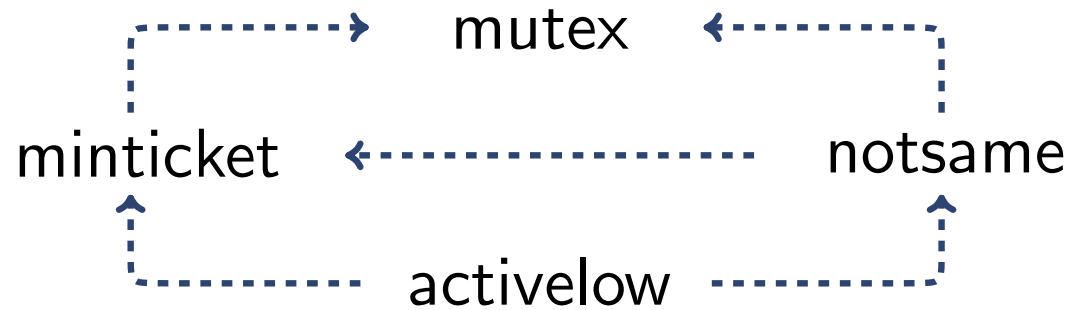
**Every node is invariant** if every node is either:

- ▶ an inductive invariant, or
- ▶ has an incident edge and all VCs are valid

# Parametrized Invariance with Graph Support (g-inv)

To show that  $\mathcal{S}$  satisfies  $\Box\varphi_i(\bar{v}) \wedge \Box\varphi_j(\bar{w})$ :

( I )	$\Theta$	$\rightarrow$	$\varphi_i \wedge \varphi_j$	<b>initiation</b>
( SC <sub>i</sub> )	$\varphi_i, \varphi_j \triangleright$	$\tau^{(t)}$	$\rightarrow \varphi'_i$	<b>self-consecution</b>
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## Theorem

Every node is invariant if every node is either:

- ▶ an inductive invariant, or
- ▶ has an incident edge and all VCs are valid

# Our Contributions

That's for safety... what about **liveness**?



# Our Contributions

## **A** Deductive Verification Techniques for Parametrized Systems

① **Parametrized Invariance**  
Deductive proof rules for concurrent parametrized invariants

 ② **Parametrized Verification Diagrams**  
Diagram based verification for concurrent parametrized liveness properties

③ **Invariant Generation with Self-refelction**  
Automatic parametrized invariant generation using off-the-shelf sequential absint

## **B** Decision Procedures for Complex Data Structures

④ **TL3: A Decidable Theory for Concurrent Lists**  
A Theory and decision procedure for concurrent data structures of the shape of a list

⑤ **TSL<sub>K</sub>: A Decidable Family for Concurrent Bounded Skiplists**  
Theories and decision procedures for concurrent skiplists of at most K levels

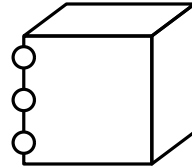
⑥ **TSL: A Decidable Theory for Skiplists with Arbitrary Levels**  
Theory and decision procedure for skiplists with unbounded many levels

## **C** Implementation and Evaluation of our Framework

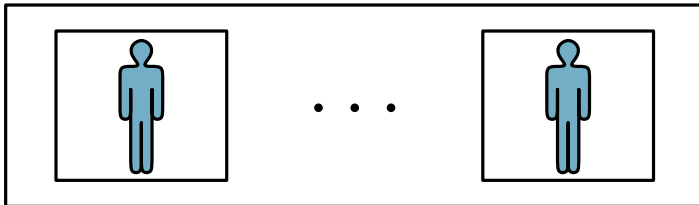
# Verification of Concurrent Data-structures

## Our verification approach

Concurrent DataStructure



Most General Client



$P[N] : P(1) || \dots || P(N)$

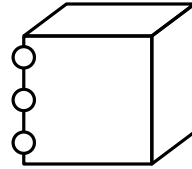
Property

$\varphi$   
LTL ( $\square, \diamond, \mathcal{U}, \dots$ )

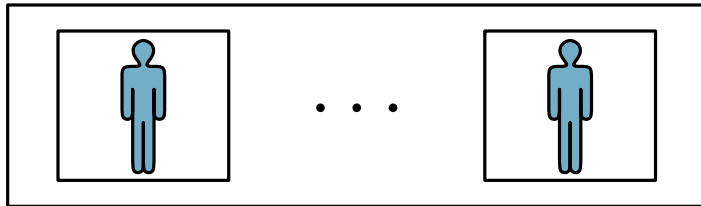
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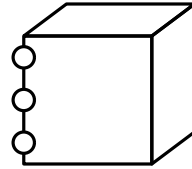
Liveness  
Property

LTL ( $\square, \diamond, \mathcal{U}, \dots$ )  $\nearrow \varphi$

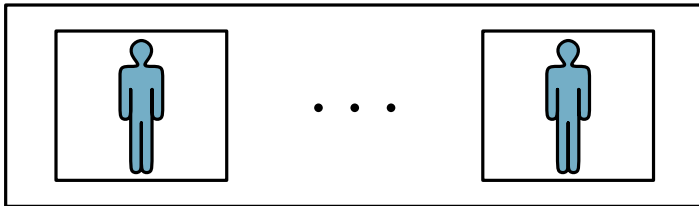
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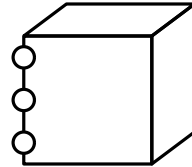
Liveness  
Property

$\varphi^{(k)}$

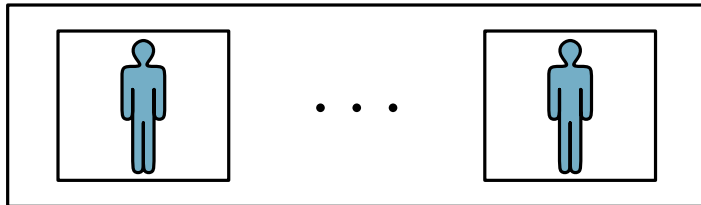
# Verification of Concurrent Data-structures

## Our verification approach

Concurrent DataStructure



Most General Client



$$P[N] : P(1) || \dots || P(N)$$



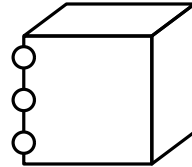
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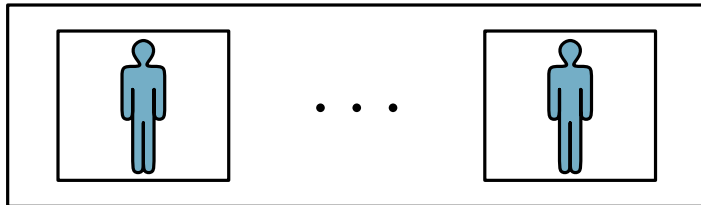
# Verification of Concurrent Data-structures

## Our verification approach

Concurrent DataStructure



Most General Client



$$P[N] : P(1) || \dots || P(N)$$

Diagram

$\mathcal{D}$

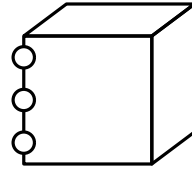
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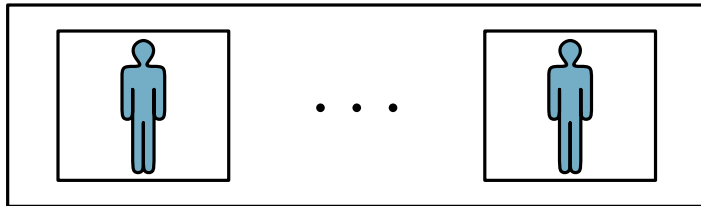
# Verification of Concurrent Data-structures

## Our verification approach

Concurrent DataStructure



Most General Client



$P[N] : P(1) || \dots || P(N)$

Diagram

$\models \mathcal{D}$

Verification Conditions:

- ▶ Initiation
- ▶ Consecution
- ▶ Acceptance
- ▶ Fairness

Liveness  
Property

$\models \varphi^{(k)}$

Satisfaction  
(Model Checking)

# The Need of Parametrized Diagrams

System:      $\text{SETMUTEX}(T_1) \parallel \text{SETMUTEX}(T_2)$

**global**

int *avail* := 0

set<int, tid> *bag* :=  $\emptyset$

**procedure**  $\text{SETMUTEX}$

int *ticket*

**begin**

1: **loop**

2:     **noncritical**

3:      $\left\langle \begin{array}{l} \textit{ticket} := \textit{avail} ++ \\ \textit{bag.add}(\textit{ticket}, \textit{myId}) \end{array} \right\rangle$

4:     **await** (*bag.min* == *ticket*)

5:     **critical**

6:     *bag.remove(ticket, myId)*

7: **end loop**

**end procedure**



# The Need of Parametrized Diagrams

System:  $\text{SETMUTEX}(T_1) \parallel \text{SETMUTEX}(T_2)$

Spec:  $\square (pc_{T_1} = 3 \rightarrow \diamond pc_{T_1} = 5)$

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$T_1$  not interested

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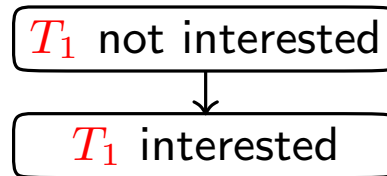
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```
global
  int avail := 0
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```

```
procedure SETMUTEX
```

```
  int ticket
```

```
  begin
```

```
  1: loop
```

```
  2:   noncritical
```

```
  3:    $\langle$  ticket := avail ++  
  4:   bag.add(ticket, myld)  $\rangle$ 
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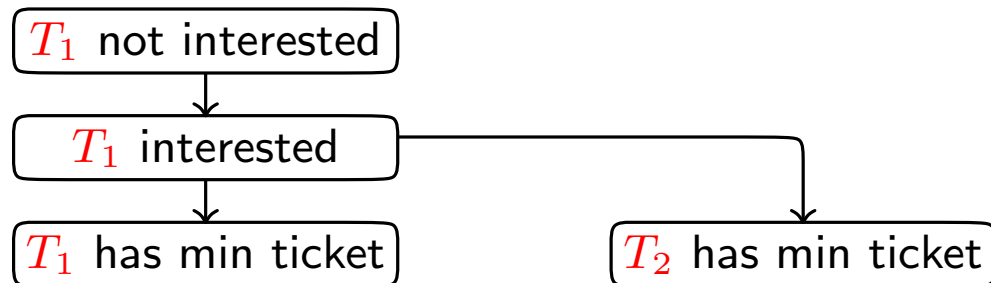
```
  5:   await (bag.min == ticket)
```

```
  6:   critical
```

```
  7:   bag.remove(ticket, myld)
```

```
  8: end loop
```

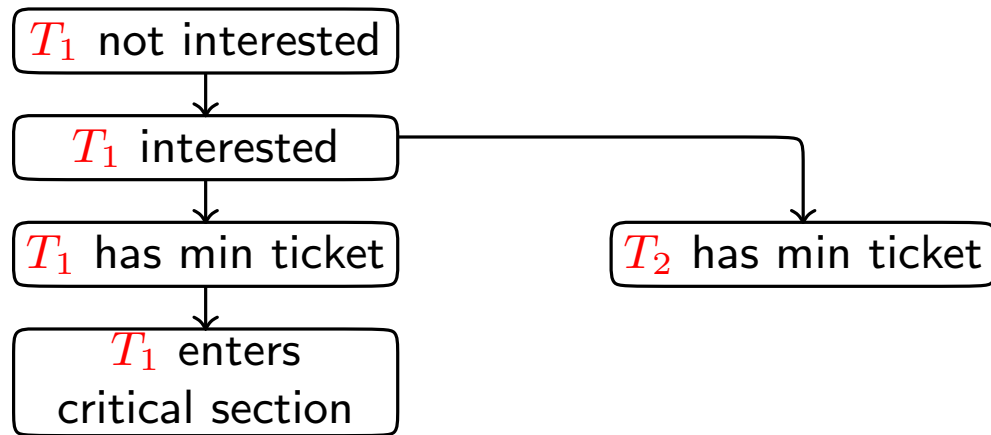
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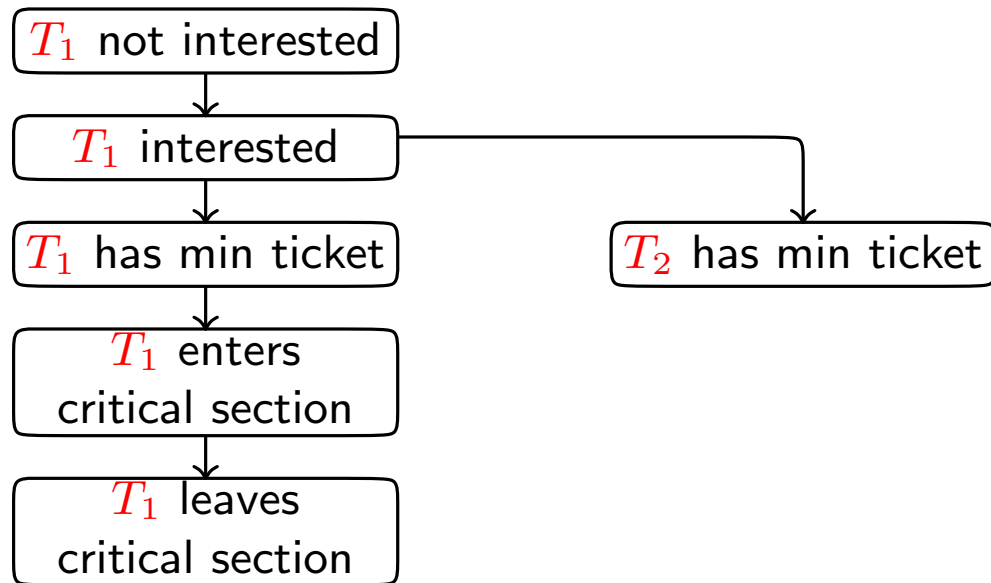
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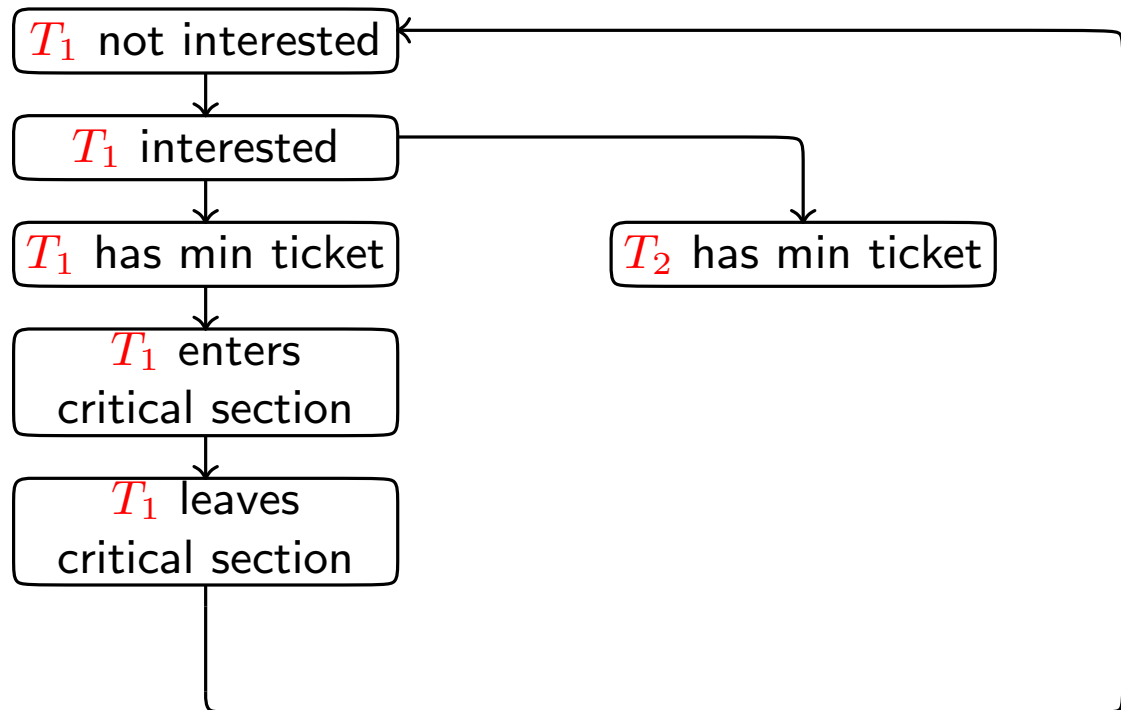
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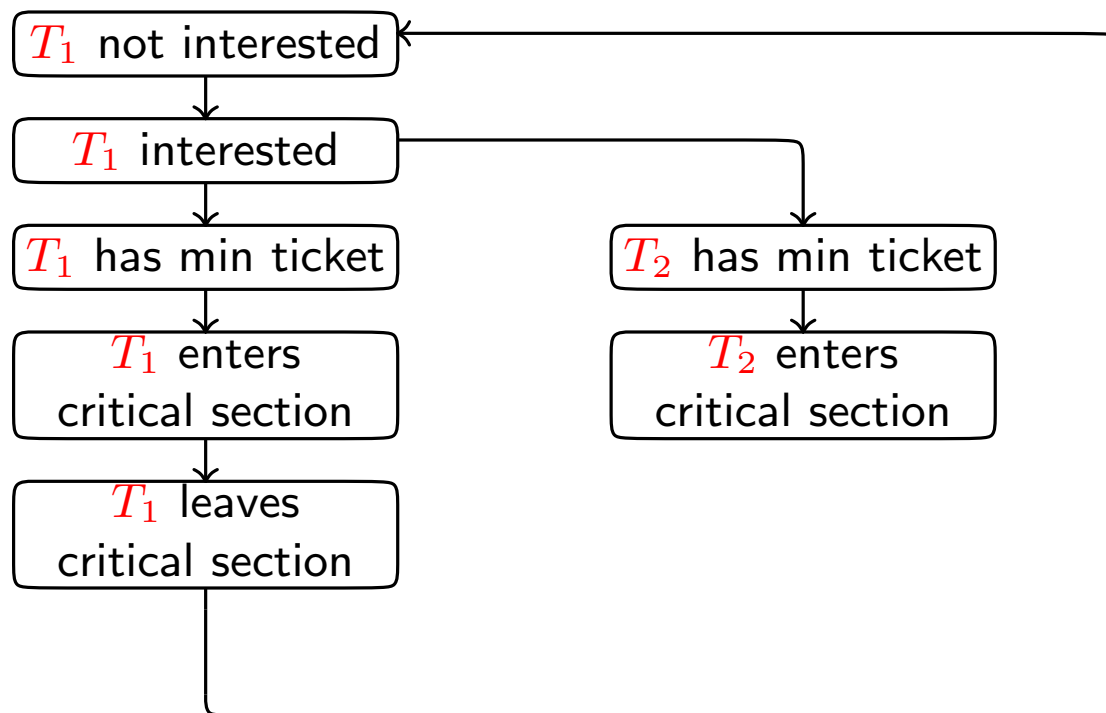
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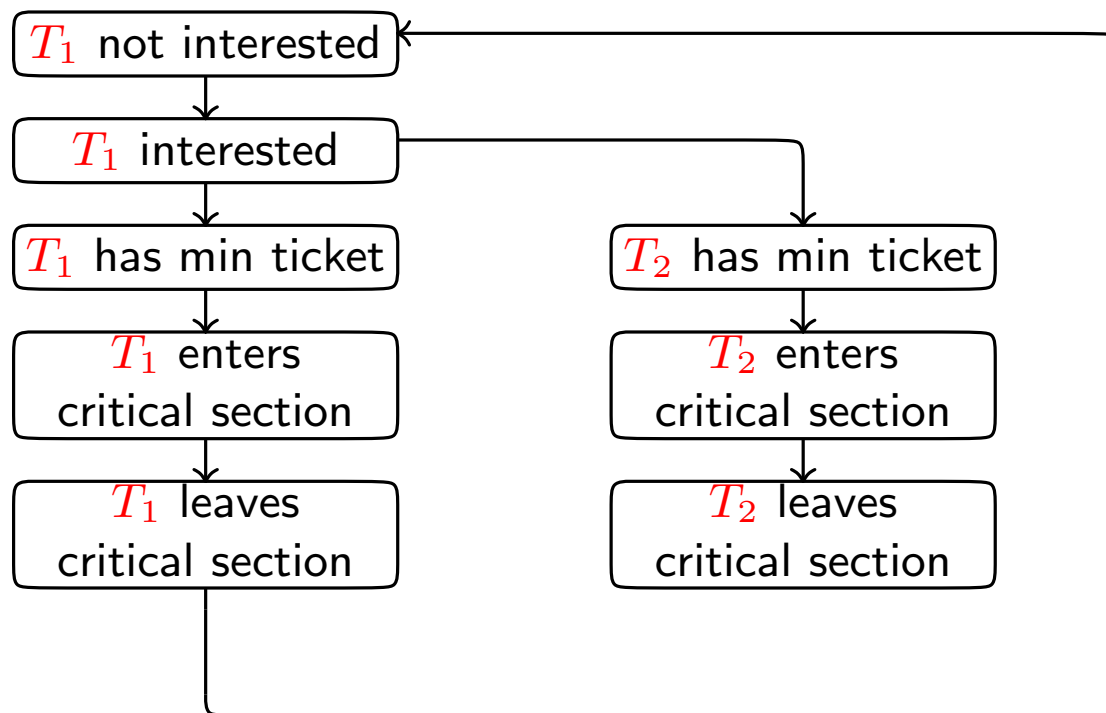
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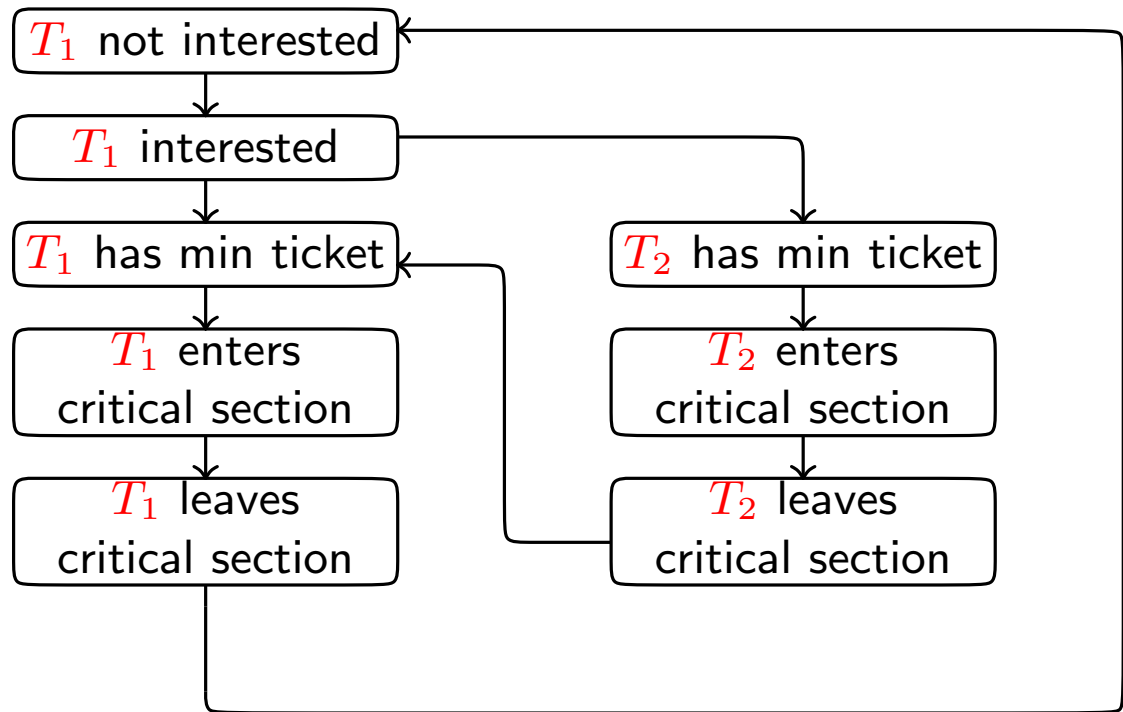
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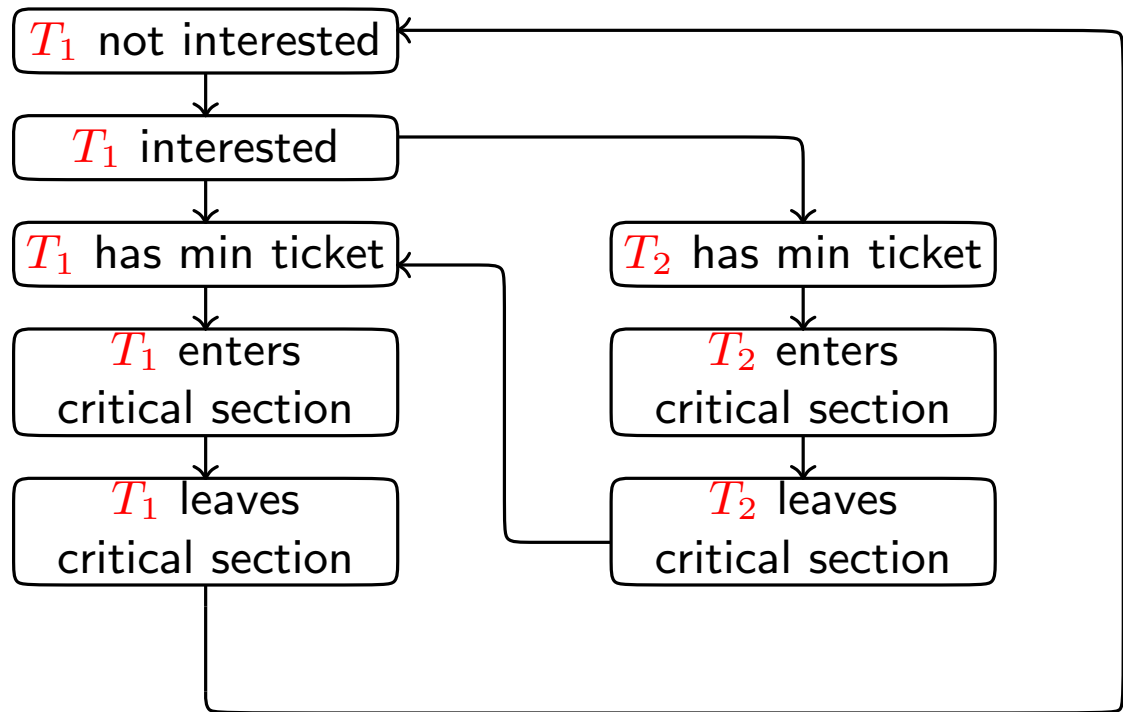
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System:  $\text{SETMUTEX}(T_1) \parallel \text{SETMUTEX}(T_2)$

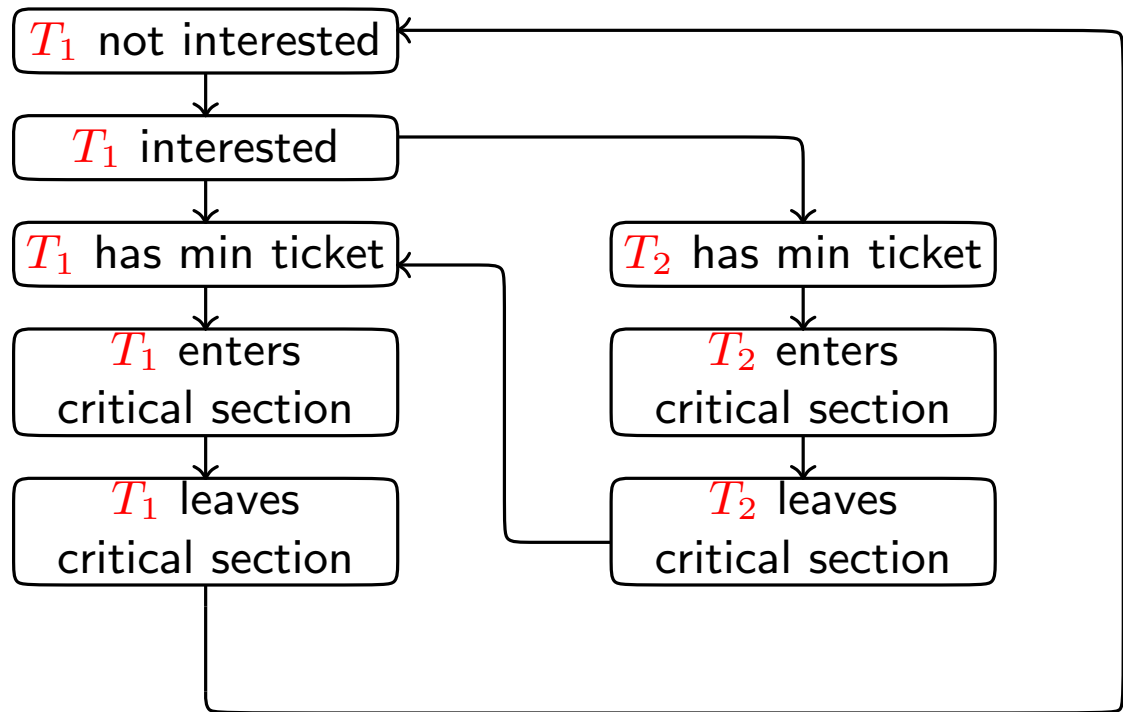
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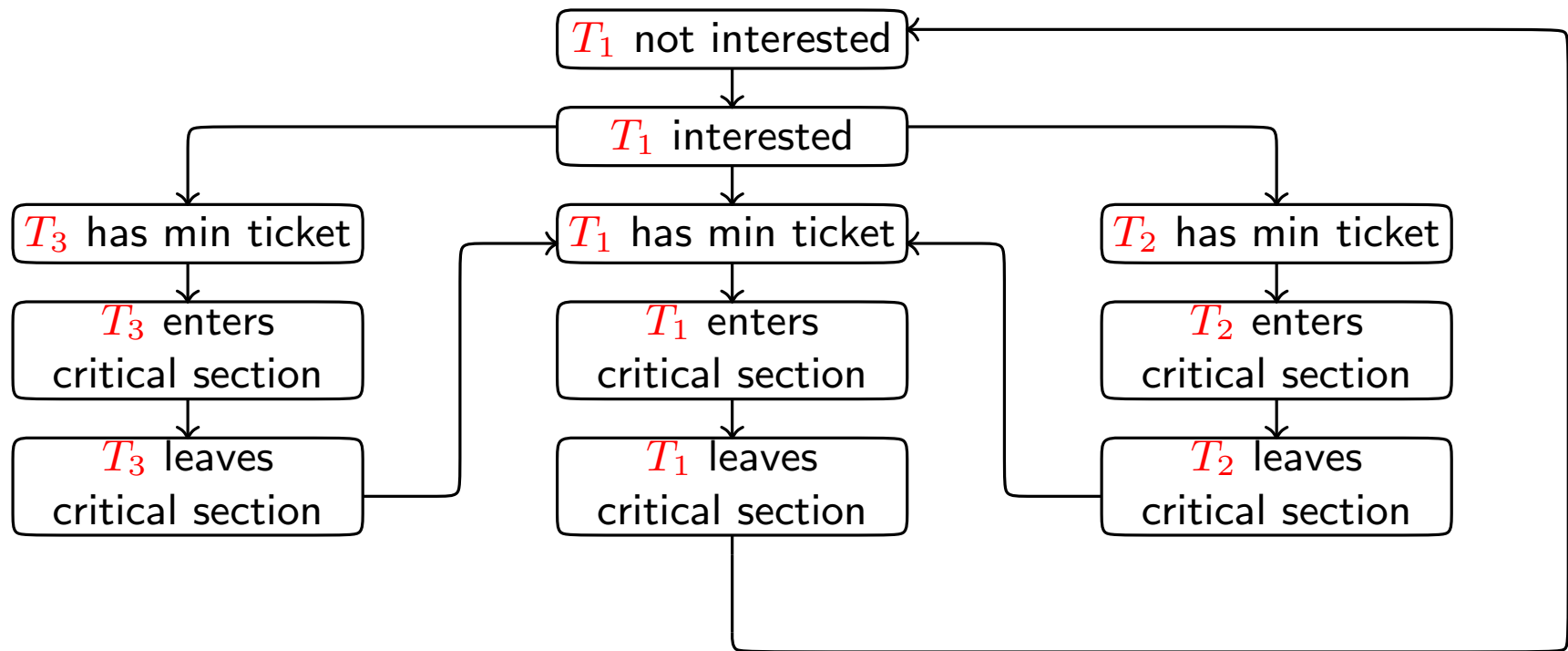
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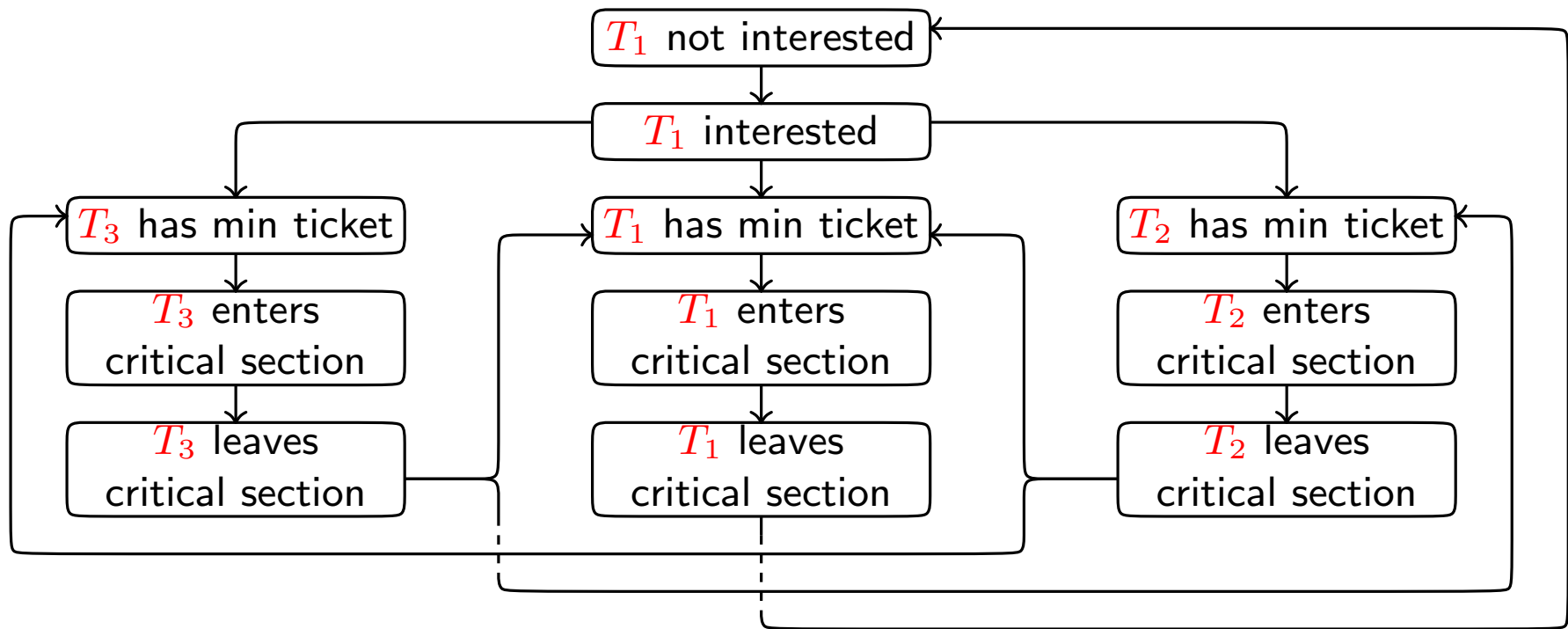
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# Solution: Parametrized Verification Diagrams

## Problem

- ▶ **Not a single diagram** for arbitrary number of threads
- ▶ **Unbounded** number of **verification conditions**

# Solution: Parametrized Verification Diagrams

## Problem

- ▶ **Not a single diagram** for arbitrary number of threads
- ▶ **Unbounded** number of **verification conditions**

## Our solution

- ▶ **Unique diagram** for arbitrary number of threads
- ▶ **Finite and bounded** number of verification conditions

Parametrized Verification Diagrams exploit  
symmetry



# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD

**Nodes**

*a*

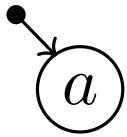
*b*

  
**Parametrized VD**

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are an extension of GVD

**Initial Node**

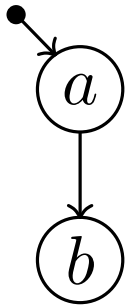


**Parametrized VD**

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD

**Edges**

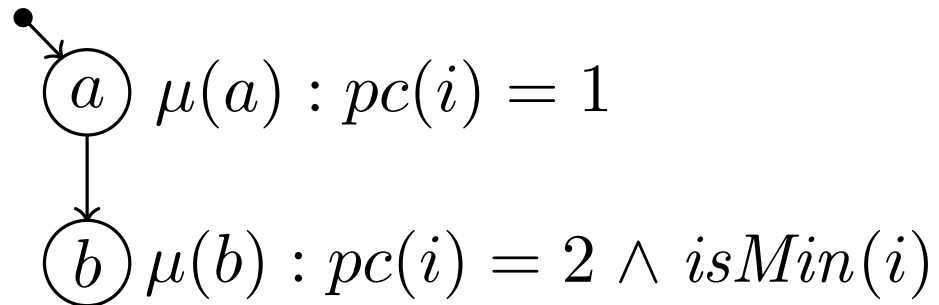


**Parametrized VD**

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD

## Node labeling

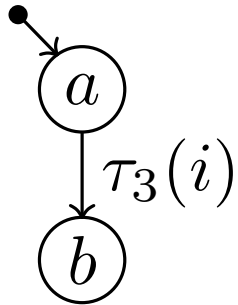


**Parametrized VD**

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD

**Edge labeling**

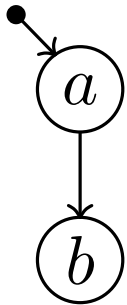


**Parametrized VD**

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD

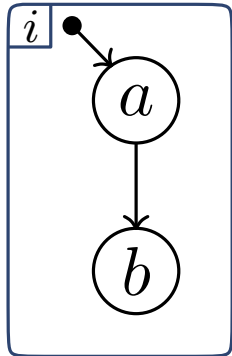
## Ranking functions



⏟  
Parametrized VD

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD
- ▶ Includes the notion of **boxes**

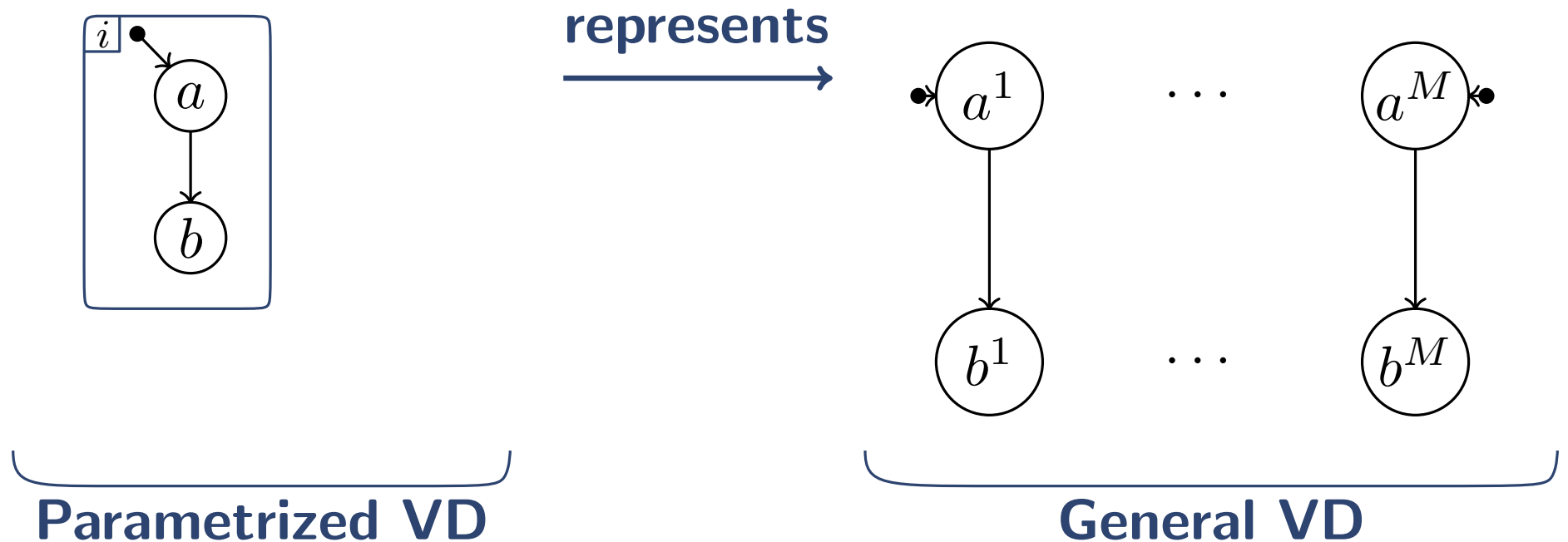


⏟  
**Parametrized VD**

# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD
- ▶ Includes the notion of **boxes**

For  $M$  threads

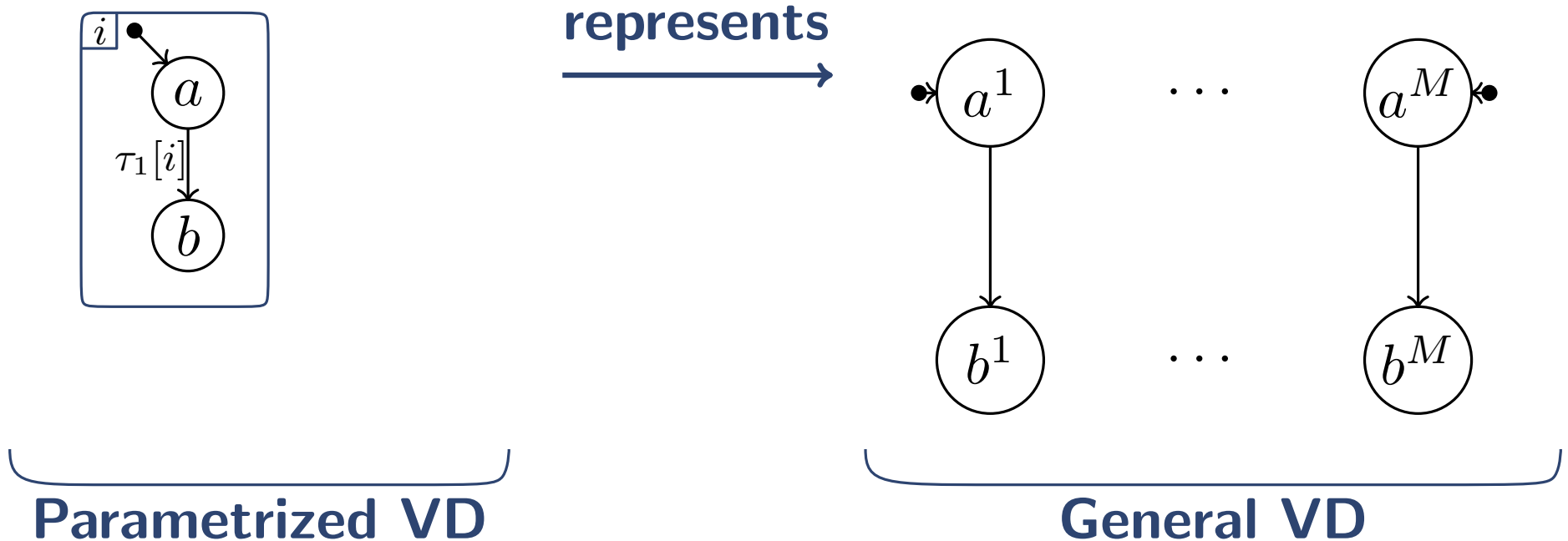




# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD
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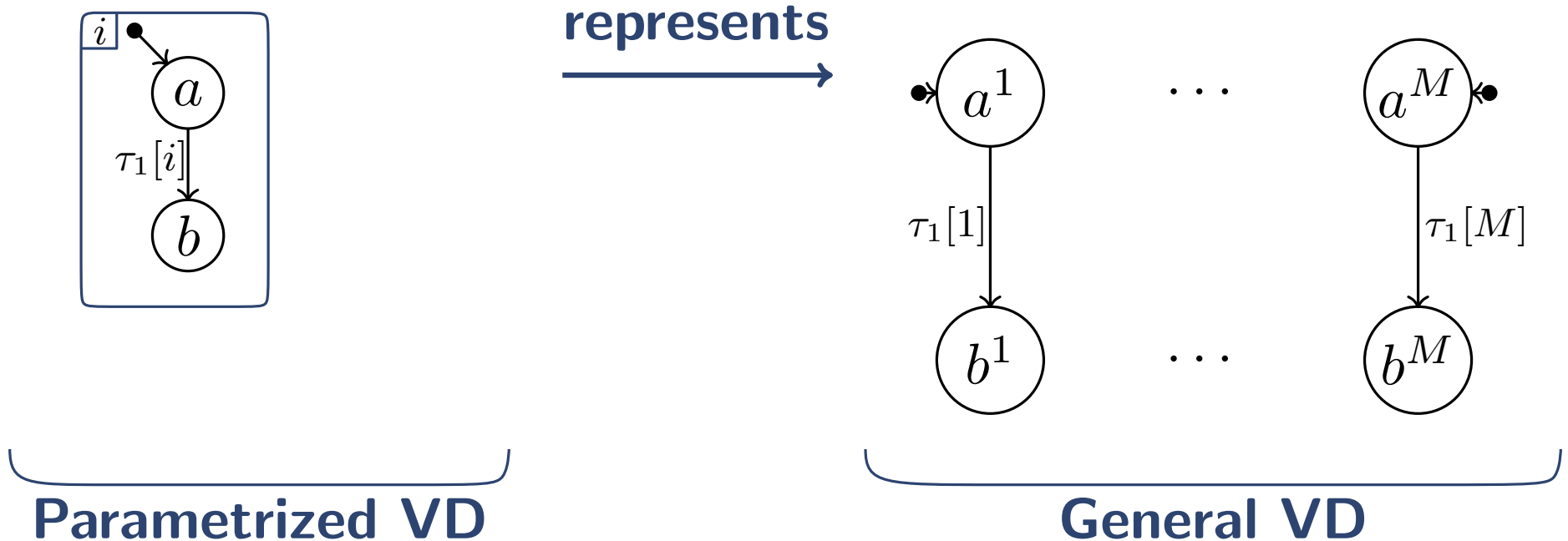
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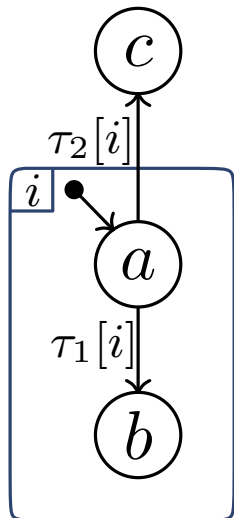
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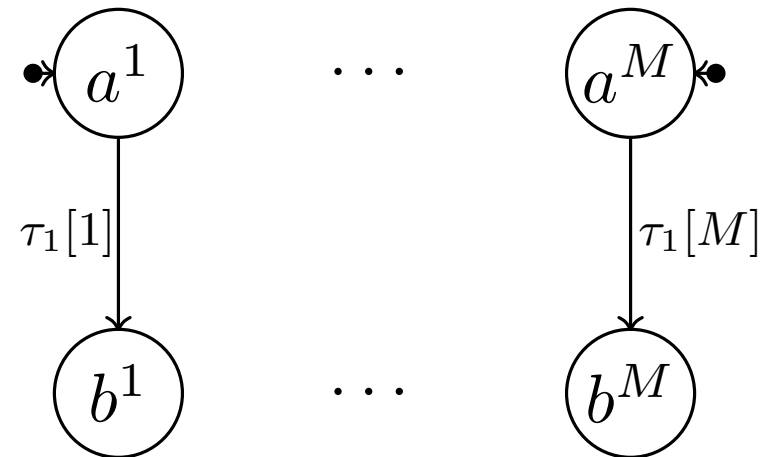
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- ▶ PVDS are **an extension** of GVD
- ▶ Includes the notion of **boxes**

For  $M$  threads



represents



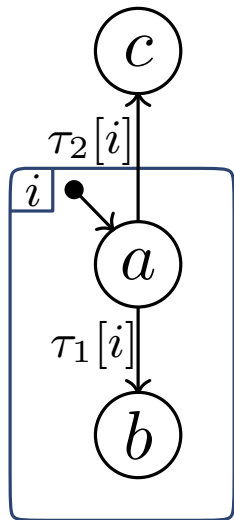
Parametrized VD

General VD

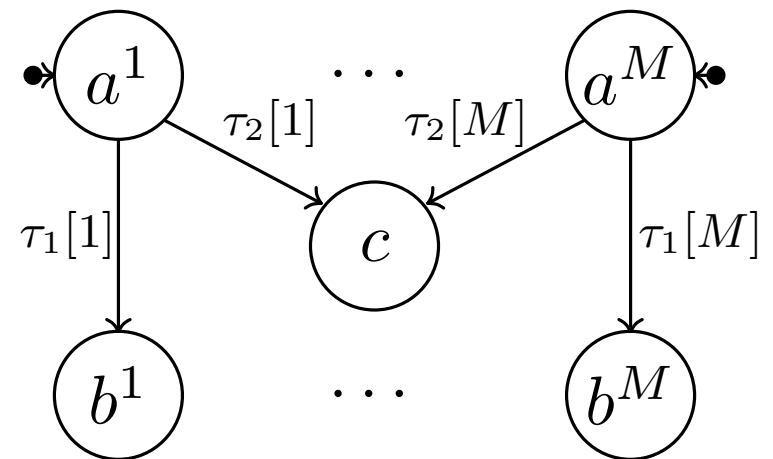
# Parametrized Verification Diagrams: Sketch

- ▶ PVDS are **an extension** of GVD
- ▶ Includes the notion of **boxes**

For  $M$  threads



represents



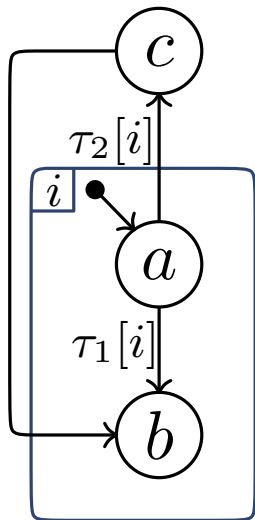
Parametrized VD

General VD

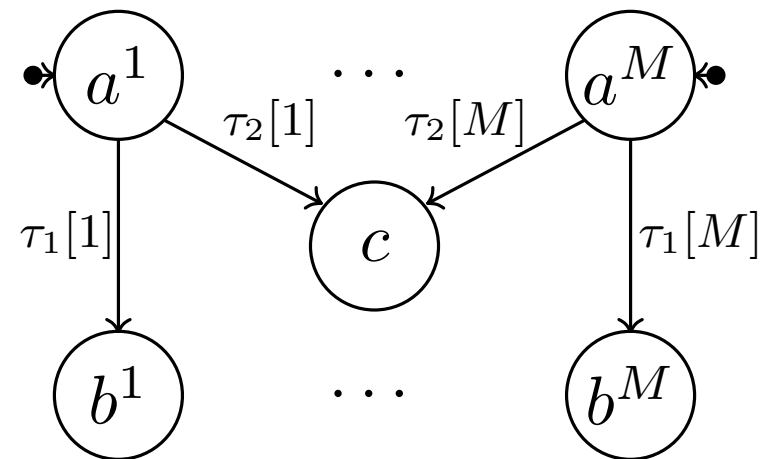
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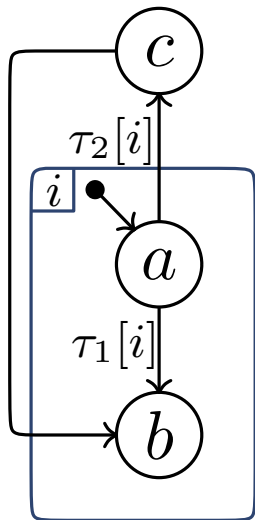
**Parametrized VD**

**General VD**

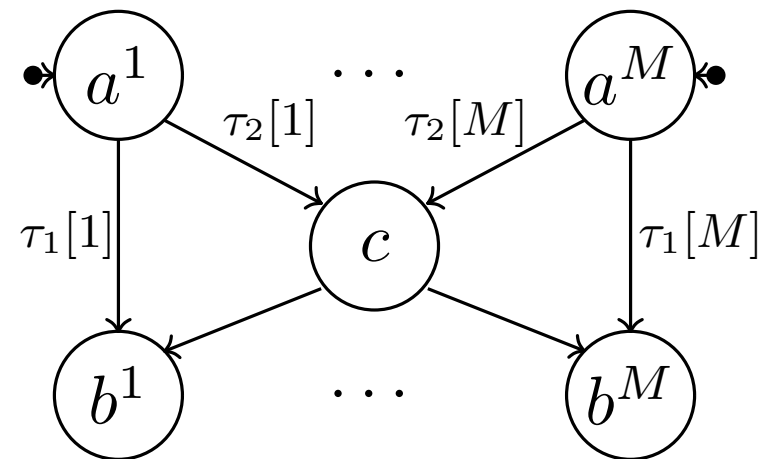
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represents



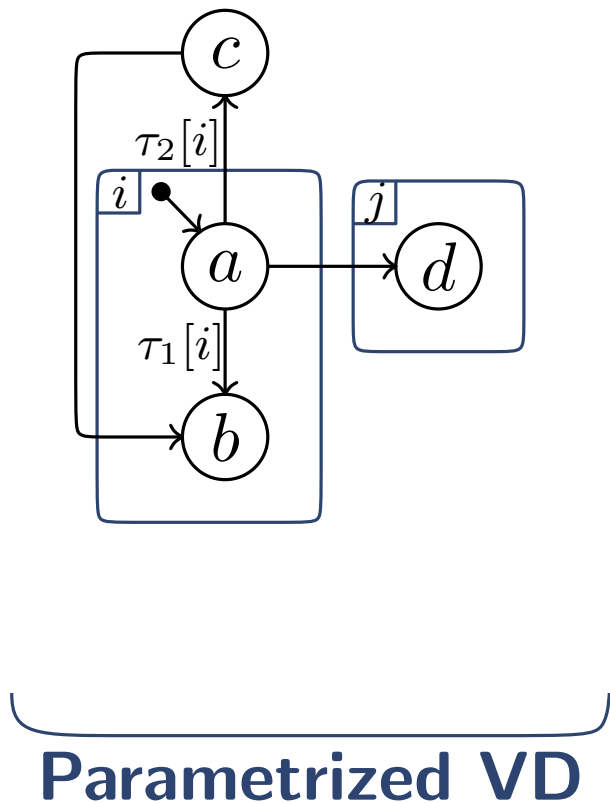
Parametrized VD

General VD

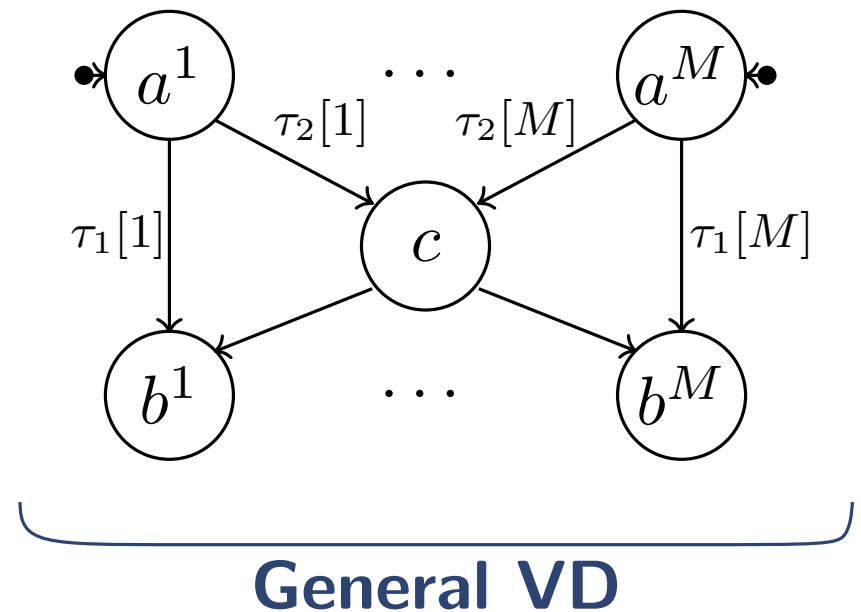
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For  $M$  threads



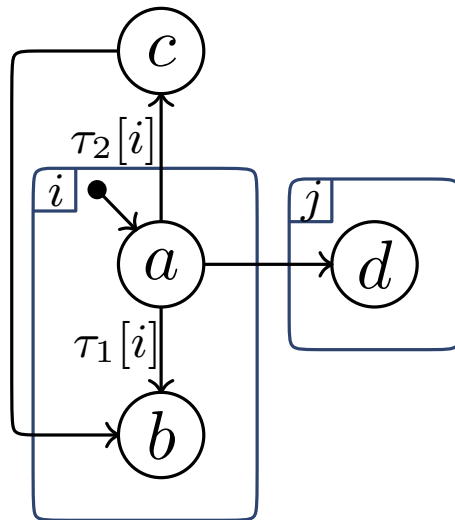
represents



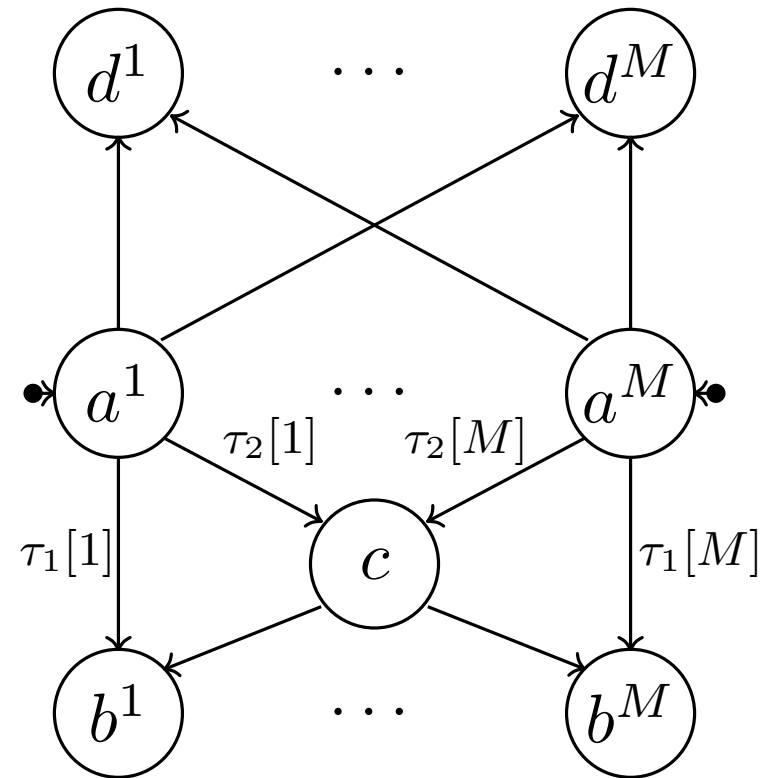
# Parametrized Verification Diagrams: Sketch

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For  $M$  threads



represents



**Parametrized VD**

**General VD**

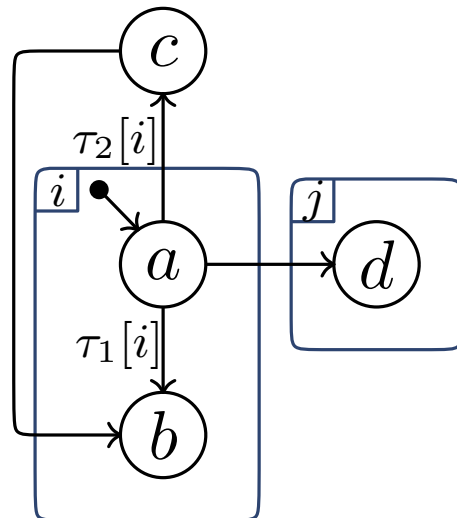


# Parametrized Verification Diagrams: Sketch

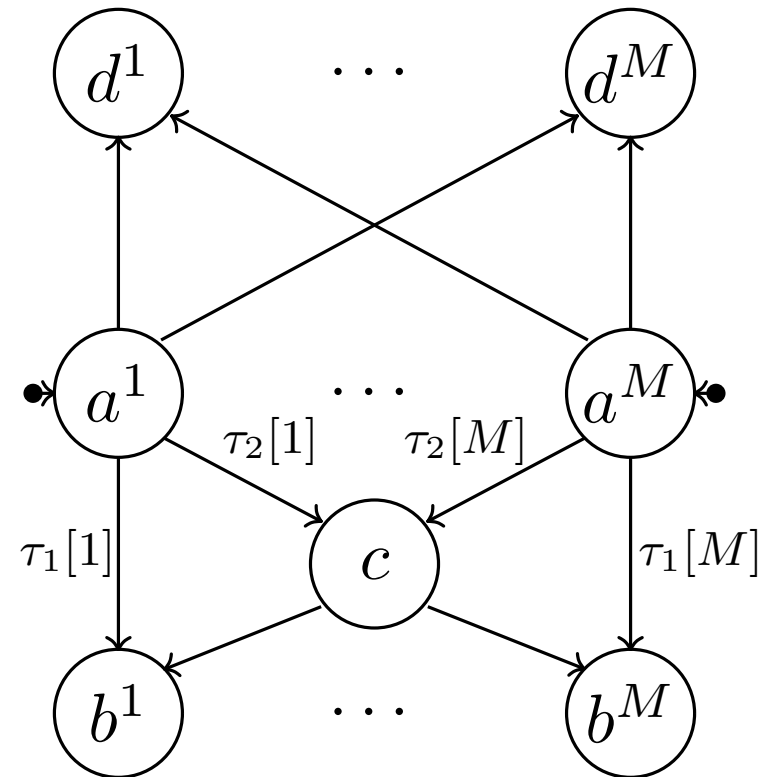
- ▶ PVDS are **an extension** of GVD
- ▶ Includes the notion of **boxes**

A PVD **abstracts all instantiations** of a parametric system

For  $M$  threads



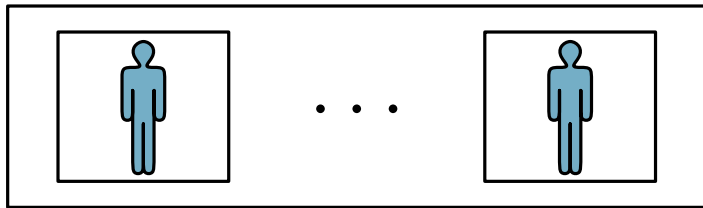
represents



Parametrized VD

General VD

# Verification Conditions for Parametrized Diagrams



$P[N] : P(1) || \dots || P(N)$

Diagram

Property

$\models$

$\mathcal{D}$

$\models$

$\varphi^{(k)}$

Verification Conditions:

- ▶ Initiation
- ▶ Consecution
- ▶ Acceptance
- ▶ Fairness

# Verification Conditions for Parametrized Diagrams

► **Initiation**       $\Theta \rightarrow \mu(N_0)$

# Verification Conditions for Parametrized Diagrams

► **Initiation**

► **Consecution:** For every  $n \in N$  and  $\tau \in \mathcal{T}$ ,

$$\bigvee_{n \rightarrow m} \mu(n) \wedge \tau(i) \rightarrow \mu(m')$$

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## Self-Consecution

$$\bigvee_{n \rightarrow m} \mu(n) \wedge \tau(i) \rightarrow \mu(m') \quad \text{for all } i \in \text{Voc}(n, m)$$

# Verification Conditions for Parametrized Diagrams

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## Others-Consecution

$$\bigvee_{n \rightarrow m} \mu(n) \wedge \tau(j) \wedge j \neq i \rightarrow \mu(m') \quad \text{for fresh } j \notin \text{Voc}(n, m)$$

# Verification Conditions for Parametrized Diagrams

## ► Initiation

► **Consecution:** For every  $n \in N$  and  $\tau \in \mathcal{T}$ ,

$$\bigvee_{n \rightarrow m} \mu(n) \wedge \tau(i) \rightarrow \mu(m')$$

How many VCs?

$$|\mathcal{T}| \cdot |\mathcal{V}|$$

Self-Consecution

$$\bigvee_{n \rightarrow m} \mu(n) \wedge \tau(i) \rightarrow \mu(m')$$

for all  $i \in \text{Voc}(n, m)$

Others-Consecution

$$\bigvee_{n \rightarrow m} \mu(n) \wedge \tau(j) \wedge j \neq i \rightarrow \mu(m')$$

for fresh  $j \notin \text{Voc}(n, m)$

# Verification Conditions for Parametrized Diagrams

- ▶ **Initiation**
- ▶ **Consecution**
- ▶ **Acceptance:** For every  $(B, G, \delta)$  and all edges  $n \rightarrow_e m$ :



# Verification Conditions for Parametrized Diagrams

► **Initiation**

► **Consecution**

► **Acceptance:** For every  $(B, G, \delta)$  and all edges  $n \rightarrow_e m$ :

Self-Acceptance (for all  $i \in \text{Voc}(n, m)$ )

$$(\mu(n) \wedge \tau(i) \wedge \mu(m')) \rightarrow \delta(n) > \delta(m) \quad \text{if } e \in B$$

$$(\mu(n) \wedge \tau(i) \wedge \mu(m')) \rightarrow \delta(n) \geq \delta(m) \quad \text{if } e \in E \setminus (B \cup G)$$

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# Verification Conditions for Parametrized Diagrams

▶ **Initiation**

▶ **Consecution**

▶ **Acceptance**

▶ **Fairness**      For each  $n \rightarrow_e m$  with  $\eta(e) = \tau(i)$

# Verification Conditions for Parametrized Diagrams

▶ **Initiation**

▶ **Consecution**

▶ **Acceptance**

▶ **Fairness**      For each  $n \rightarrow_e m$  with  $\eta(e) = \tau(i)$

$$\mu(n) \rightarrow En(\tau(i))$$

$$\mu(n) \wedge \tau(i) \rightarrow \mu(m')$$

# Verification Conditions for Parametrized Diagrams

- ▶ **Initiation**
- ▶ **Consecution**
- ▶ **Acceptance**
- ▶ **Fairness**
- ▶ **Satisfaction**

$$\mu(n) \rightarrow f(n)$$

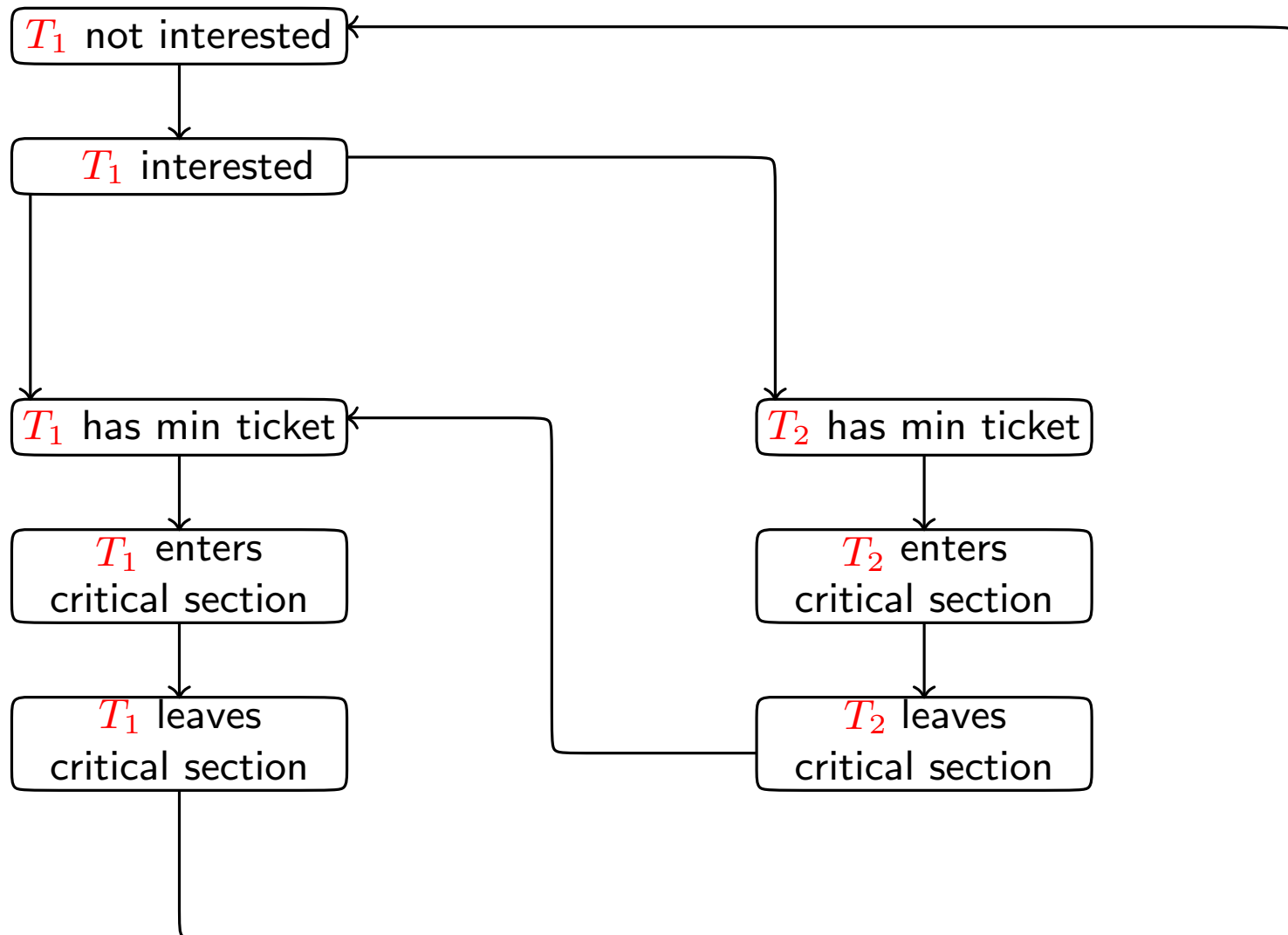
ModelCheck( $\mathcal{D} \models \varphi$ )

# Mutual Exclusion Algorithm (revisited)

$$\varphi(k) = \square(pc(k) = 3 \rightarrow \diamond pc(k) = 5)$$

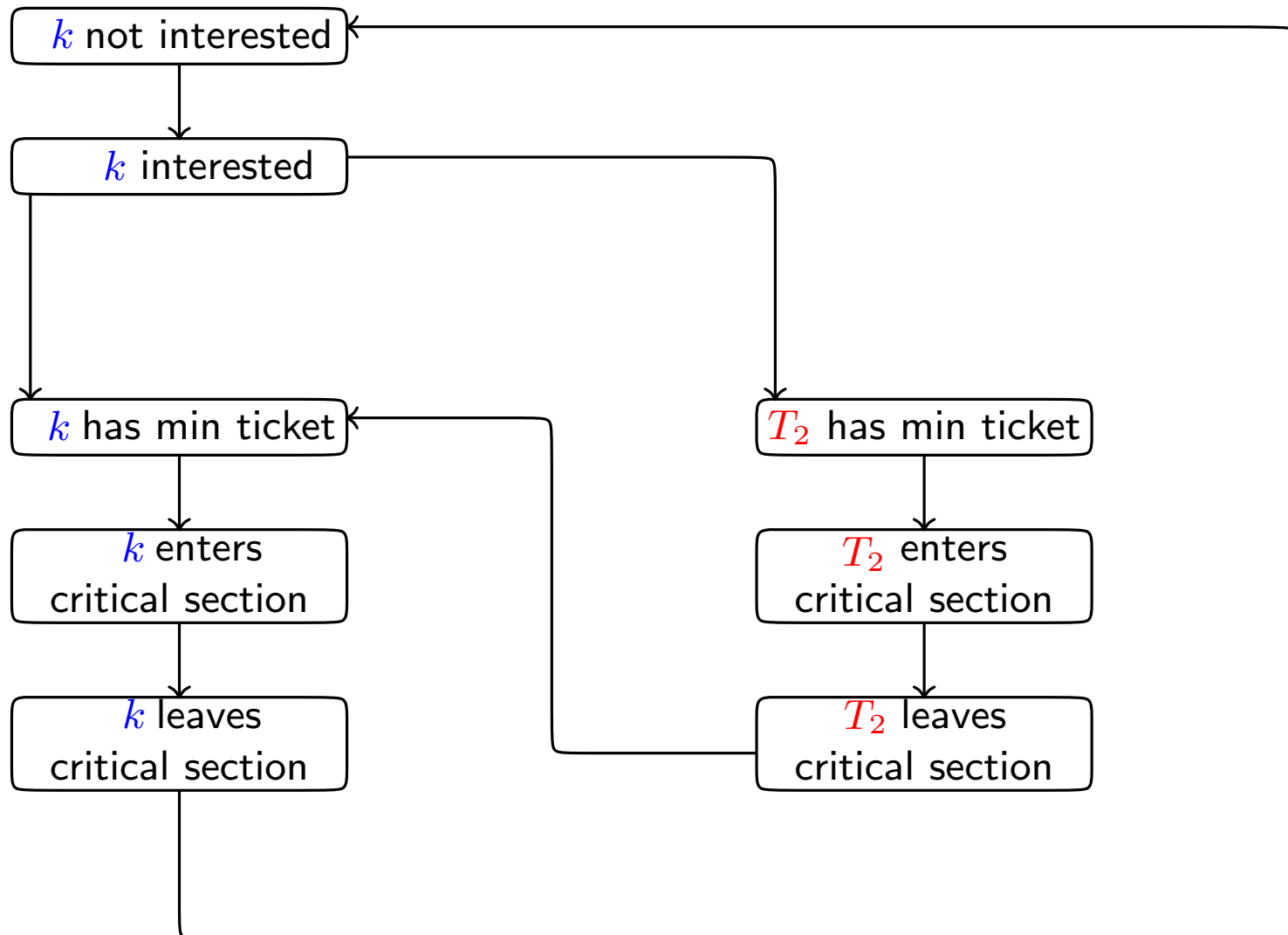
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# Mutual Exclusion Algorithm (revisited)

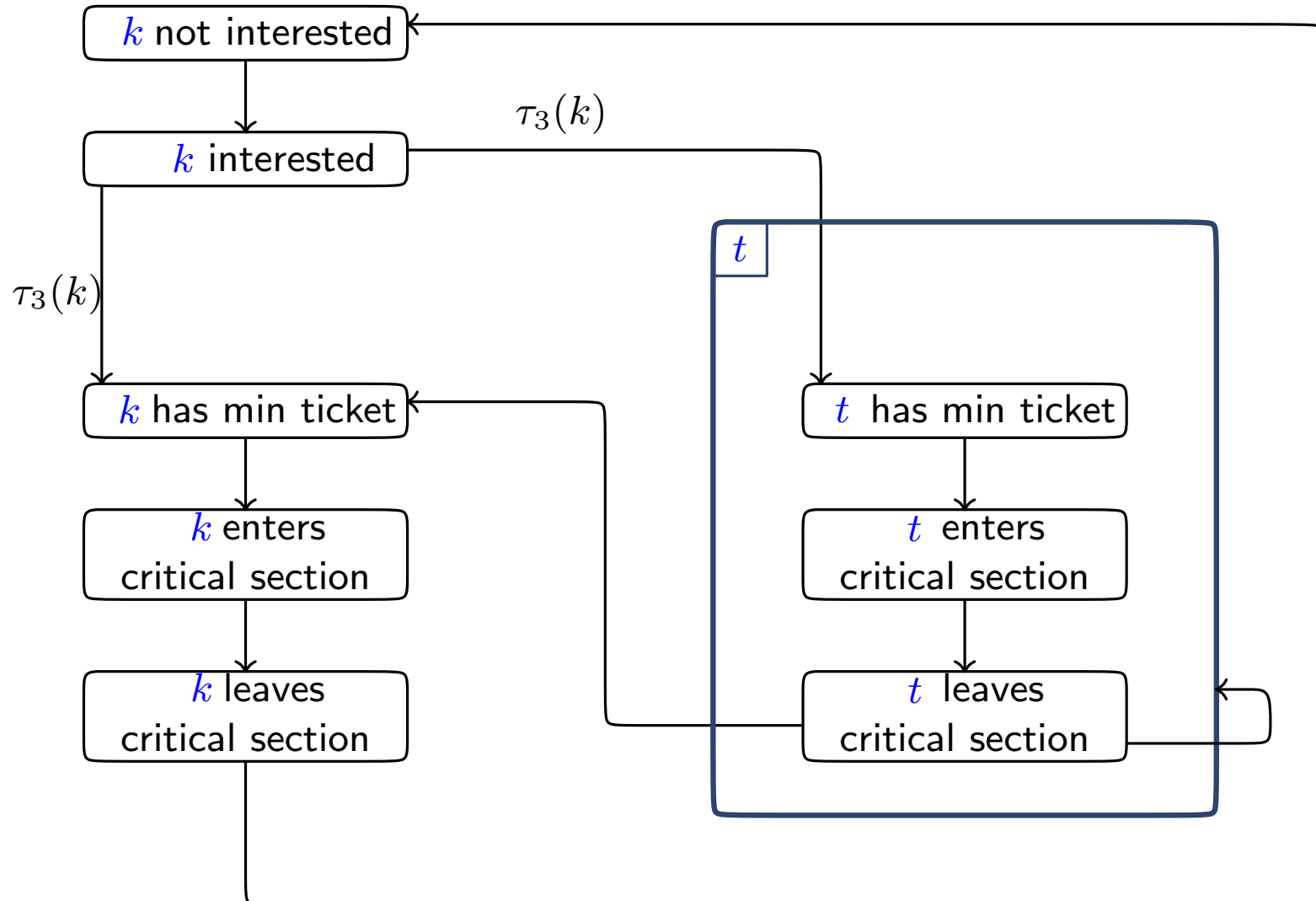
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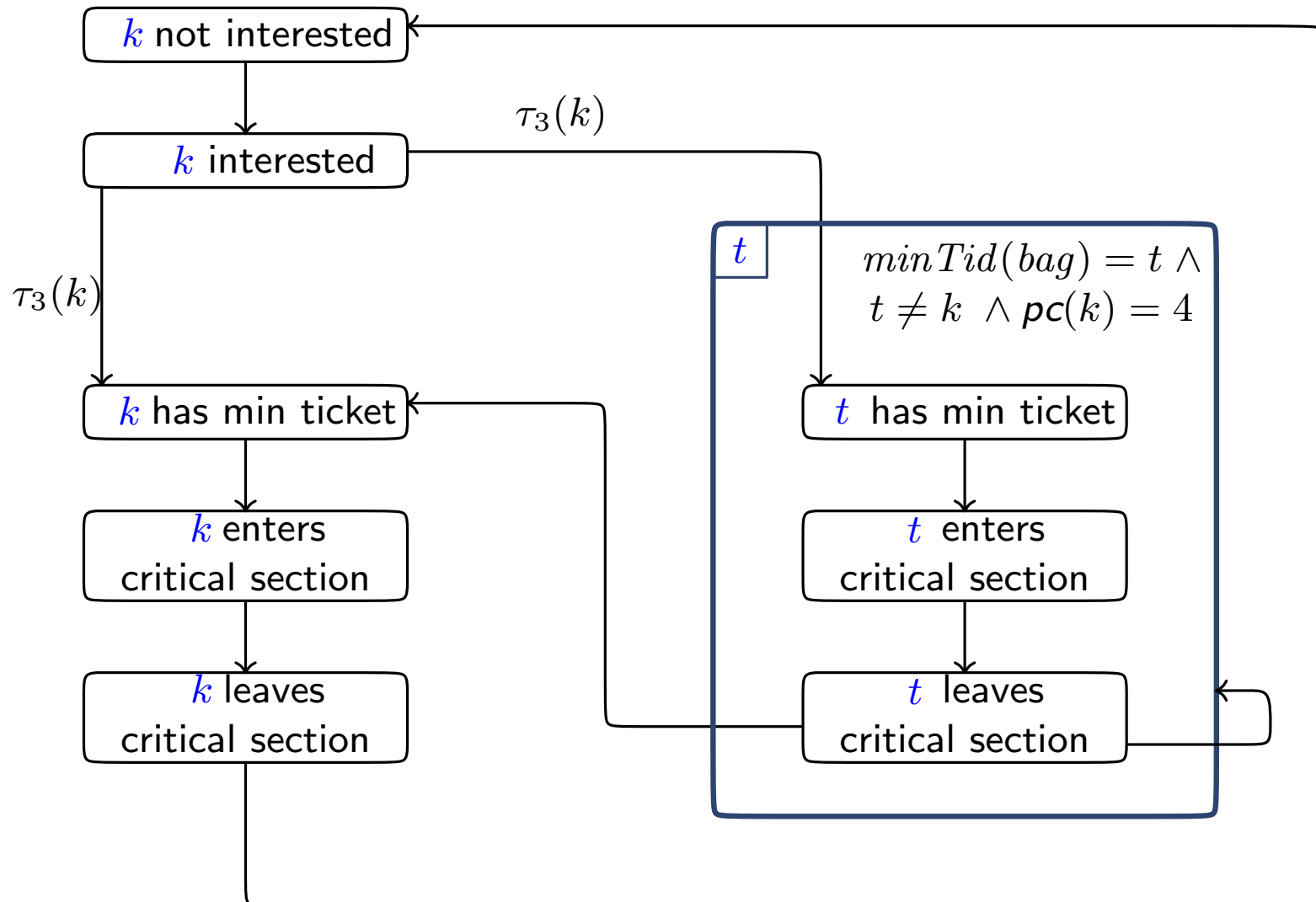
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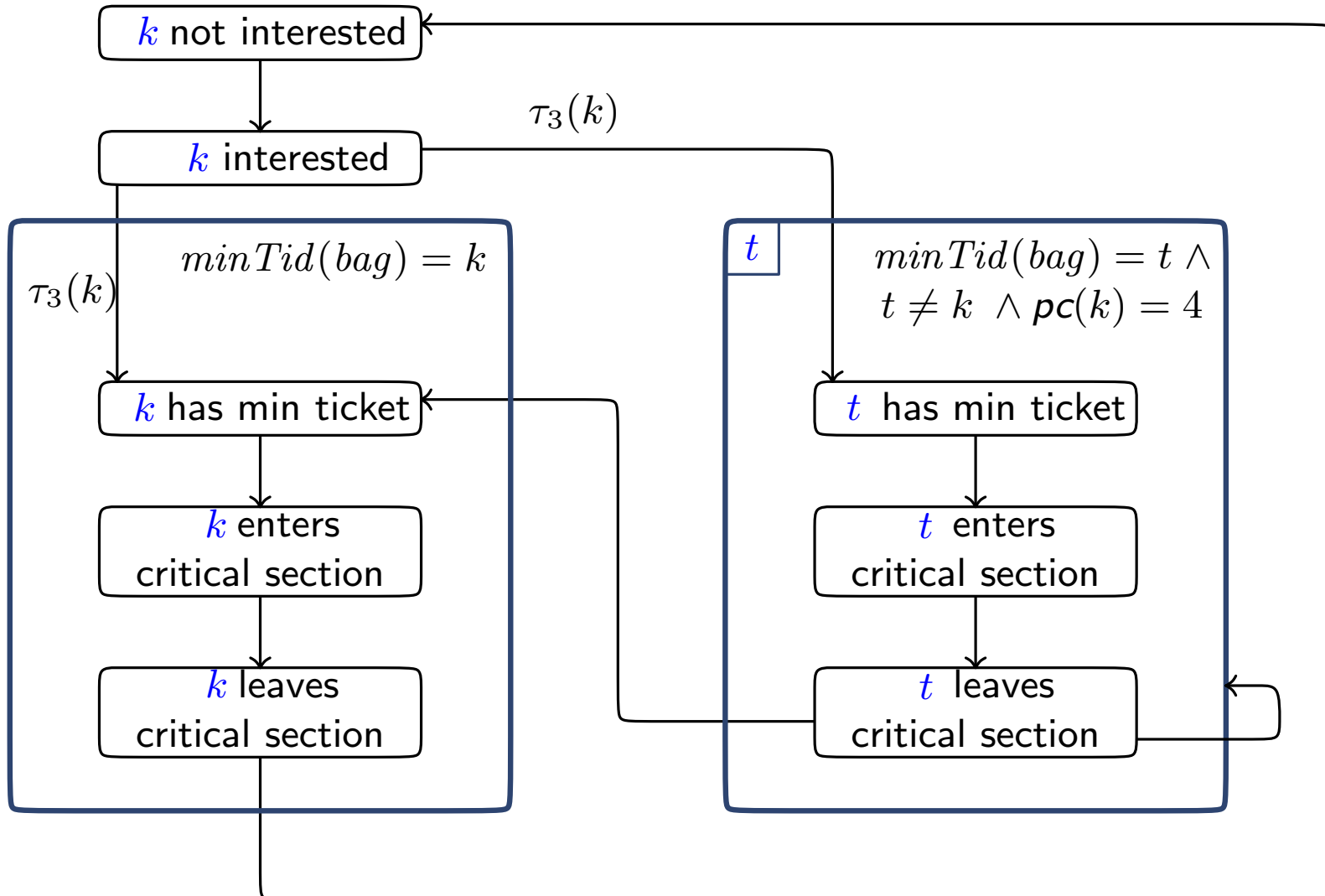
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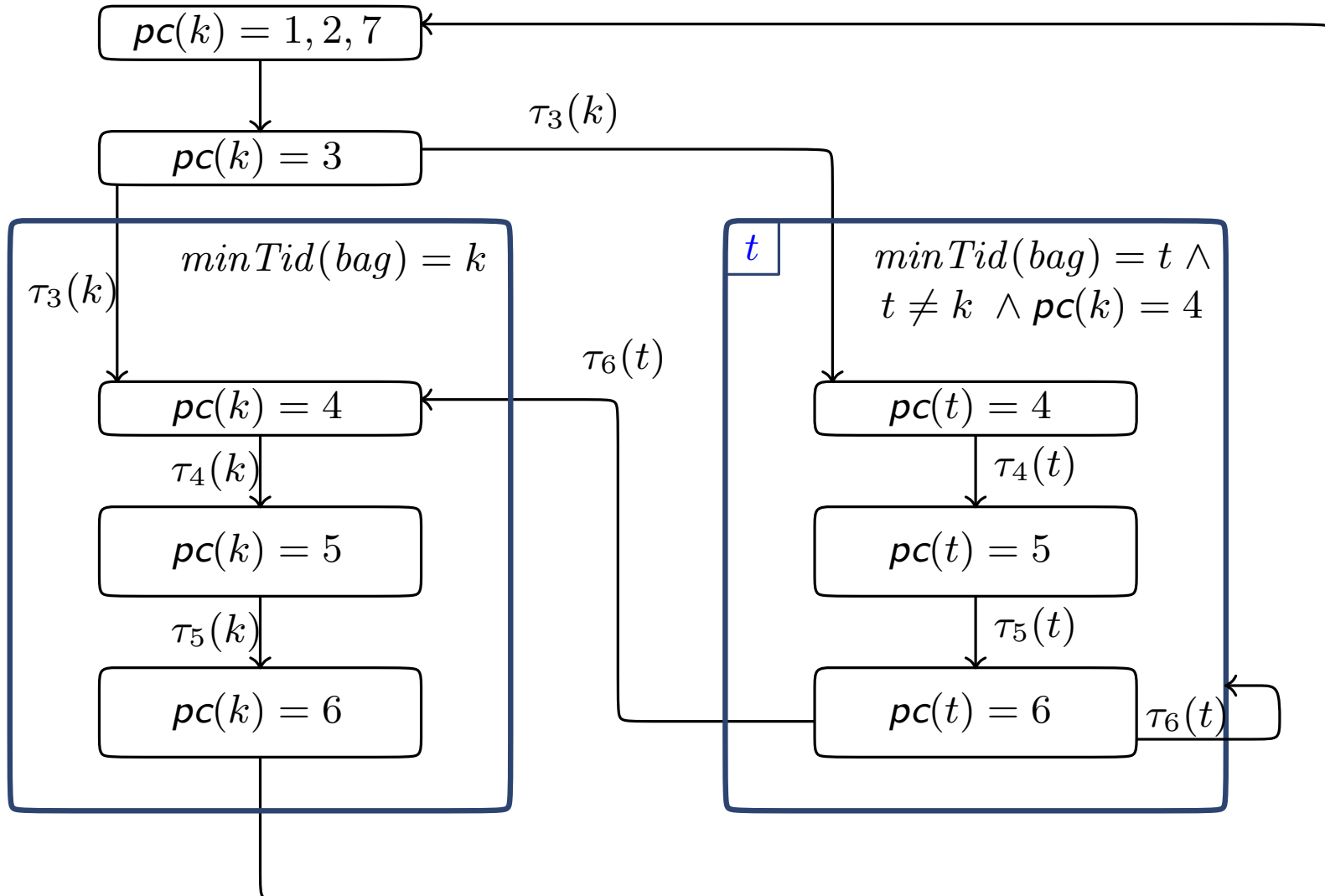
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$$\varphi(k) = \square(pc(k) = 3 \rightarrow \diamond pc(k) = 5)$$



# Mutual Exclusion Algorithm (revisited)

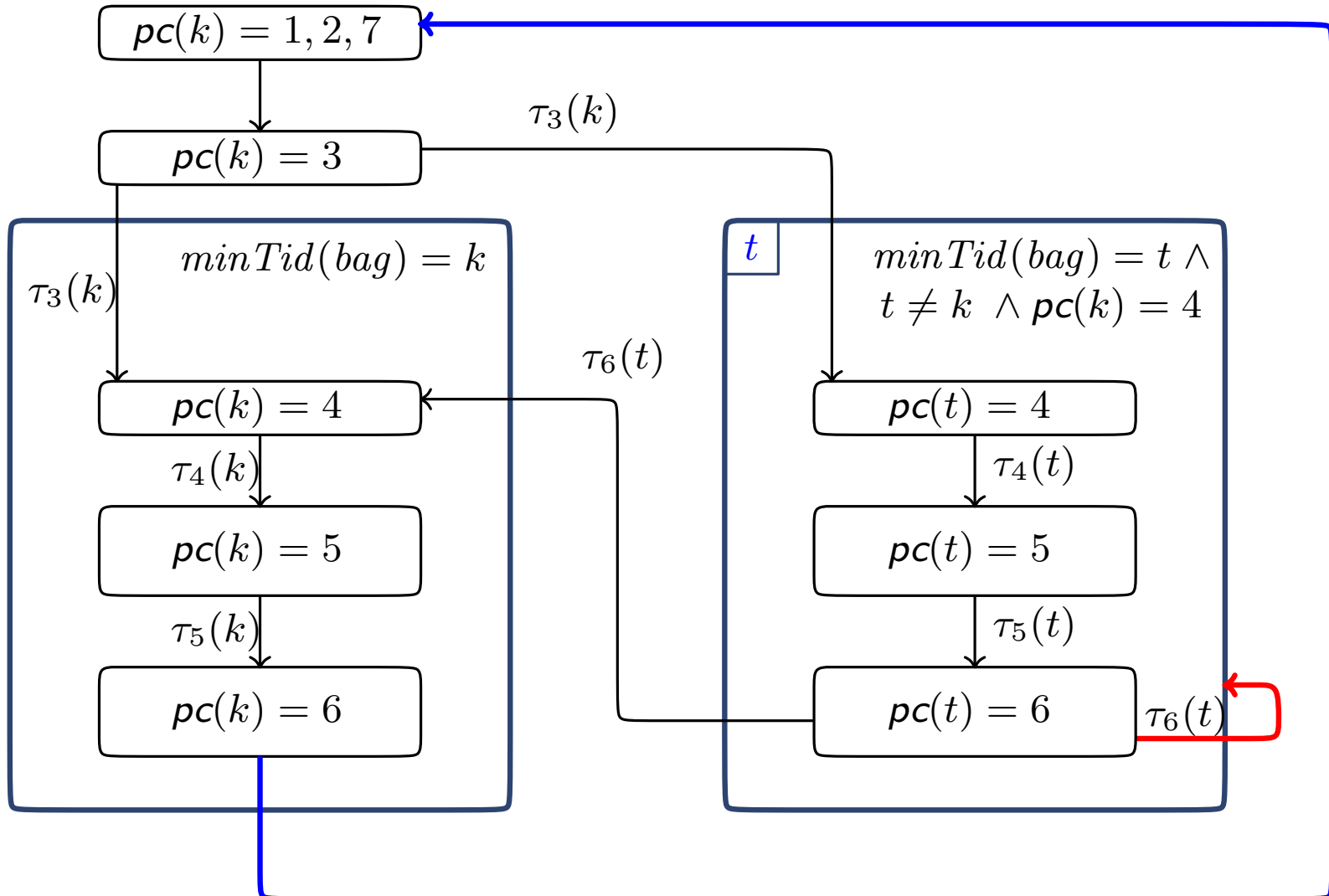
$$\varphi(k) = \square(pc(k) = 3 \rightarrow \diamond pc(k) = 5)$$



# Mutual Exclusion Algorithm (revisited)

$$\varphi(k) = \square(pc(k) = 3 \rightarrow \diamond pc(k) = 5)$$

**B, G**  $\delta : lower(bag, ticket(k))$





# Our Contributions

## A Deductive Verification Techniques for Parametrized Systems

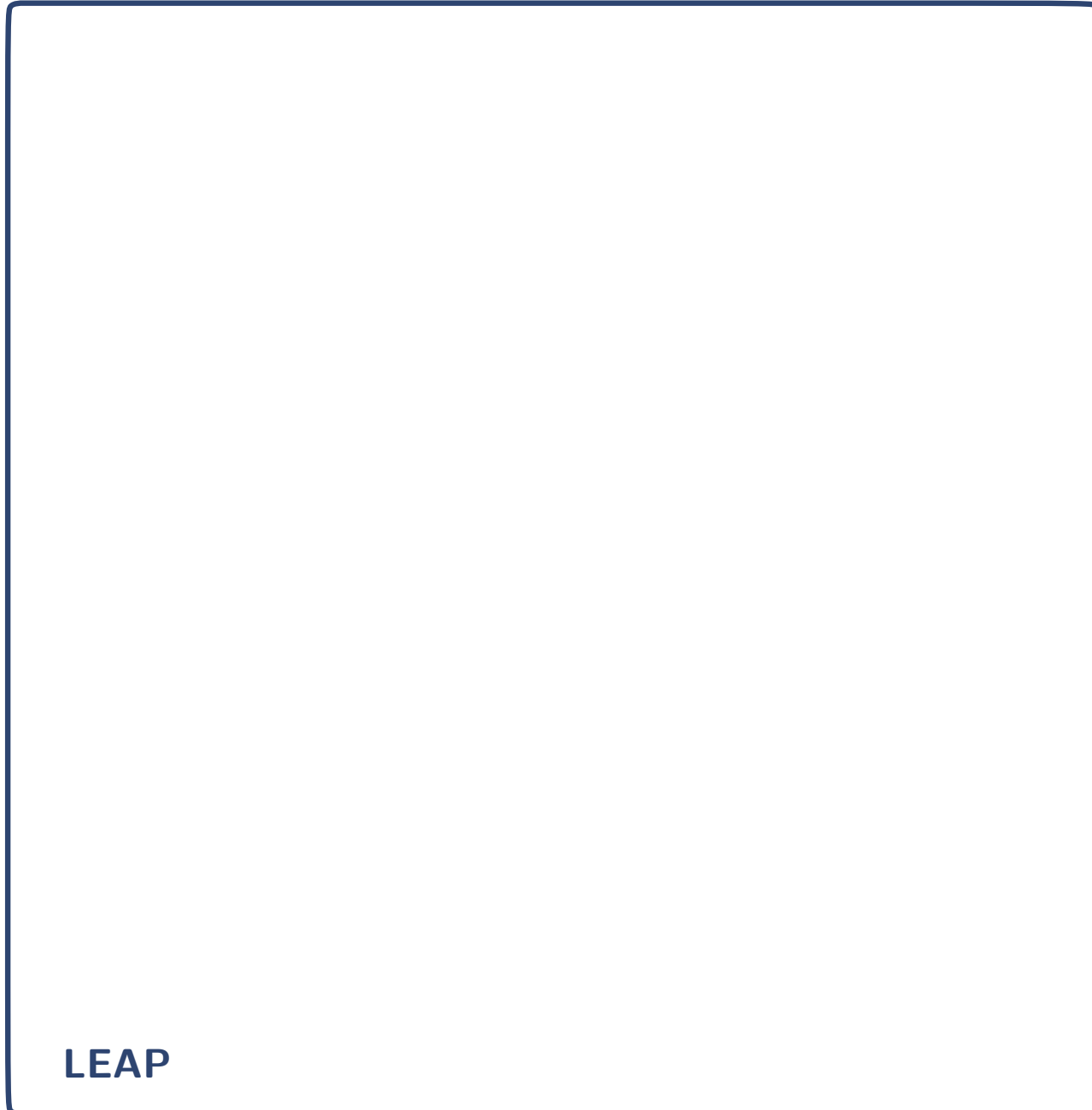
- 1 **Parametrized Invariance**  
Deductive proof rules for concurrent parametrized invariants
- 2 **Parametrized Verification Diagrams**  
Diagram based verification for concurrent parametrized liveness properties
- 3 **Invariant Generation using Self-reflection**  
Automatic parametrized invariant generation using off-the-shelf sequential absint

## B Decision Procedures for Complex Data Structures

- 4 **TL3: A Decidable Theory for Concurrent Lists**  
A Theory and decision procedure for concurrent data structures of the shape of a list
- 5 **TSL<sub>K</sub>: A Decidable Family for Concurrent Bounded Skiplists**  
Theories and decision procedures for concurrent skiplists of at most K levels
- 6 **TSL: A Decidable Theory for Skiplists with Arbitrary Levels**  
Theory and decision procedure for skiplists with unbounded many levels


## C Implementation and Evaluation of our Framework

# LEAP: Structure



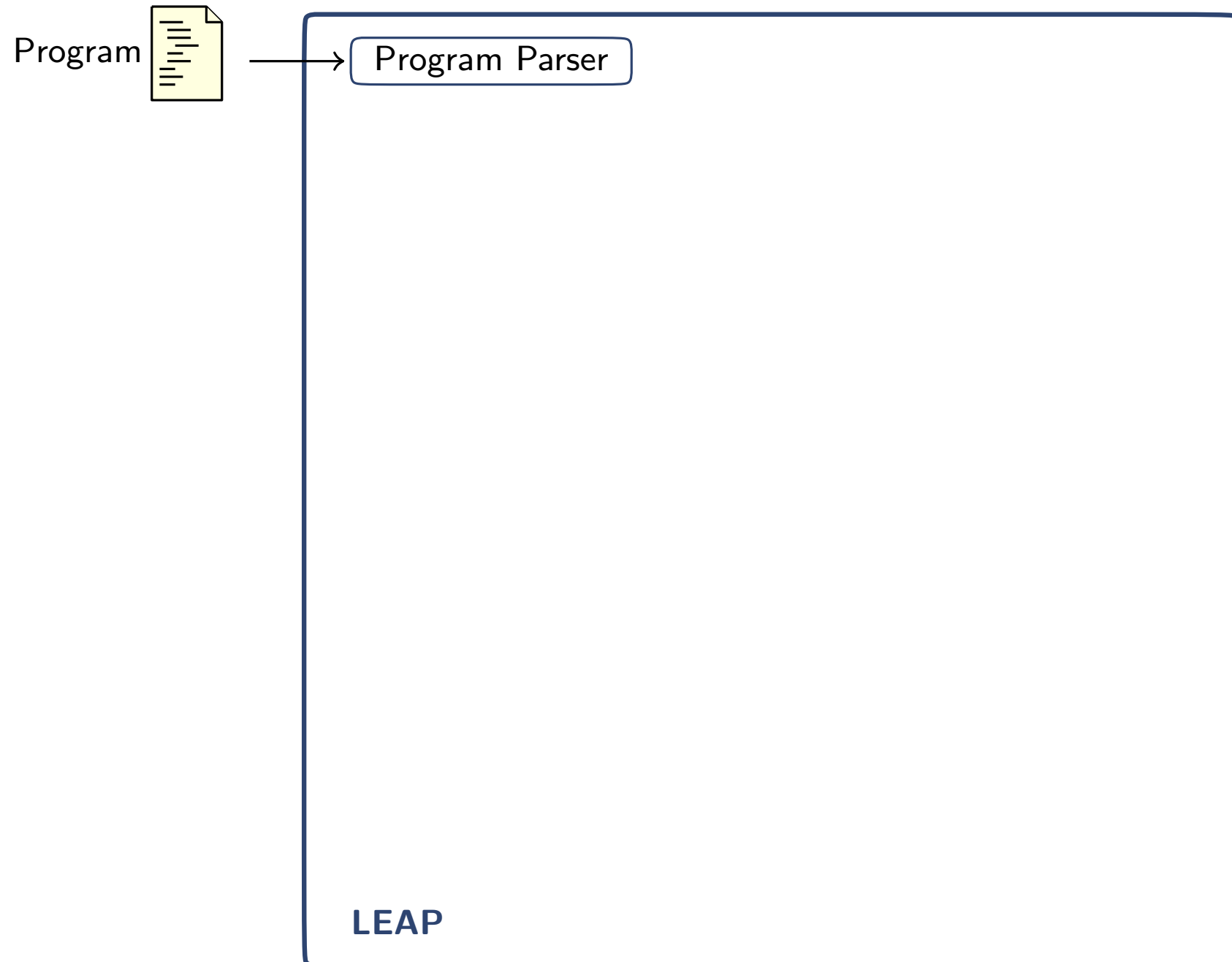


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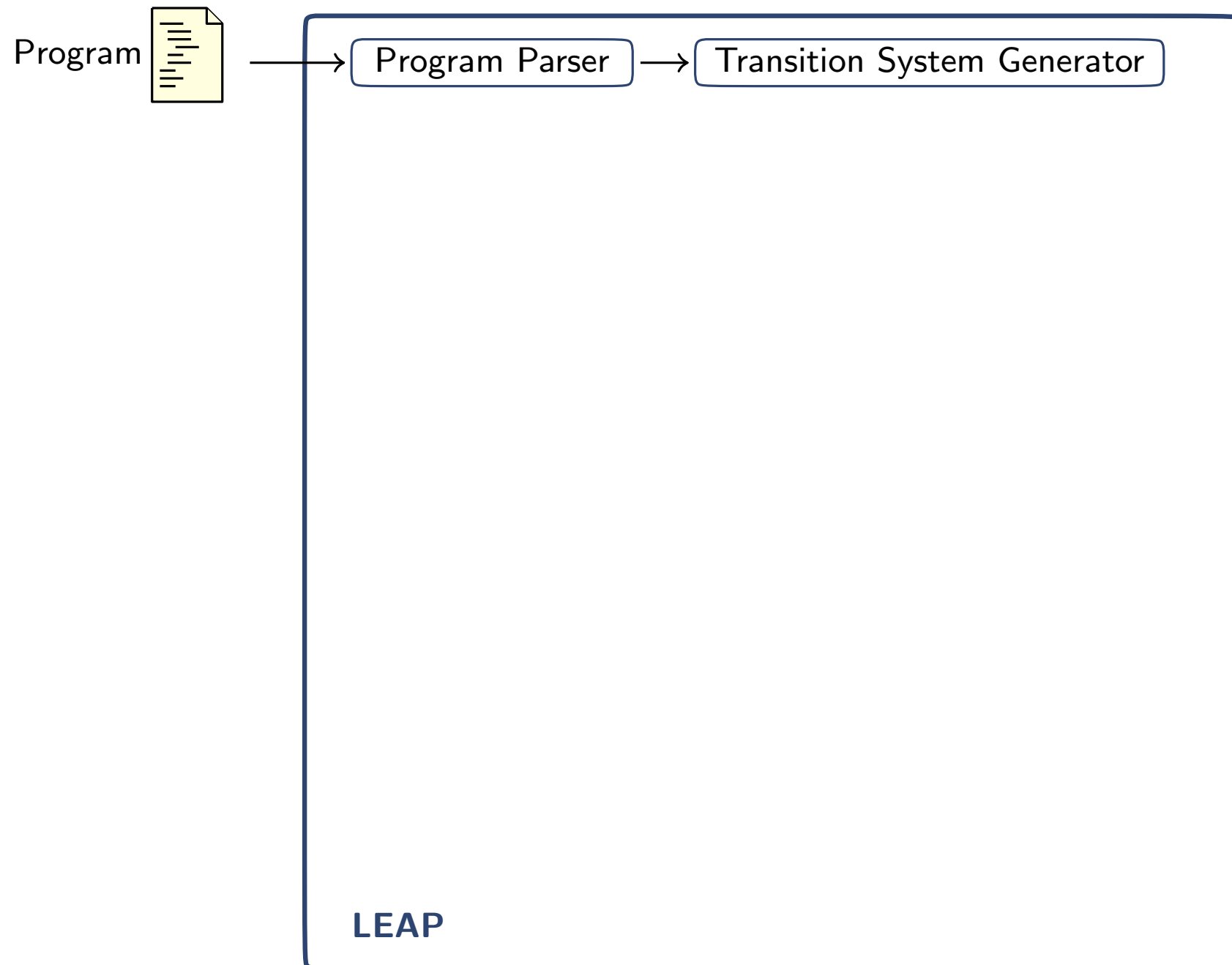
Program 

LEAP

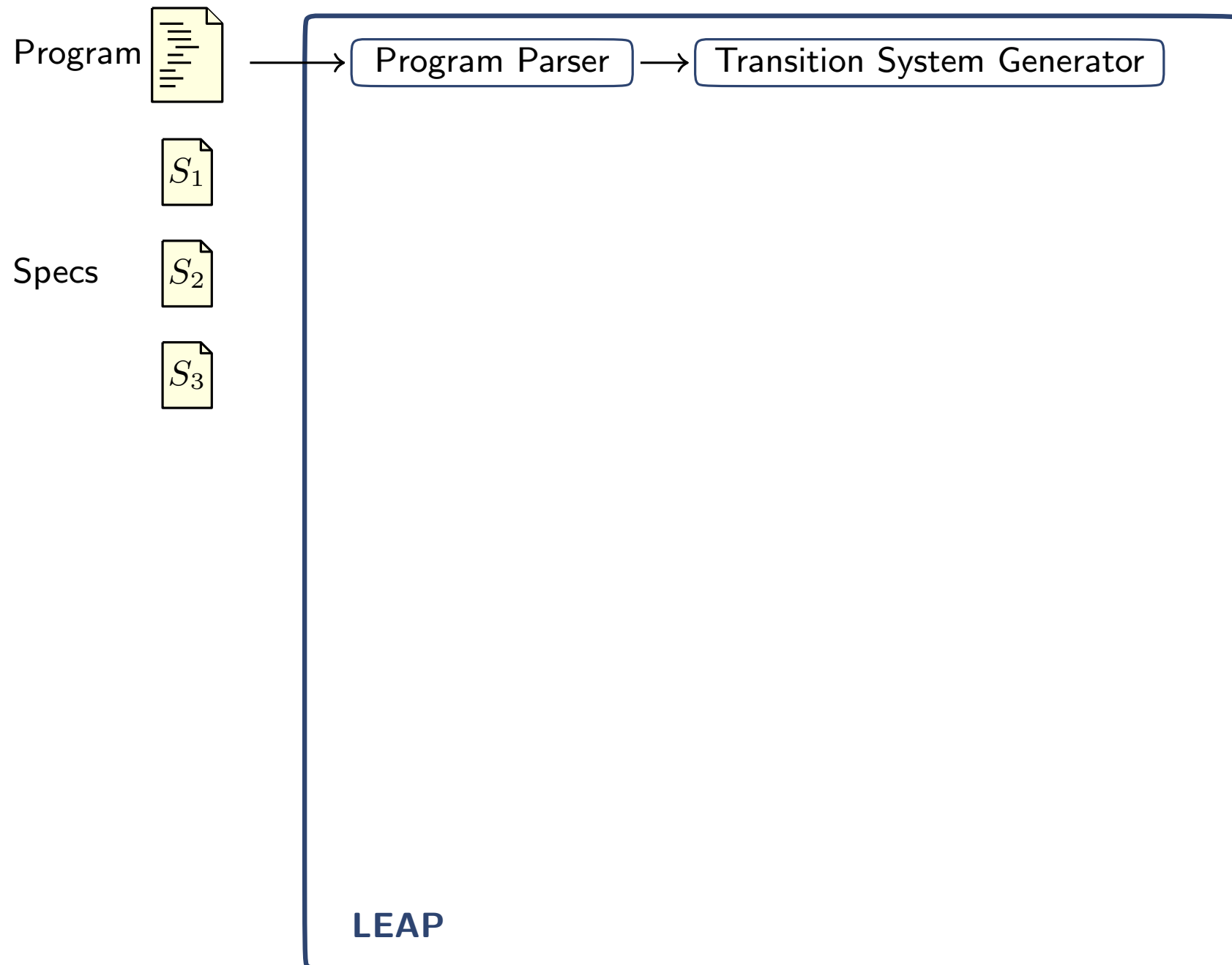
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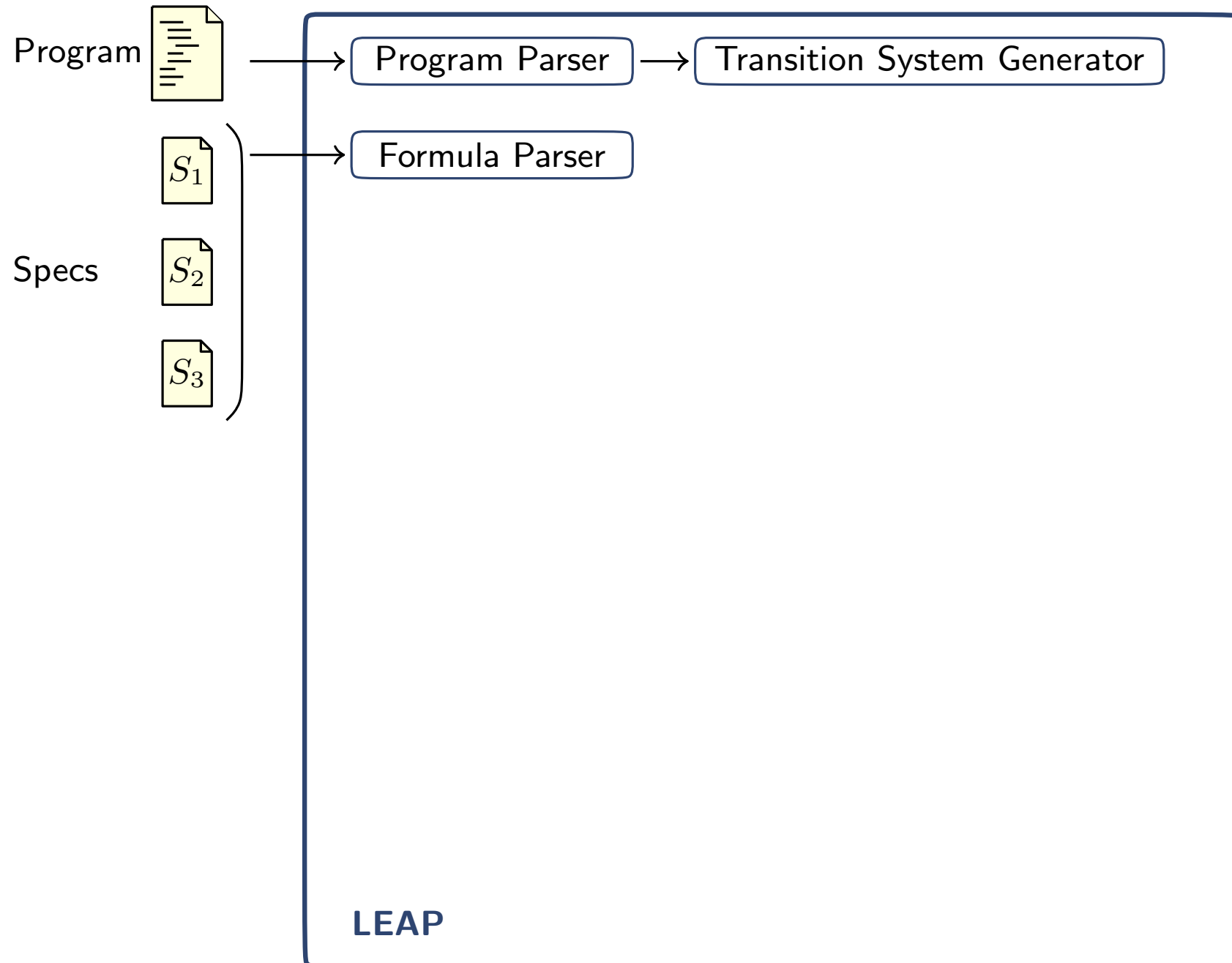
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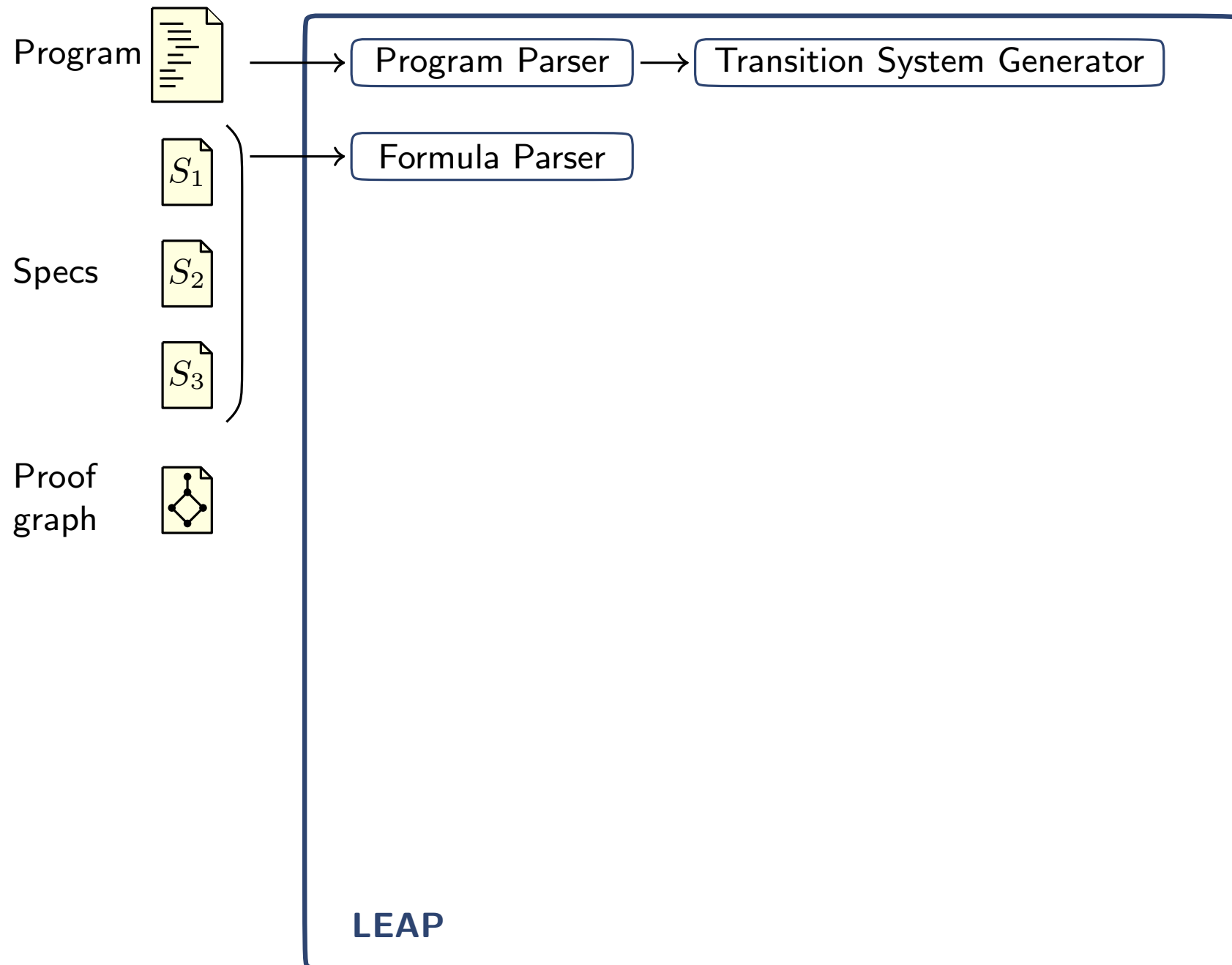
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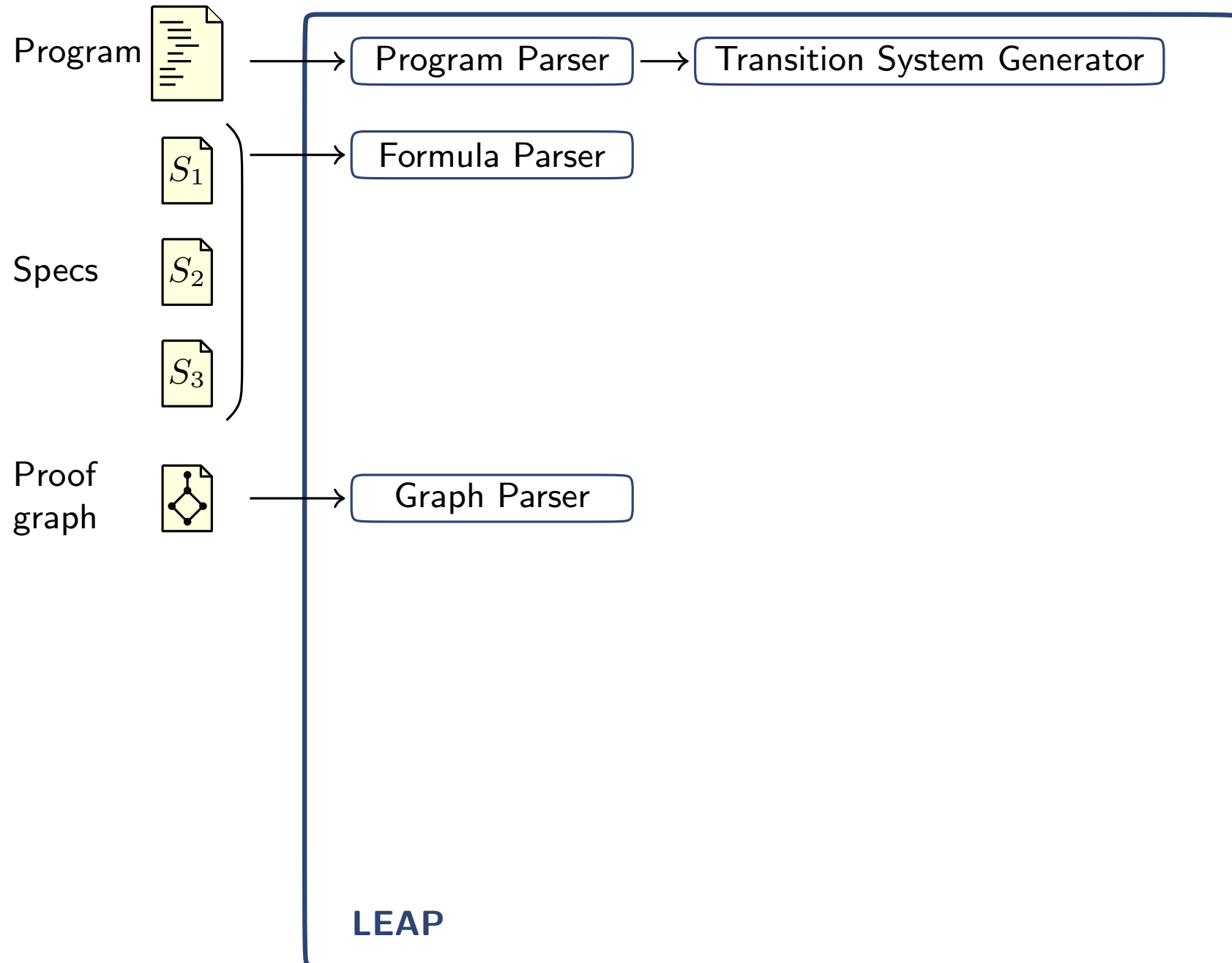
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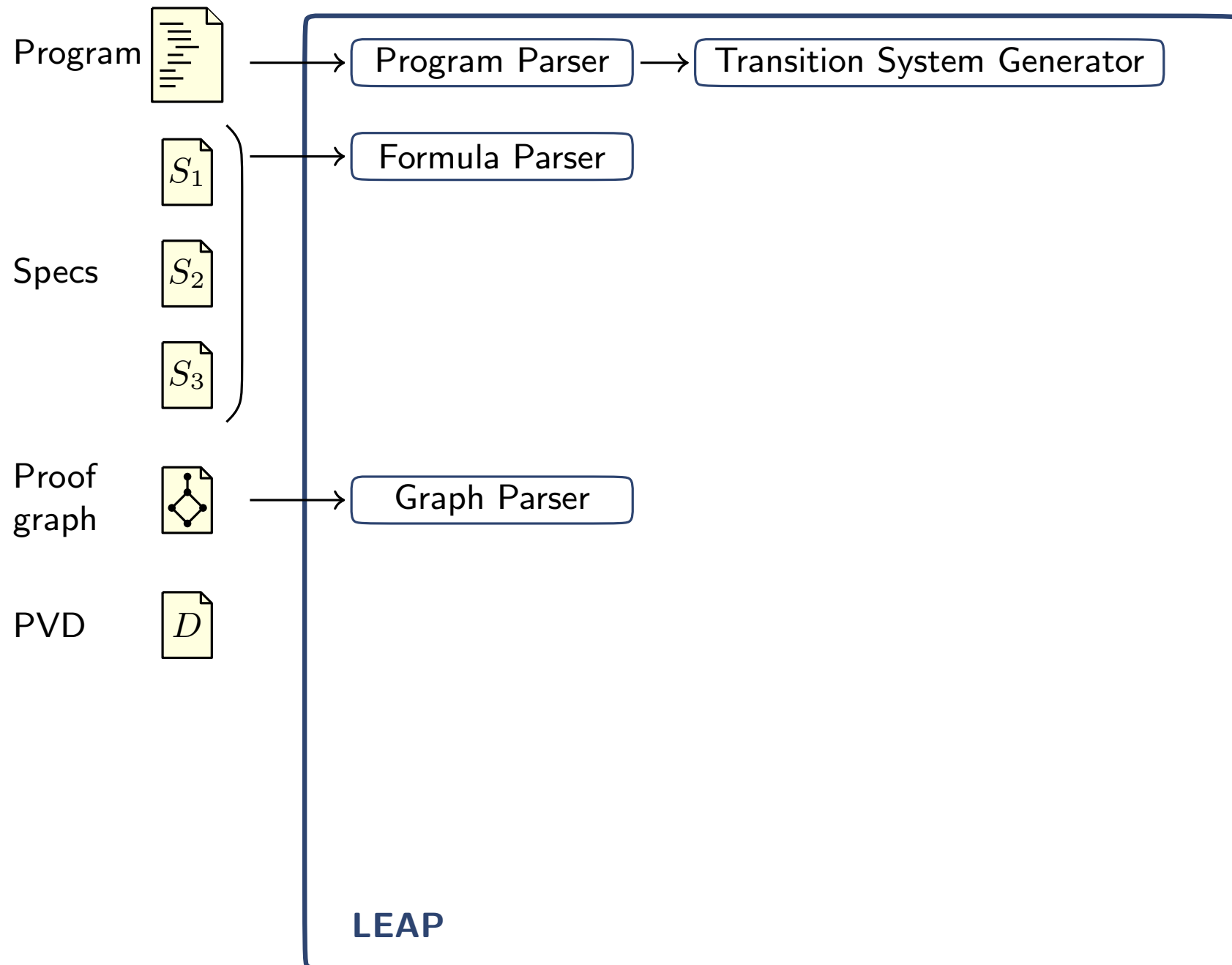
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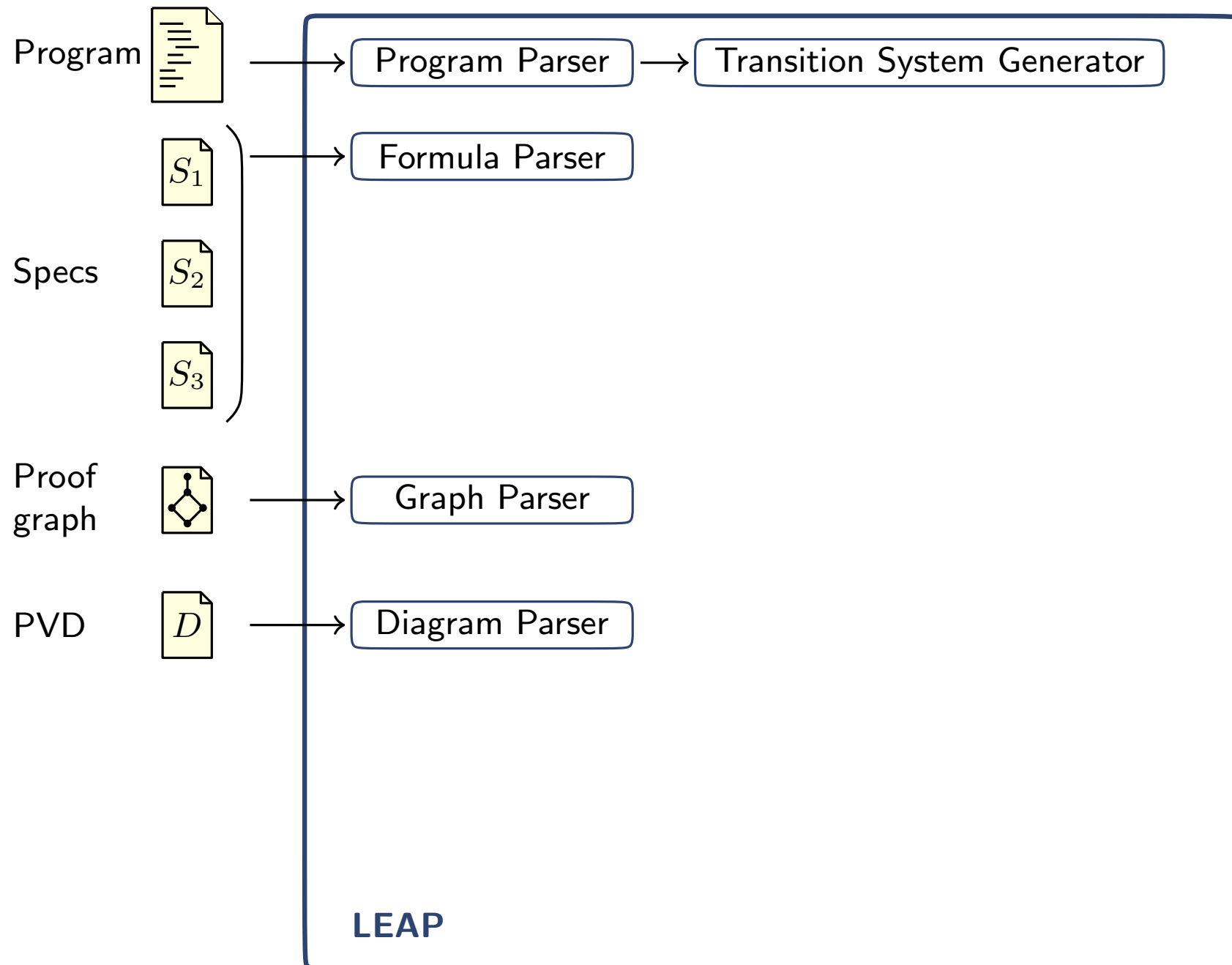


# LEAP: Structure

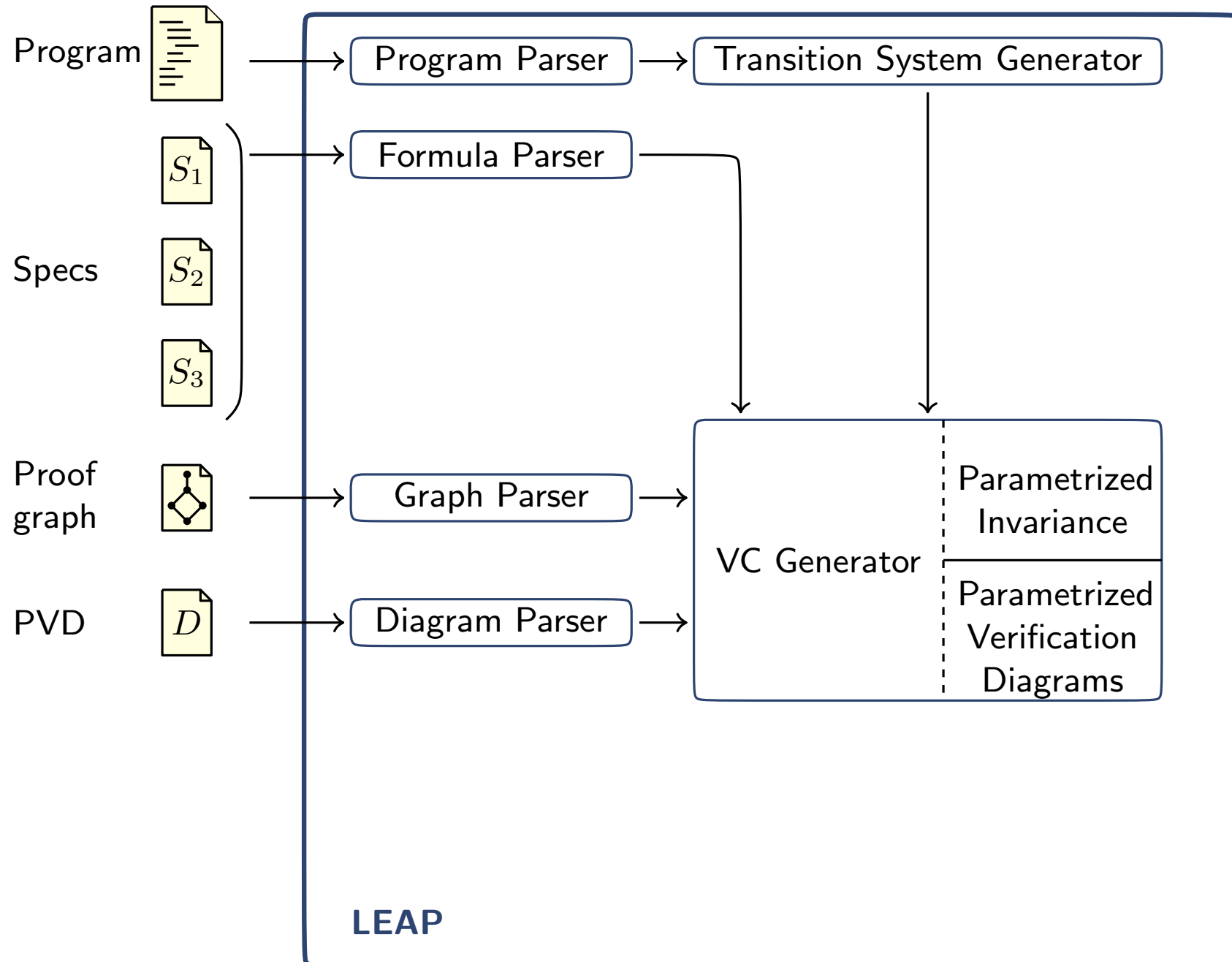




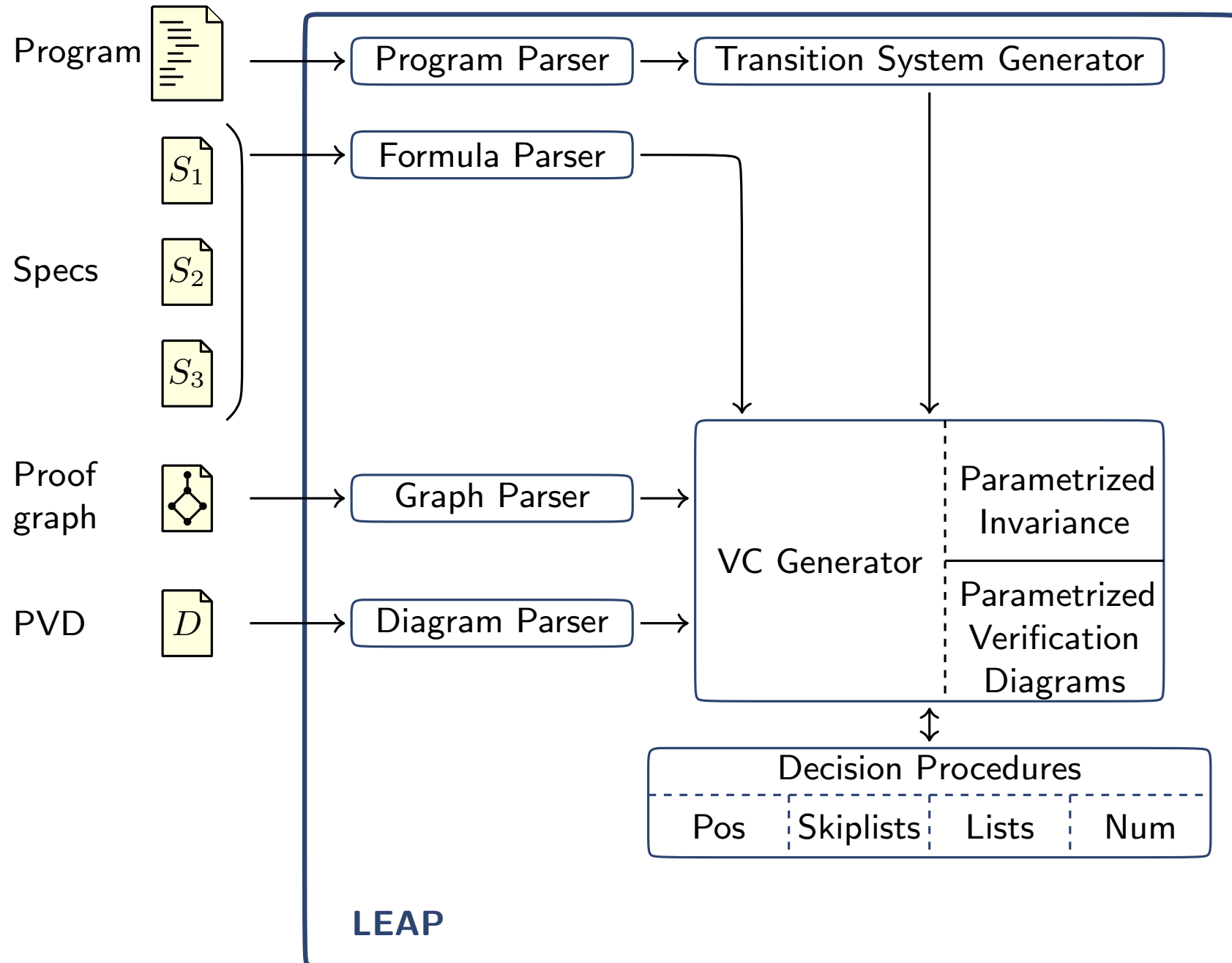
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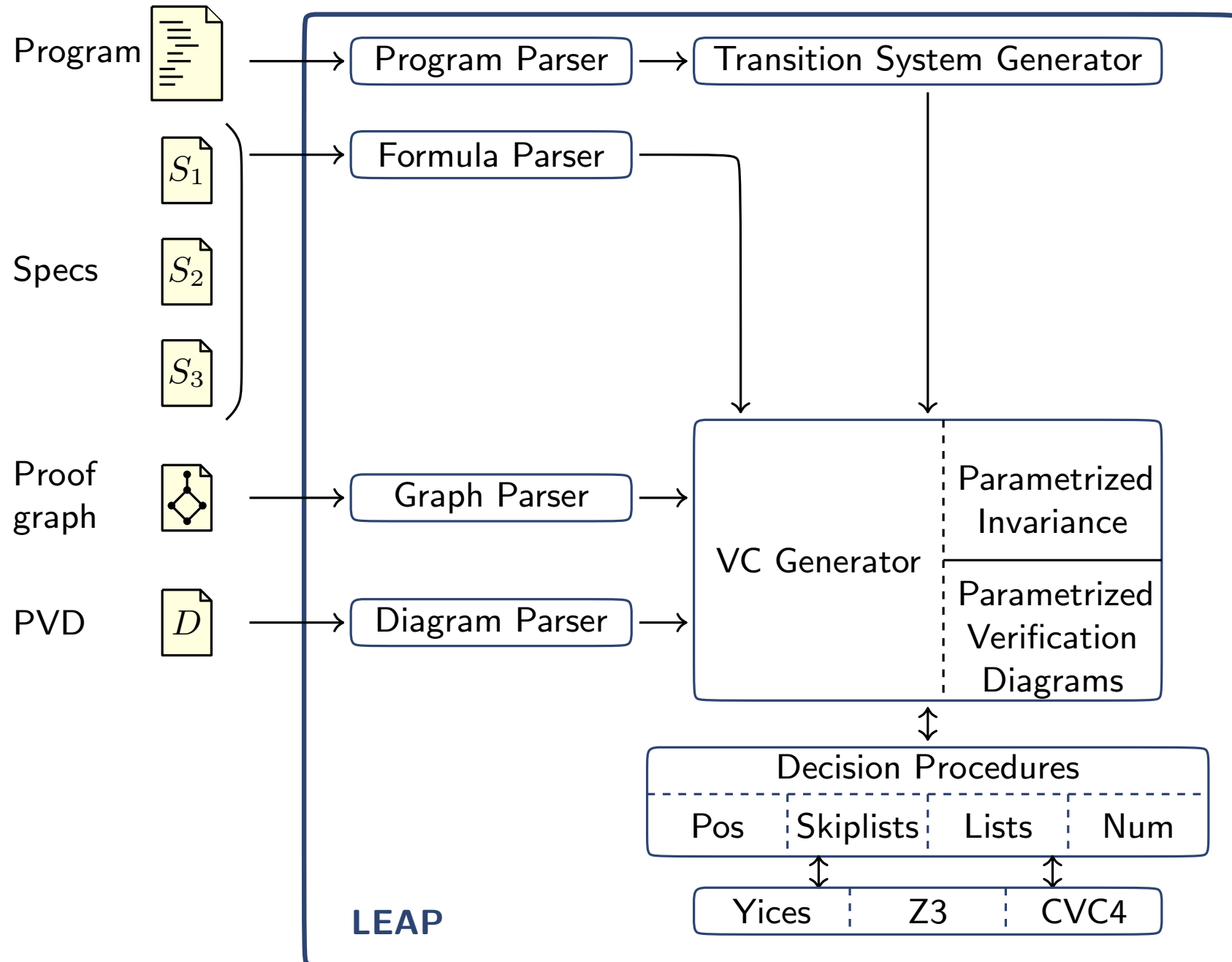
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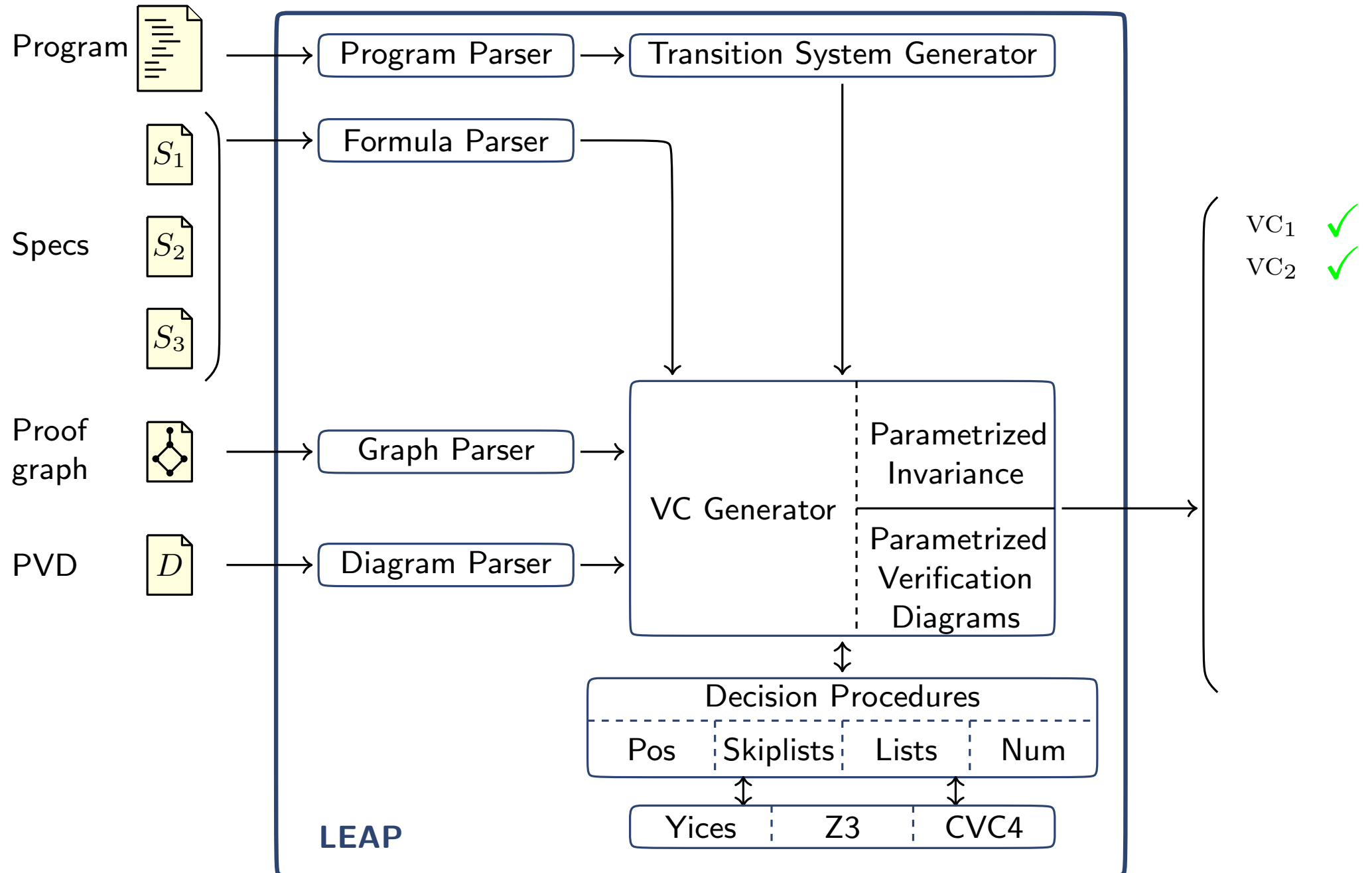
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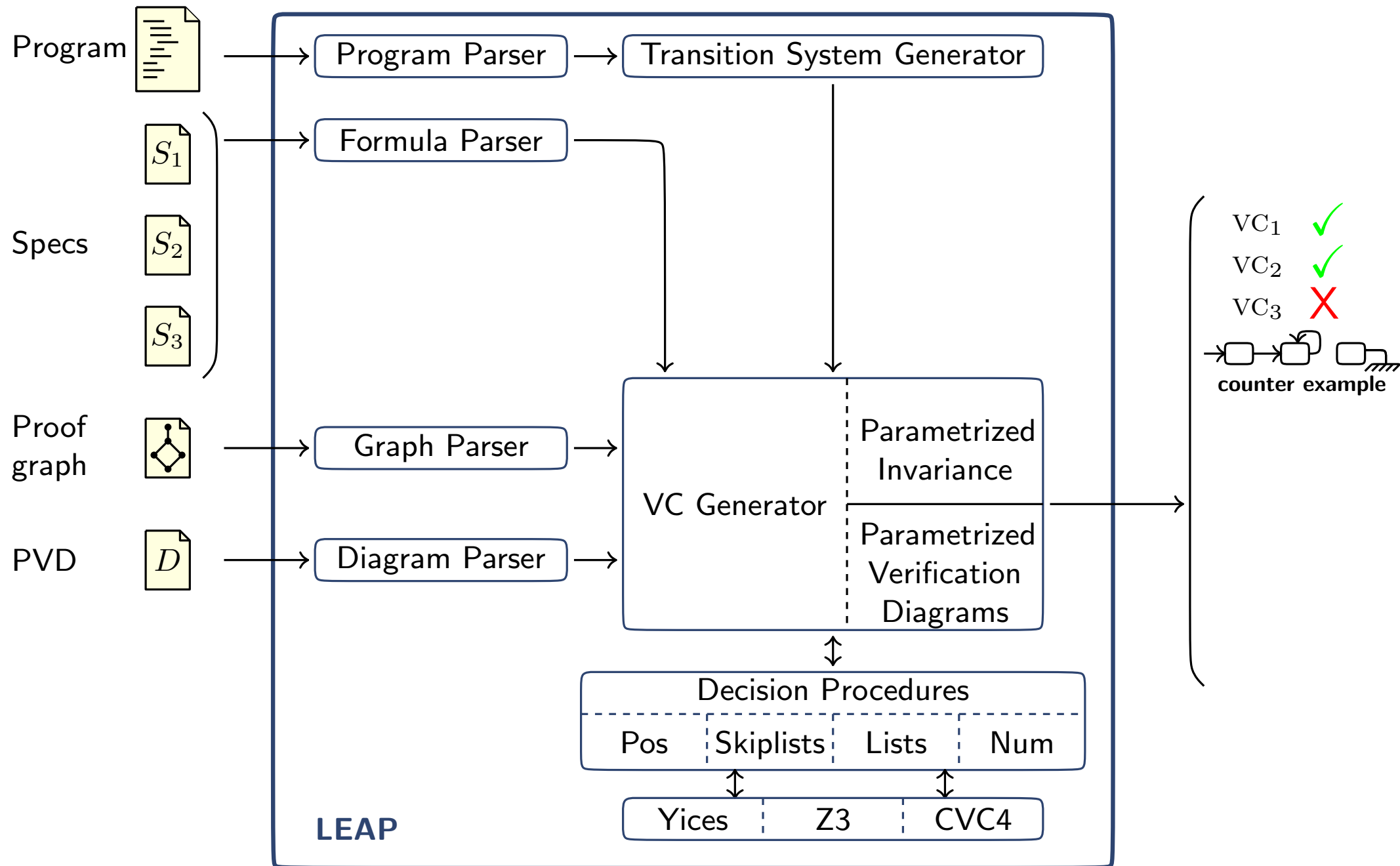
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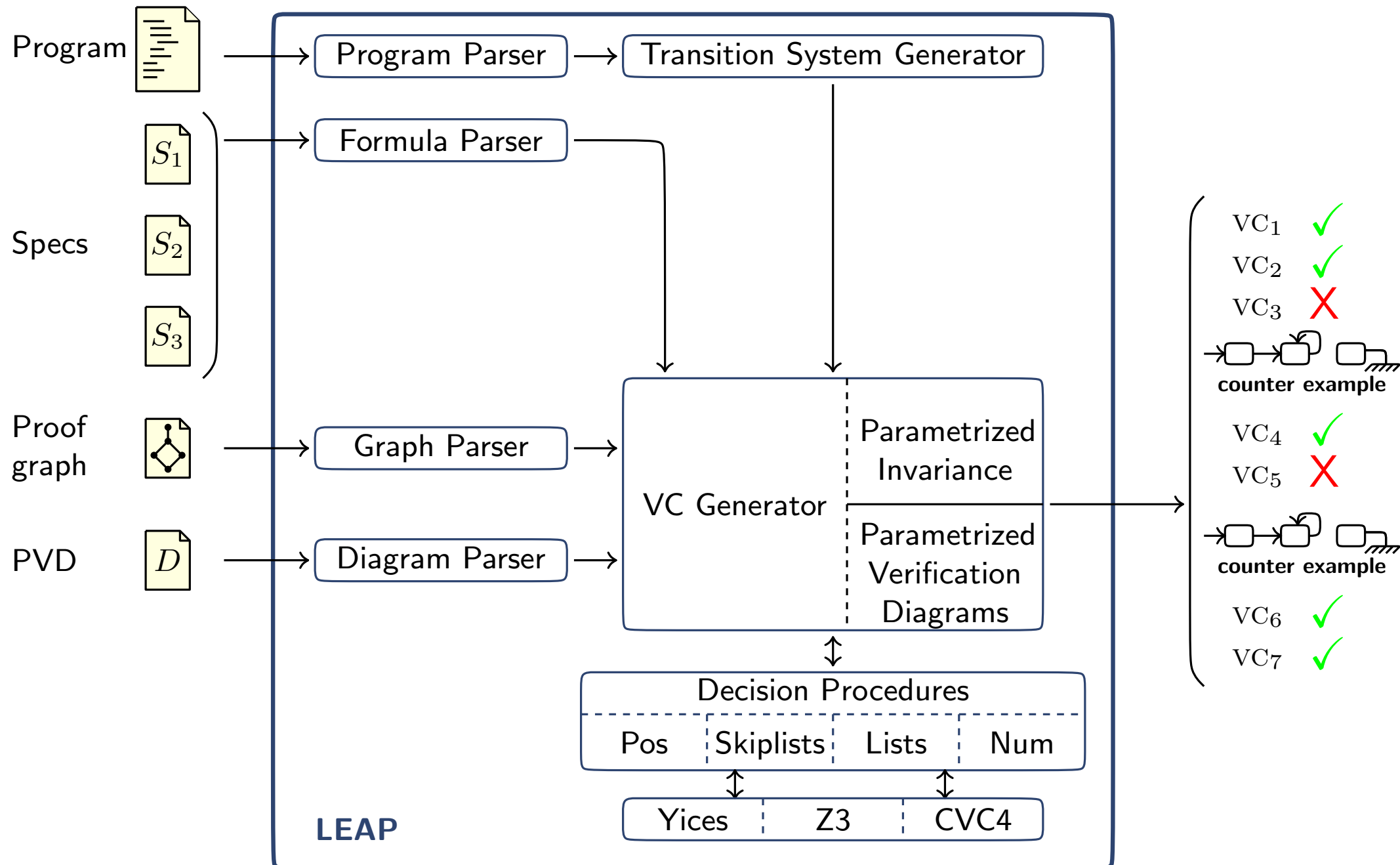
# LEAP: Structure



# LEAP: Structure



# LEAP: Structure



# Verification using LEAP

We use **LEAP** to verify temporal, structural and functional properties

- ▶ Mutual exclusion protocols
- ▶ Concurrent lock-coupling lists
- ▶ Concurrent lock-based queues
- ▶ Lock-free stacks
- ▶ Lock-free queues
- ▶ Skiplists with 2,3,4,5,.. levels
- ▶ Skiplists with unbounded levels (including KDE implementation)

**LEAP** and examples **available online** at  
`software.imdea.org/leap`



# LEAP: Some Experimental Results (safety)

	formula	#solved vc		Brute time(s.)	Heurist. time(s.)	DP time(s.)		LEAP time(s.)		
		idx	#vc			pos	dp		slowest	average
1	list	0	61	38	23	$\infty$	18.67	11.90	0.30	0.20
2	order	1	121	62	59	998.35	1.12	0.03	0.01	0.47
3	lock	1	121	76	45	778.15	0.47	0.02	0.01	0.18
4	next	1	121	60	61	$\infty$	2.11	0.61	0.01	0.59
5	region	1	121	95	26	$\infty$	22.58	18.17	0.18	0.23
6	disj	2	181	177	4	121.74	0.19	0.01	0.01	0.12
7	funSchLin	1	121	97	24	$\infty$	6.29	3.04	0.05	0.08
8	funSchIns	1	121	93	28	$\infty$	4.15	1.91	0.03	0.08
9	funSchRem	1	121	93	28	$\infty$	5.40	2.60	0.04	0.10
10	funSearch	1	208	198	10	$\infty$	3.54	1.57	0.01	0.34
11	funInsert	1	208	200	8	$\infty$	0.50	0.01	0.01	0.22
12	funRemove	1	208	200	8	$\infty$	1.41	0.95	0.01	0.24
13	skiplist <sub>3</sub>	0	154	92	62	$\infty$	1221.97	776.45	15.27	0.45
14	region <sub>3</sub>	0	124	97	27	$\infty$	27.50	17.36	0.34	0.58
15	next <sub>3</sub>	0	84	65	19	$\infty$	0.67	0.09	0.01	0.20
16	order <sub>3</sub>	0	84	59	25	$\infty$	9.66	7.80	0.10	1.31
17	skiplist	0	560	532	28	$\infty$	19.79	5.40	0.24	0.15
18	region	0	1583	1527	56	$\infty$	44.28	22.66	0.54	1.35
19	next	0	1899	1869	30	$\infty$	3.19	0.32	0.02	1.59
20	order	0	2531	2474	57	$\infty$	11.19	2.35	0.84	6.75
21	mutex	2	28	26	2	0.32	0.01	0.01	0.01	0.01
22	minticket	1	19	18	1	0.04	0.01	0.01	0.01	0.01
23	notsame	2	28	26	2	0.13	0.03	0.01	0.01	0.01
24	mutexS	2	28	26	2	0.44	0.04	0.01	0.01	0.01
25	minticketS	1	19	18	1	0.31	0.01	0.01	0.01	0.01
26	notsameS	2	28	26	2	0.14	0.02	0.01	0.01	0.01

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5	region	1	121	95	26	$\infty$	22.58	18.17	0.18	0.23
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11	funInsert	1	208	200	8	$\infty$	0.50	0.01	0.01	0.22
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13	skiplist <sub>3</sub>	0	154	92	62	$\infty$	1221.97	776.45	15.27	0.45
14	region <sub>3</sub>	0	124	97	27	$\infty$	27.50	17.36	0.34	0.58
15	next <sub>3</sub>	0	84	65	19	$\infty$	0.67	0.09	0.01	0.20
16	order <sub>3</sub>	0	84	59	25	$\infty$	9.66	7.80	0.10	1.31
17	skiplist	0	560	532	28	$\infty$	19.79	5.40	0.24	0.15
18	region	0	1583	1527	56	$\infty$	44.28	22.66	0.54	1.35
19	next	0	1899	1869	30	$\infty$	3.19	0.32	0.02	1.59
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23	notsame	2	28	26	2	0.13	0.03	0.01	0.01	0.01
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4	next	1	121	60	61	$\infty$	2.11	0.61	0.01	0.59
5	region	1	121	95	26	$\infty$	22.58	18.17	0.18	0.23
6	disj	2	181	177	4	121.74	0.19	0.01	0.01	0.12
7	funSchLin	1	121	97	24	$\infty$	6.29	3.04	0.05	0.08
8	funSchIns	1	121	93	28	$\infty$	4.15	1.91	0.03	0.08
9	funSchRem	1	121	93	28	$\infty$	5.40	2.60	0.04	0.10
10	funSearch	1	208	198	10	$\infty$	3.54	1.57	0.01	0.34
11	funInsert	1	208	200	8	$\infty$	0.50	0.01	0.01	0.22
12	funRemove	1	208	200	8	$\infty$	1.41	0.95	0.01	0.24
13	skiplist <sub>3</sub>	0	154	92	62	$\infty$	1221.97	776.45	15.27	0.45
14	region <sub>3</sub>	0	124	97	27	$\infty$	27.50	17.36	0.34	0.58
15	next <sub>3</sub>	0	84	65	19	$\infty$	0.67	0.09	0.01	0.20
16	order <sub>3</sub>	0	84	59	25	$\infty$	9.66	7.80	0.10	1.31
17	skiplist	0	560	532	28	$\infty$	19.79	5.40	0.24	0.15
18	region	0	1583	1527	56	$\infty$	44.28	22.66	0.54	1.35
19	next	0	1899	1869	30	$\infty$	3.19	0.32	0.02	1.59
20	order	0	2531	2474	57	$\infty$	11.19	2.35	0.84	6.75
21	mutex	2	28	26	2	0.32	0.01	0.01	0.01	0.01
22	minticket	1	19	18	1	0.04	0.01	0.01	0.01	0.01
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# LEAP: Some Experimental Results (safety)

	formula	#solved vc		Brute time(s.)	Heurist. time(s.)	DP time(s.)		LEAP time(s.)		
		idx	#vc			pos	dp		slowest	average
1	list	0	61	38	23	$\infty$	18.67	11.90	0.30	0.20
2	order	1	121	62	59	998.35	1.12	0.03	0.01	0.47
3	lock	1	121	76	45	778.15	0.47	0.02	0.01	0.18
4	next	1	121	60	61	$\infty$	2.11	0.61	0.01	0.59
5	region	1	121	95	26	$\infty$	22.58	18.17	0.18	0.23
6	disj	2	181	177	4	121.74	0.19	0.01	0.01	0.12
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19	next	0	1899	1869	30	$\infty$	3.19	0.32	0.02	1.59
20	order	0	2531	2474	57	$\infty$	11.19	2.35	0.84	6.75
21	mutex	2	28	26	2	0.32	0.01	0.01	0.01	0.01
22	minticket	1	19	18	1	0.04	0.01	0.01	0.01	0.01
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20	order	0	2531	2474	57	$\infty$	11.18	8.85	0.84	6.75
21	mutex	2	28	26	2	0.32	0.05	0.01	0.01	0.01
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**LEAP analysis time remains insignificant**

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4	next	1	121	60	61	$\infty$	2.11	0.61	0.01	0.59
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23	notsame	2	28	26	2	0.13	0.03	0.01	0.01	0.01
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25	minticketS	1	19	18	1	0.31	0.01	0.01	0.01	0.01
26	notsameS	2	28	26	2	0.14	0.02	0.01	0.01	0.01

**Decision procedures perform well...  
but still room for improvements**

# LEAP: Some Experimental Results (liveness)

MinTicket (progress):

	#VC	#solved VC		single VC time(s.)		DP time(s)	LEAP time(s)
		pos	num	slowest	average		
Initiation	1	0	1	0.01	0.01	0.01	0.01
Consecution	153	144	9	2.66	0.03	4.22	0.06
Acceptance	195	132	63	1.46	0.08	15.28	0.05
Fairness	24	20	4	0.03	0.01	0.10	0.02

Concurrent List (termination):

	#VC	#solved VC		single VC time(s.)		DP time(s)	LEAP time(s)
		pos	TLL	slowest	average		
Initiation	1	0	1	0.01	0.01	0.01	0.01
Consecution	1550	1343	207	3.80	0.05	78.12	3.42
Acceptance	5404	4352	1052	191.61	0.12	647.04	1.61
Fairness	48	20	28	0.42	0.16	7.82	0.14



# Published Results

## **A** Deductive Verification Techniques for Parametrized Systems

- 1** **Parametrized Invariance**  
Deductive proof rules for concurrent parametrized invariants
- 2** **Parametrized Verification Diagrams**  
Diagram based verification for concurrent parametrized liveness properties
- 3** **Invariant Generation using Abstract Interpretation**  
Automatic parametrized invariant generation using off-the-shelf sequential absint

## **B** Decision Procedures for Complex Data Structures

- 5** **TL3: Decidable Theory for Concurrent Lists**  
A Theory and decision procedure for concurrent data structures of the shape of a list
- 6** **TSL<sub>K</sub>: Decidable Theories for Concurrent Bounded Skiplists**  
Theories and decision procedures for concurrent skiplists of at most K levels
- 7** **TSL: A Decidable Theory for Skiplists with Arbitrary Levels**  
Theory and decision procedure for skiplists with unbounded many levels

## **C** Implementation and Evaluation of our Framework

# Published Results

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ACTA  
2015

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ACTA  
2015

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TIME  
2014

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CAV  
2014



# Conclusions

- ▶ A novel **deductive framework** for parametrized verification
- ▶ Designed for **concurrent data structures**
- ▶ Suitable for **safety** and **liveness** temporal properties
- ▶ Constructed **specialized theories and decision procedures**
- ▶ We studied **automatic generation** of parametrized invariants
- ▶ **Implemented a tool** with our verification framework
- ▶ We **verified various programs** and concurrent data structures

# Future Work and Open Questions

- ▶ Relax **symmetry** and study **process topologies**
- ▶ Better **invariant generation**: for more data domains
- ▶ Beyond interleaving semantics:  
    **Weak memory models** and **distributed algorithms**
- ▶ More **decision procedures** for more datatypes
- ▶ Implement a generic **Nelson-Oppen** for faster decision procedures
- ▶ Use **theorem provers** in combination with SMT solvers, and generate full formal proofs
- ▶ Add support for **real world programming languages**

# Future Work

- ▶ Relax symmetry and study **process topologies**

To show that  $\mathcal{S}$  satisfies  $\varphi(i)$ :

$$(I) \quad \Theta \rightarrow \varphi$$

$$(SC) \quad \varphi \wedge \tau^{(i)} \rightarrow \varphi' \quad \text{forall } \tau$$

$$(OC) \quad \varphi \wedge k \neq i \wedge \tau^{(k)} \rightarrow \varphi' \quad \text{forall } \tau, \text{ fresh } k$$

---

$\square \varphi$

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---

$$\square \varphi$$

$$\begin{array}{l} \text{(OC}_{<}\text{)} \quad \varphi \wedge k < i \wedge \tau^{(k)} \rightarrow \varphi' \quad \text{forall } \tau, \text{ fresh } k \\ \text{(OC}_{>}\text{)} \quad \varphi \wedge k > i \wedge \tau^{(k)} \rightarrow \varphi' \quad \text{forall } \tau, \text{ fresh } k \end{array}$$

**Thanks!**